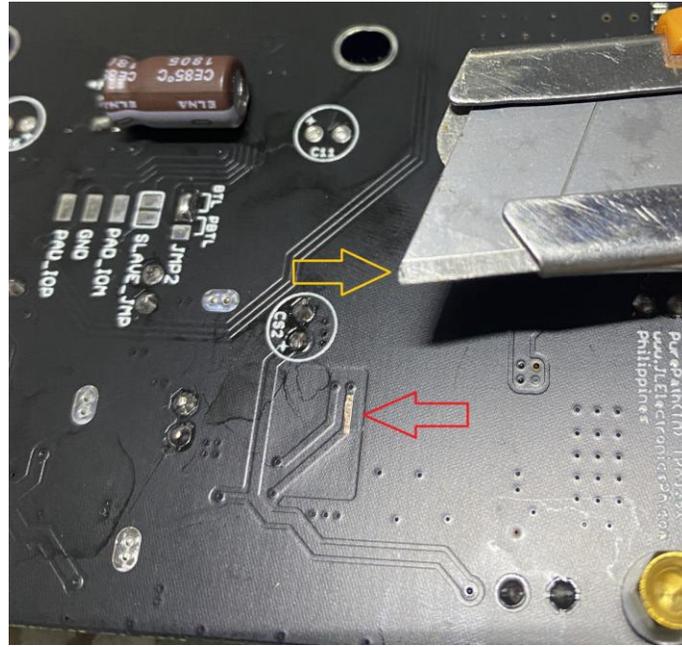


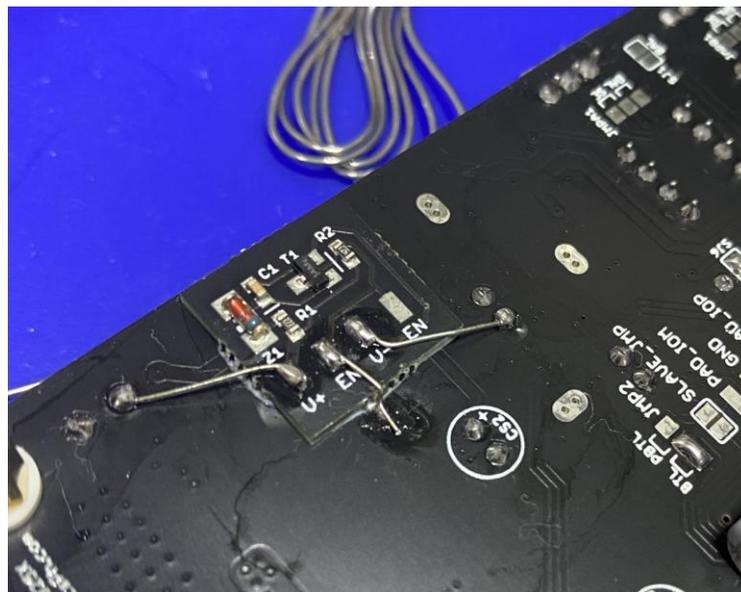
## Connecting ramp up-down daughterboard under the PCB

Using the non-sharp side of cutter tool (yellow arrow), scrape off the PCB trace shown in the picture (red arrow)

- placing CS2 on top side PCB is optional (for easier access when soldering daughtercard)



Glue the daughtercard using heatsink glue (or any glue), align the EN pad close to the scraped off PCB trace. Wait for the glue to dry before doing any solder job. Connect the traces on the main board using the pic below as a guide, we used 1/4w through hole resistor leads as a wire to connect the daughtercard to the main board trace taps. V+ connects to 12.5V, EN connects to scraped-off trace, V- connects to -6.5V.

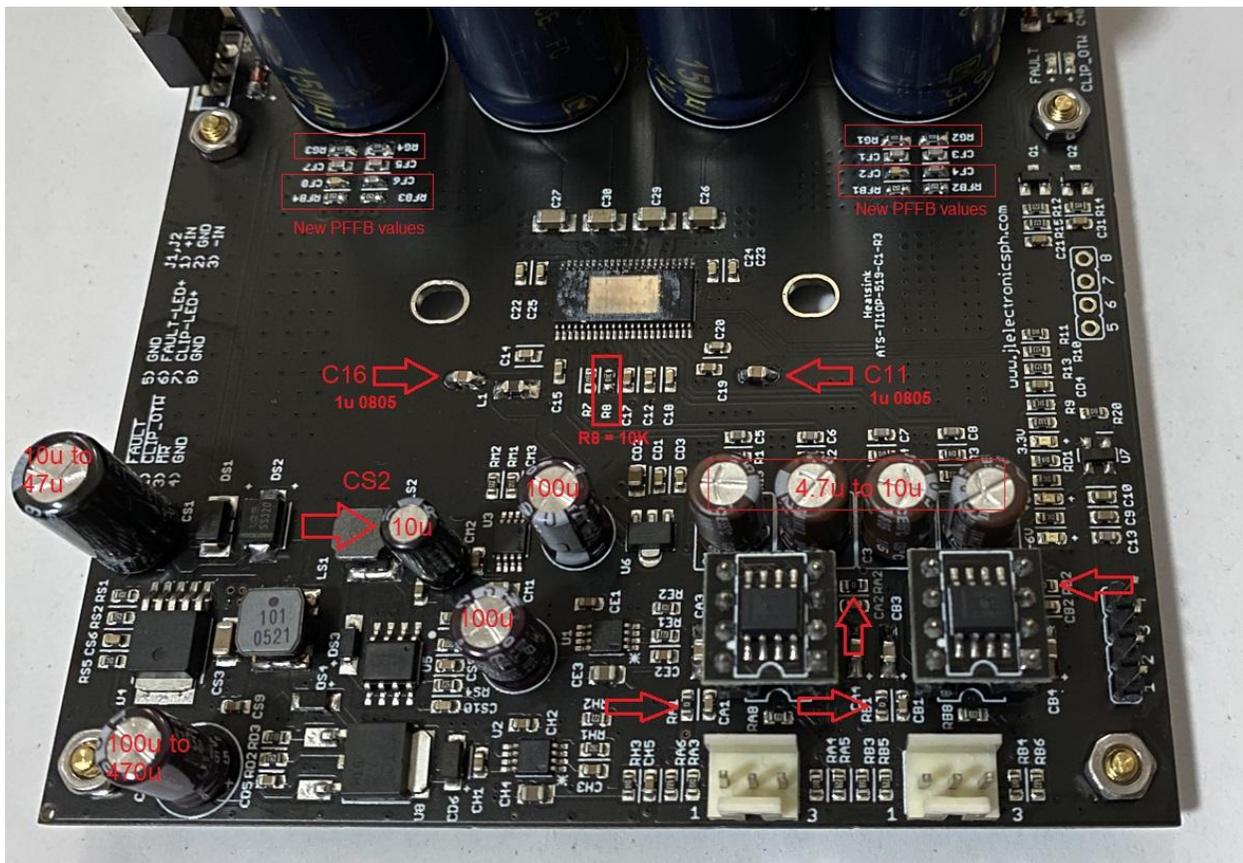


## Latest PFFB modifications (Lower THD+N and 2<sup>nd</sup> harmonic dominant) for 6.8uH output inductors only. No changes required for boards with 10uH

- < please request latest values through email >
- Change R8 to 10K for 600kHz operating freq (for 6.8uH output inductors only)

## Modifications to improve sound quality

- solder 1uF (>= 16V) 0805 ceramic on C11, C16
- use 100uF or more for AVDD under the PCB (100uF >= 16V)
- make sure RA1, RA2, RB1, RB2 is 0 ohm or shorted with solder blob



Use >= 100uF, >= 25V on AVDD cap

JLE Sylph-D200 V1P (2)

