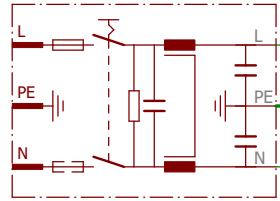
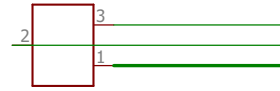


Main Power Entry
115 - 230VAC/50 - 60Hz

X201
DD22.9111.1111



X2
691412420003

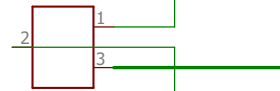


CHÂSSIS



Main Power
115 - 230VAC/50 - 60Hz

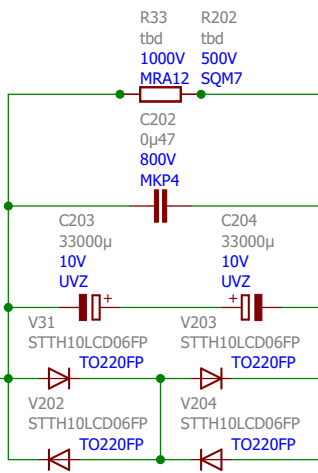
X6
691412420003



CHÂSSIS



GROUND



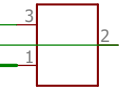
Main Power
115/230VAC OUT

X202
691412420004



Main Power
115 - 230VAC/50 - 60Hz

X1
691412420003



to microcontroller

X26-4
MICROMATCH-16



to microcontroller

X26-16

X26-15

X26-6

X26-8

X26-5

X26-7

to microcontroller

X26-10

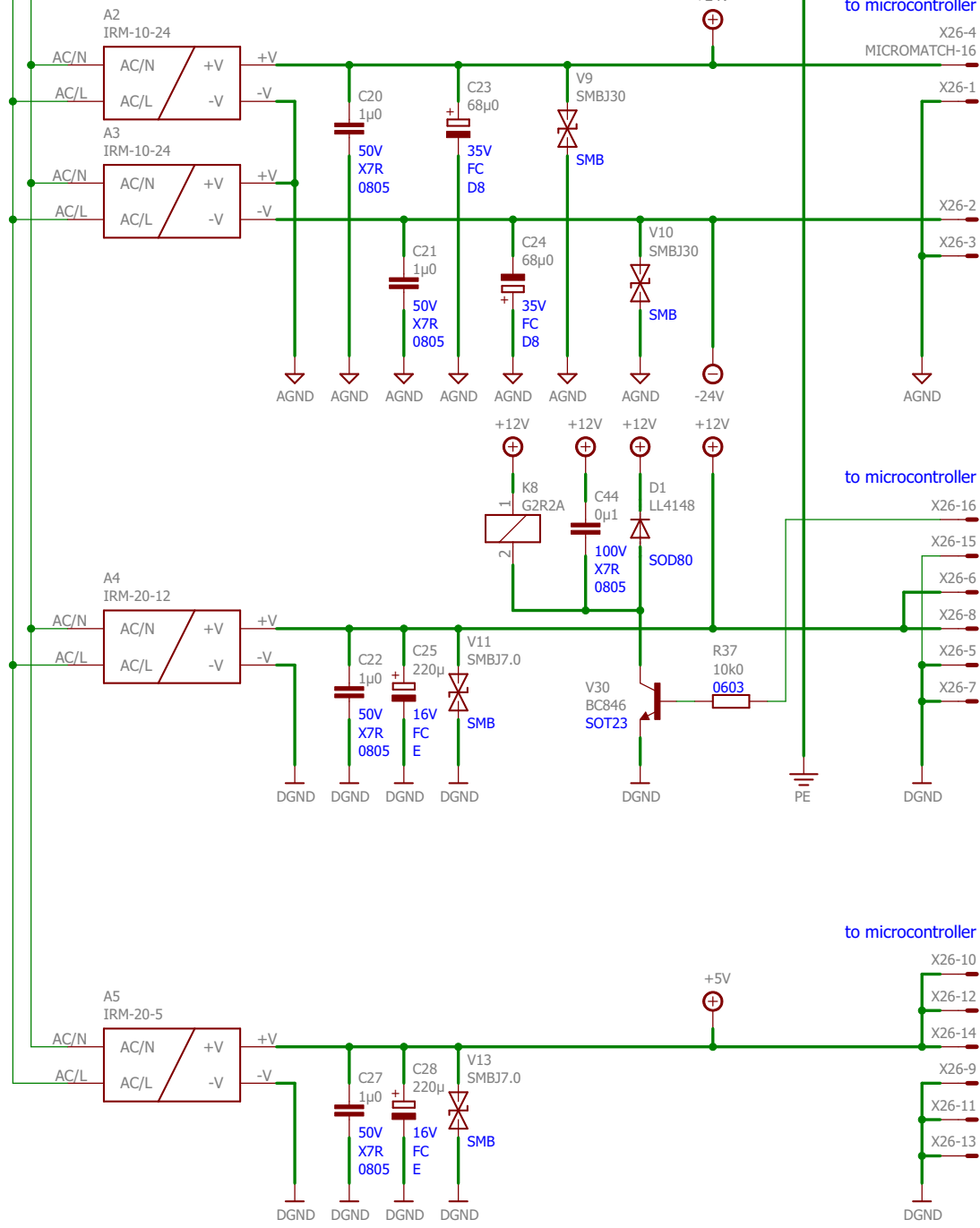
X26-12

X26-14

X26-9

X26-11

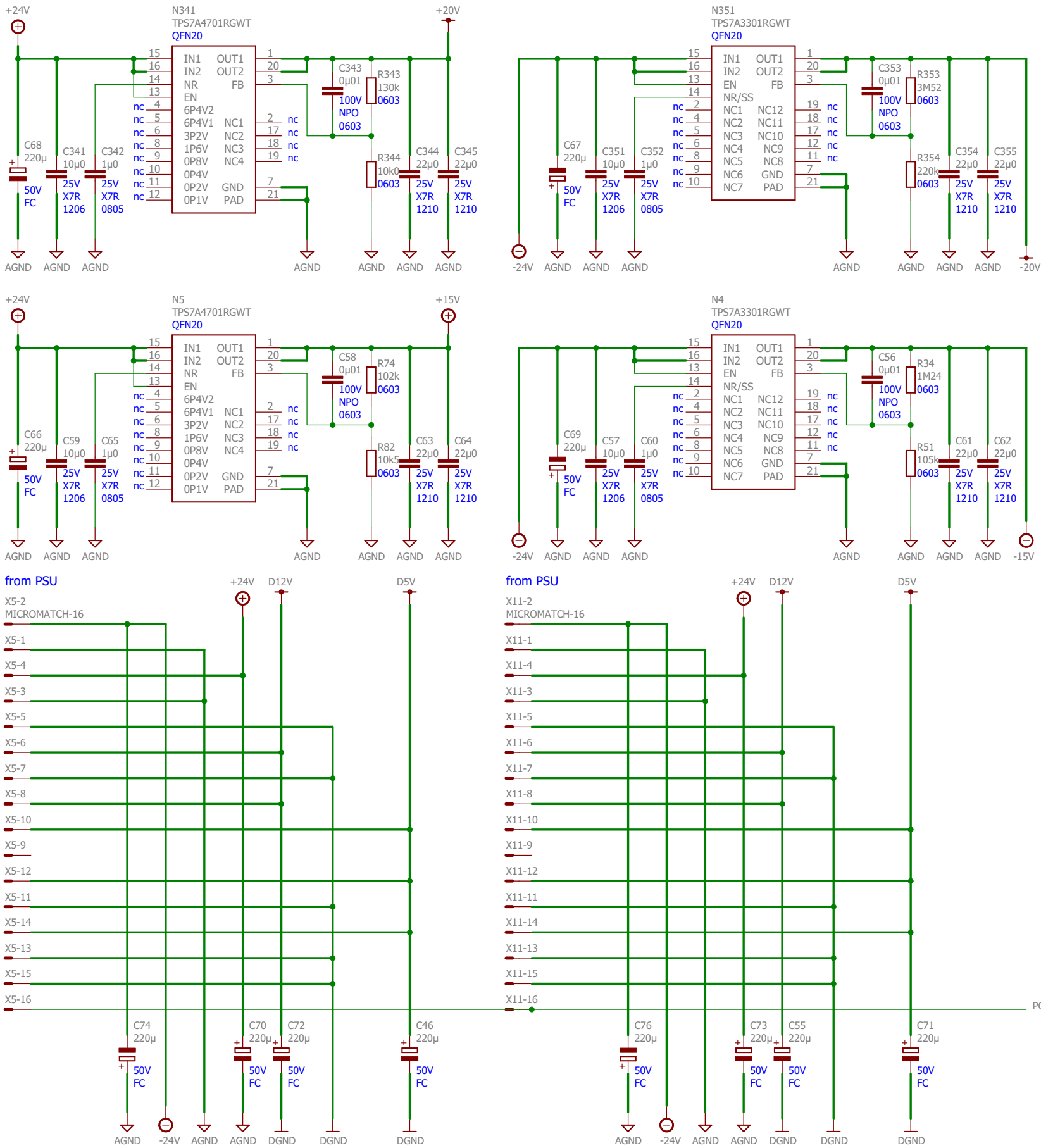
X26-13



THE INFORMATION CONTAINED IN THIS DRAWING IS THE SOLE PROPERTY OF
DIYAUDIO. ANY REPRODUCTION IN PART OR WHOLE IS
STRICTLY PROHIBITED WITHOUT WRITTEN PERMISSION.

PROPRIETARY

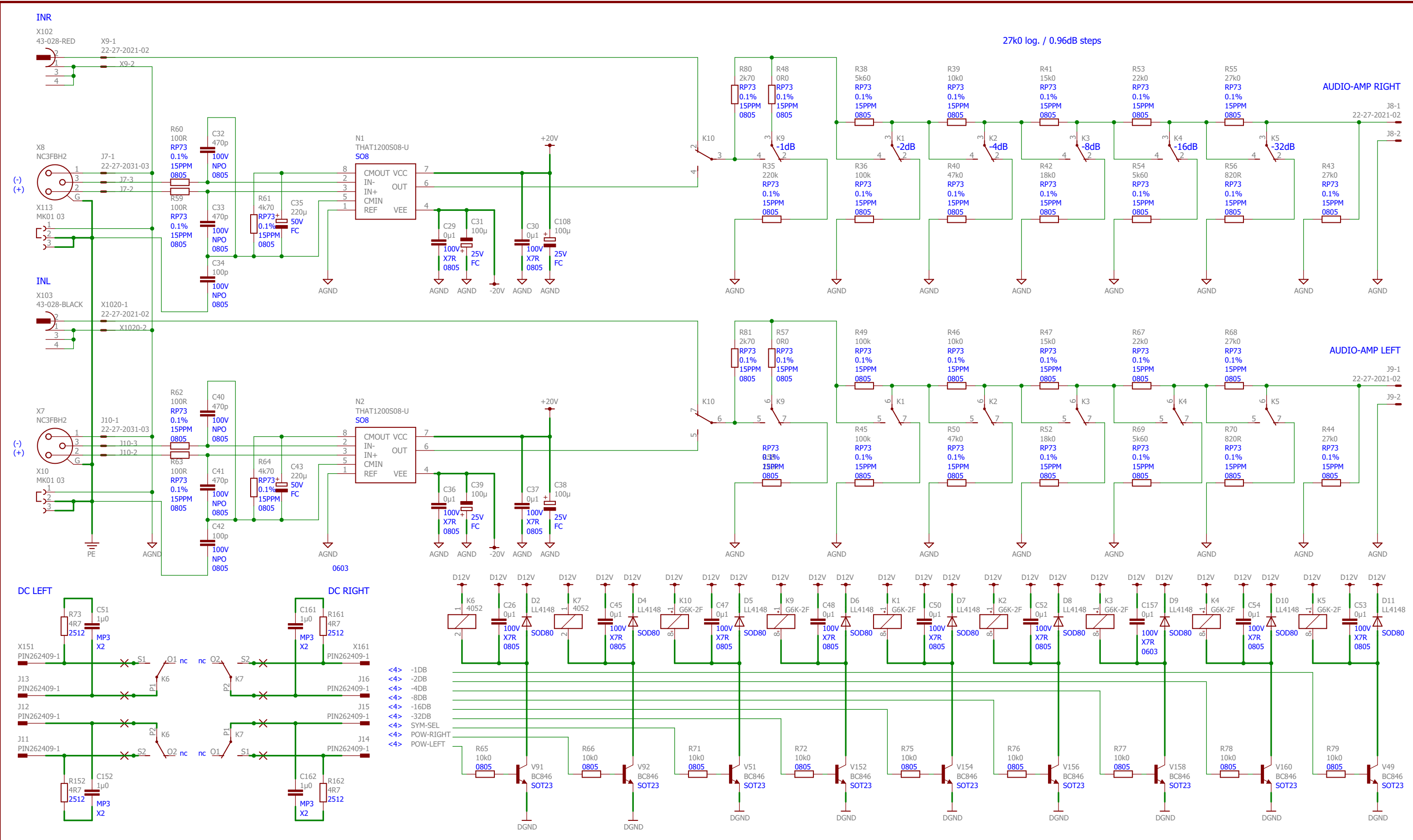
REV			DATE	CHANGE HISTORY	ISSUE	For DIYAUDIO only		THIS DRAWING CONTAINS PROPRIETARY INFORMATION AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION.	© 2017	REV
00a			05.05.2018	First iteration	DRAWN JPS64 05/05/18	TITLE UPACC Main Supplies				001
					CHECKED	DATE 05.05.2018 19:40		DRG N°		
					DATE	FILE: UPACC_00a		PAGE: 1/4		



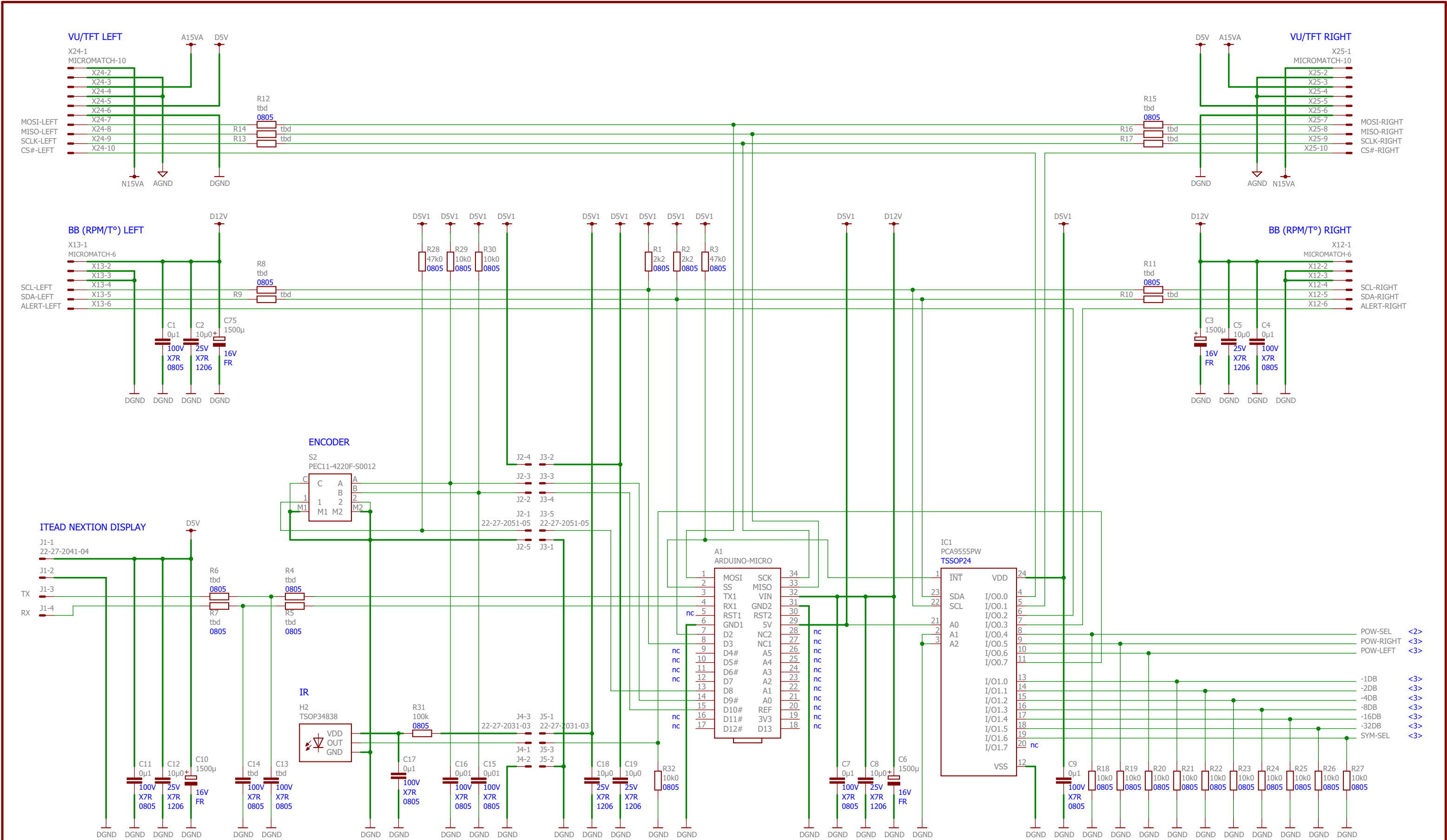
THE INFORMATION CONTAINED IN THIS DRAWING IS THE SOLE PROPERTY OF
DIYAUDIO. ANY REPRODUCTION IN PART OR WHOLE IS
STRICTLY PROHIBITED WITHOUT WRITTEN PERMISSION.

PROPRIETARY

REV			ISSUE	For DIYAUDIO only	THIS DRAWING CONTAINS PROPRIETARY INFORMATION AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION.	© 2017	REV
DATE			DRAWN	TITLE UPACC DC Supplies			001
CHANGE HISTORY			JPS64 05/05/18	DATE 05.05.2018 19:40			DRG N°
CHECKED				FILE: UPACC_00a			PAGE: 2/4
DATE			00a 05.05.2018	First iteration			



<div>THE INFORMATION CONTAINED IN THIS DRAWING IS THE SOLE PROPERTY OF DIYAUDIO. ANY REPRODUCTION IN PART OR WHOLE IS STRICTLY PROHIBITED WITHOUT WRITTEN PERMISSION.</div> <div>PROPRIETARY</div>			ISSUE		For DIYAUDIO only		THIS DRAWING CONTAINS PROPRIETARY INFORMATION AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION.		© 2017			
			REV	DATE	CHANGE HISTORY	DRAWN	TITLE UPACC Audio Inputs and Relays					REV
						JPS64 05/05/18						001
						CHECKED	DATE		DRG N°			
							05.05.2018 19:40					
						DATE	FILE: UPACC_00a		PAGE: 3/4			
00a	05.05.2018	First iteration										



<div>THE INFORMATION CONTAINED IN THIS DRAWING IS THE SOLE PROPERTY OF DIYAUDIO. ANY REPRODUCTION IN PART OR WHOLE IS STRICTLY PROHIBITED WITHOUT WRITTEN PERMISSION.</div> <div>PROPRIETARY</div>					ISSUE		For DIYAUDIO only		THIS DRAWING CONTAINS PROPRIETARY INFORMATION AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION.		© 2017					
					REV	DATE	CHANGE HISTORY	DRAWN	TITLE UPACC Microcontroller							REV
								JPS64 05/05/18								
								CHECKED	DATE					DRG N°		
									05.05.2018 19:40							
								DATE	FILE: UPACC_00a					PAGE: 4/4		
					00a	05.05.2018	First iteration									