

r13/r14 arranged on PCB to accept potentiometer footprint

bed of nails test points

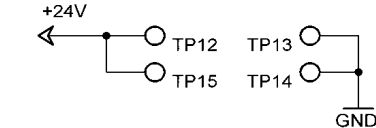
- BSPR TP20
- BSNL TP17
- BSPL TP18
- BSNR TP19
- AM0 TP3
- AM1 TP1
- AM2 TP4
- MODSEL TP2
- MUTE TP5
- SDZ TP8
- GAINSLV TP6

- AVCC TP16
- GVDD TP7
- TP9 GND

- OUTPL TP22
- OUTNL TP21
- OUTPR TP24
- OUTNR TP23

- FAULTZ TP11
- /OFF TP25
- PLIMIT TP10

- LINP
 - LINN
 - RINP
 - RINN
- use capacitor pads as test points



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Sheet: 1/1