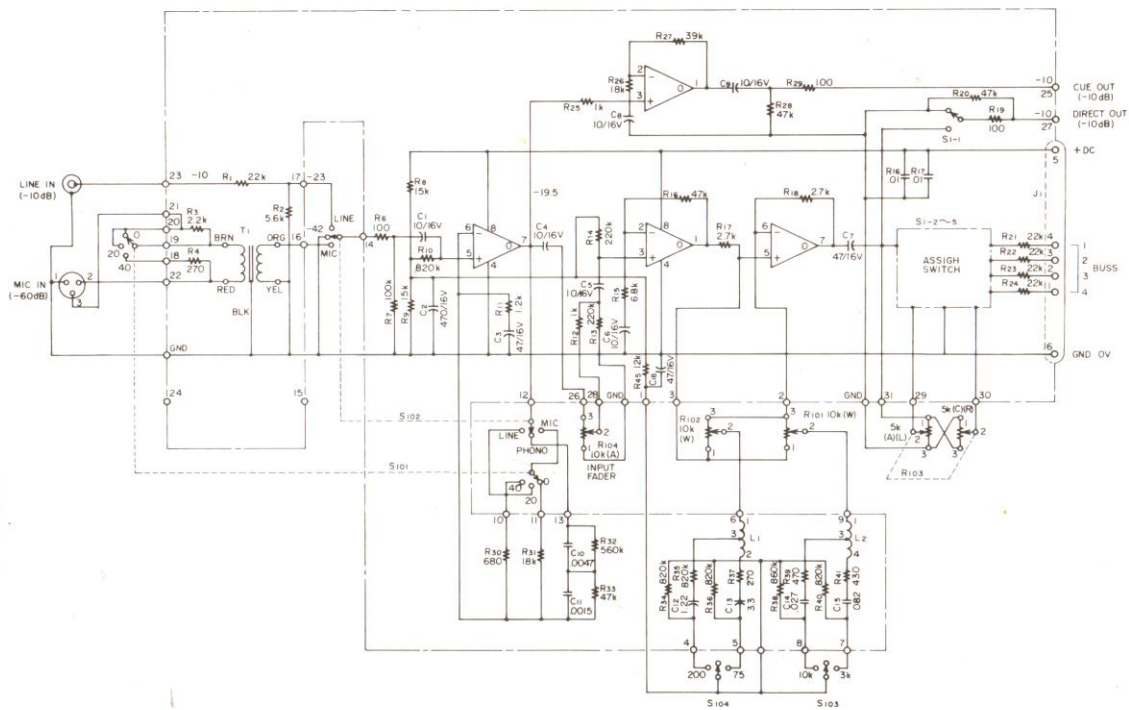


9. CIRCUIT SCHEMATICS 9.1 Input amplifier (Chan. 1 ~ 4)



SCHEMATIC
Input amp. (Ch. 1~4)
DWG. REV.