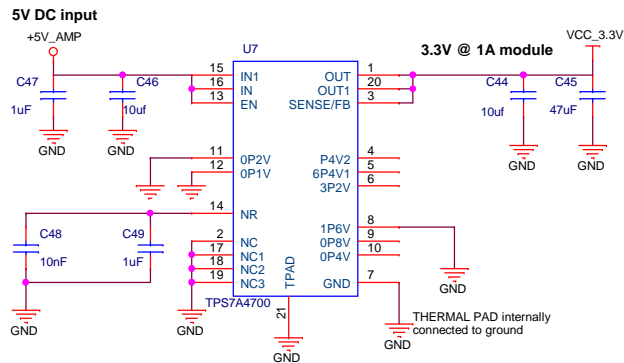
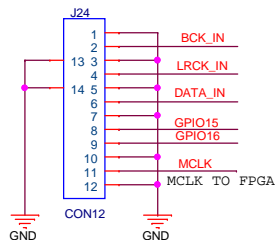


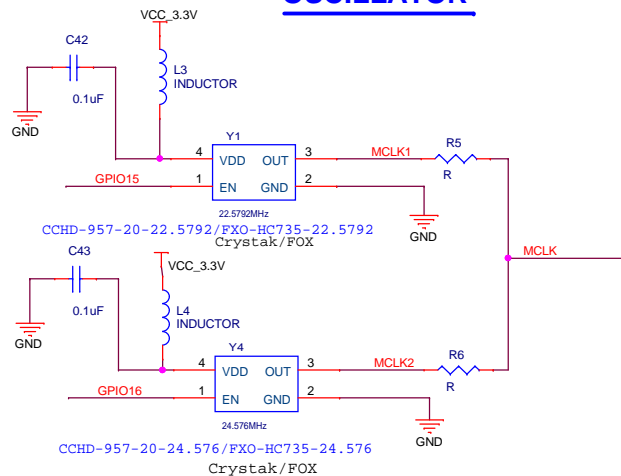
## ULTRA LOW NOISE LDO



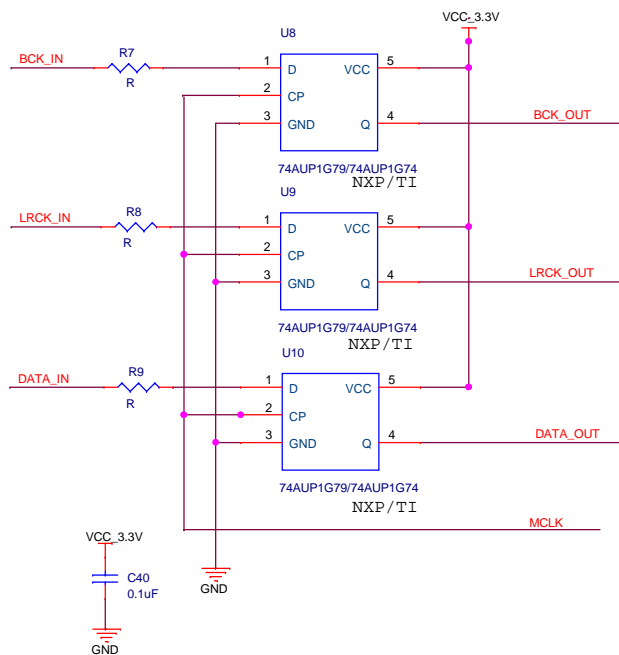
## INPUT CONNECTOR



## OSCILLATOR

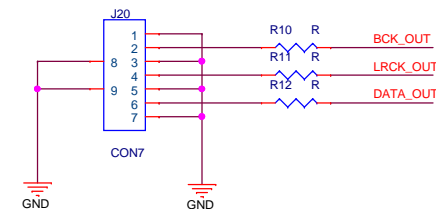


## SYNCHRONOUS RECLOCKER



NOTE: R ACTUAL VALUE WILL BE CHOSEN AFTER BOARD TESTING

## I2S OUTPUT



## MCLK



Note:

Synchronous Mode : MCLK CONNECTED TO DAC