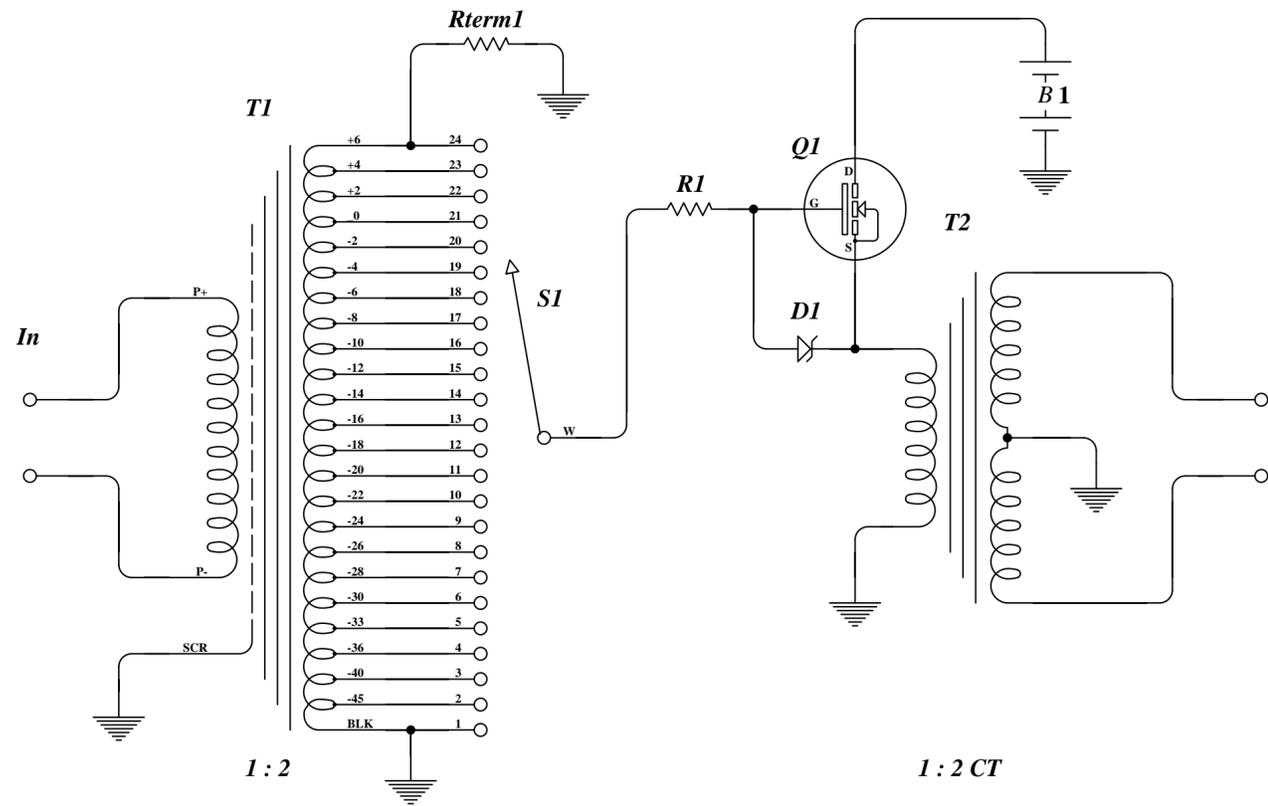
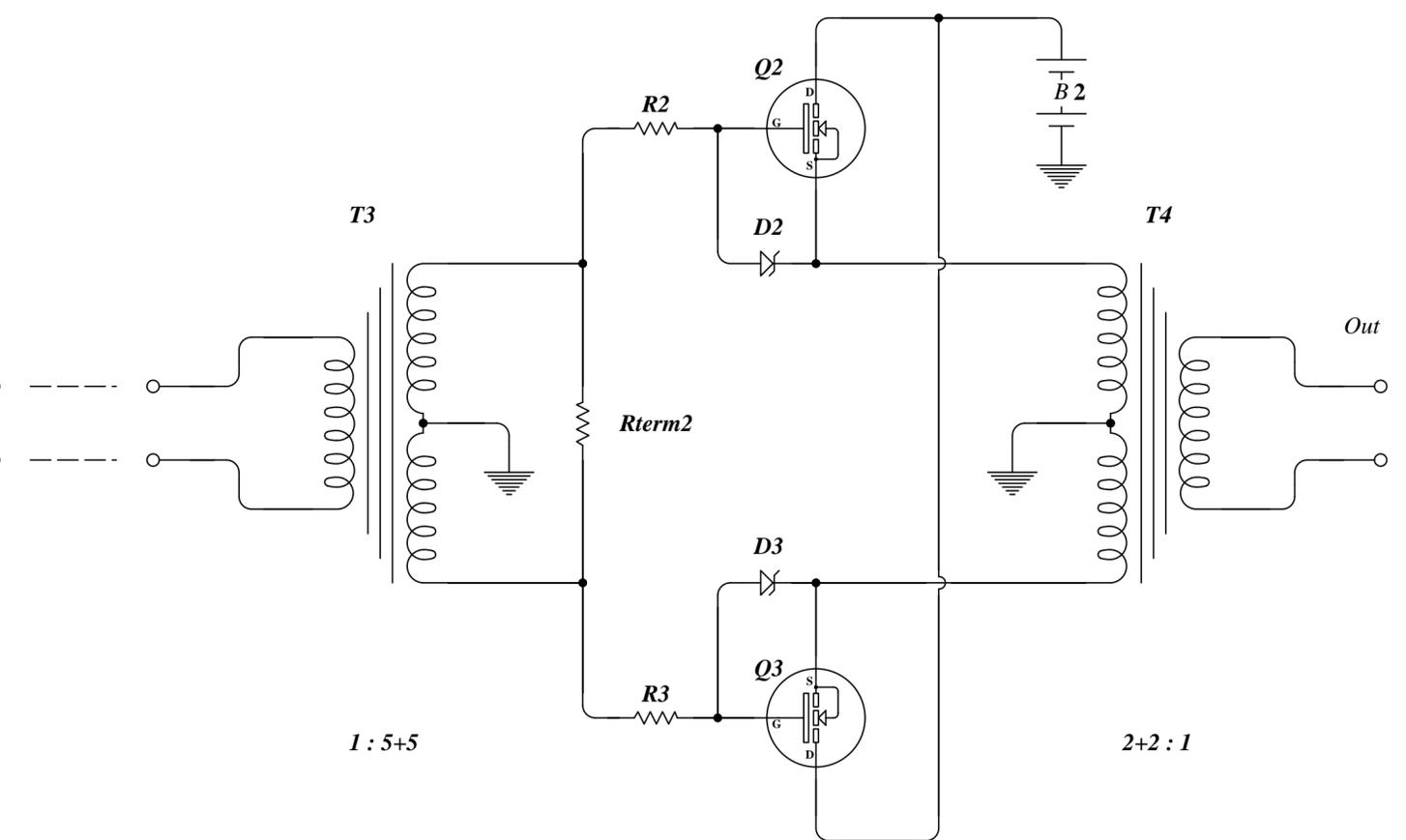


*Passive Pre & Line Driver*



*Power Stage*



*Q1, Q2 & Q3 are IXTH20N50D depletion mosfets.*

*Q1 operates at c. 500mA bias.*

*R1, R2 & R3 are 100R resistors.*

*D1, D2 & D3 are 12V gate protection zeners.*

*Q2 & Q3 operate at c. 750mA bias per device.*

*Rterm1 & Rterm2 are nominal 100K.*

*B1 is +24 volts; B2 is +36 to +48 volts.*

*For brevity the power supplies are representatively shown as batteries (in practise one would have fuses, switches and the like as well ), and source selection is omitted.*

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*Zeus Pre, Line and Power Audio Path Schematic*