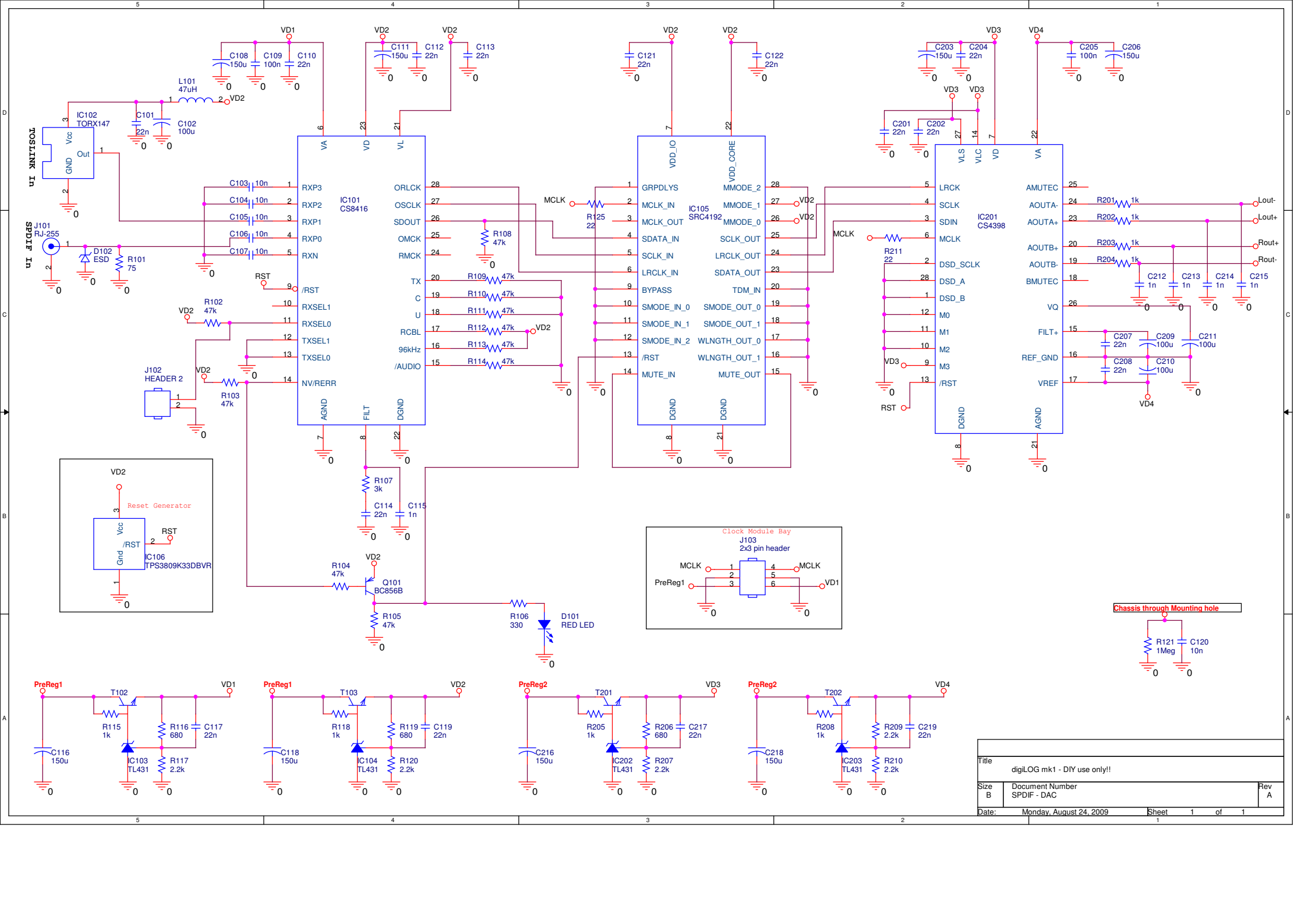
TOSLINK In
SPDIF In
J101
RJ-255
IC102
TORX147
L101
47uH
IC101
CS8416
IC105
SRC4192
IC201
CS4398
J102
HEADER 2
D102
ESD
D101
RED LED
Q101
BC856B
IC103
TL431
IC104
TL431
IC202
TL431
IC203
TL431
IC106
TPS3809K33DBVR
Reset Generator
Clock Module Bay
J103
2x3 pin header
PreReg1
PreReg2
Chassis through Mounting hole
Lout-
Lout+
Rout+
Rout-
Title
digiLOG mk1 - DIY use only!!
Size
B
Document Number
SPDIF - DAC
Rev
A
Date:
Monday, August 24, 2009
Sheet
1
of
1