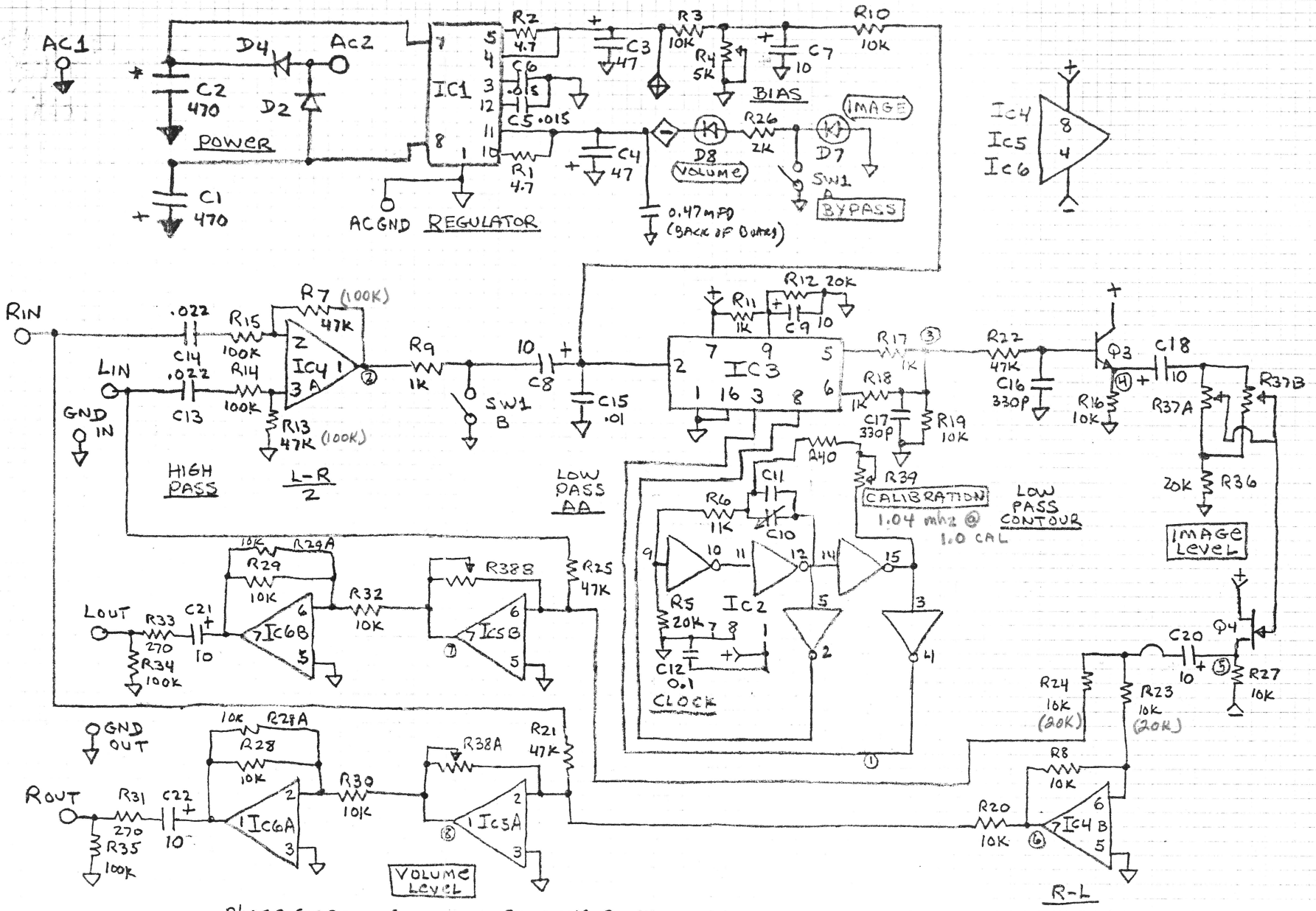

IR2100 SCHEMATIC

4/15/80 JMC

Rev 5/12/80 DROP D1, D3.

Rev 11/22/80 Q4 TO JFET

Rev 9/1/81 Q1, Q2, D5, D6 DELETED – DIRECT CONNECTION OF IC2 PIN 4 AND 7 TO IC3 PIN 3 AND 8

Rev 6/1/82 LOWER GAIN TO 1.0 VIA R28A R29A ON BOTTOM OF BOARD. ADD 0.5MFD ON V–