

FIGURE 10-1 Schematic of phono amplifier circuit.

without de-emphasis. The circuit file for this follows:

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Noise from magnetic phono-cartridge
Vgen 1 0 AC 1
Rgen 1 2 1350
Lgen 2 3 .5
Rin 3 0 47K
E1 4 0 3 0 10 ; first pole of RIAA curve @ 50Hz w/20dB boost
R1 4 5 1
C1 5 0 3.528m
R1A 4 6 212.8m ; pole @ 2120Hz
C1A 6 5 352.8u ; zero @ 500Hz
.ac dec 100 20 20K
.noise v(5) Vgen 100
.probe
.end

```

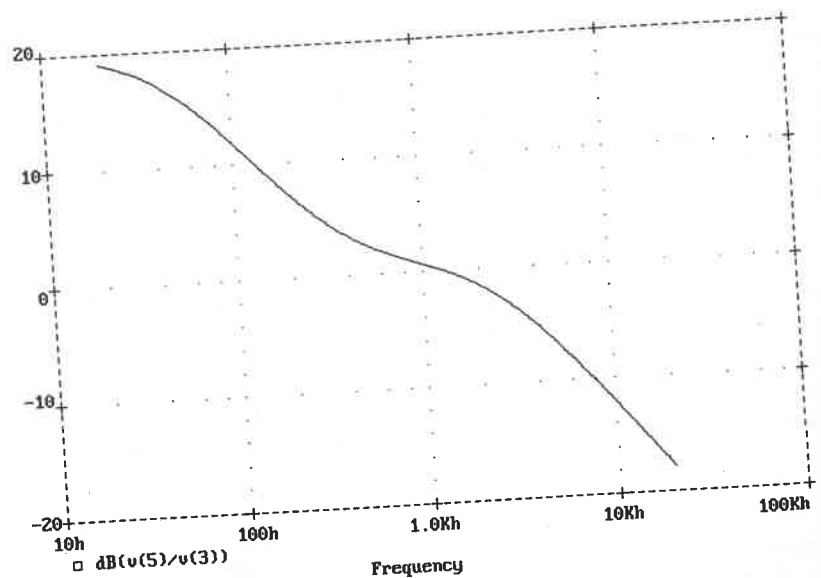


FIGURE 10-2 Plot of RIAA equalization curve.