

I.6. Expansion Port 1 Pinning

This is a dual-in-line 28-position header that currently allows connecting an external I2S stereo audio source – so this port functions as a stereo input only for now. More options will be provided in the future. Please refer to Appendix B for port location and pin numbering, and to the table below for a description of the signals.

Pin	Signal	Description
1	I2S_EXT1_LINE1	Audio data pair 1
2	DGND	Digital ground
3	I2S_EXT1_LINE2	Audio data pair 2
4	DGND	Digital ground
5	I2S_EXT1_LINE3	Audio data pair 3
6	DGND	Digital ground
7	I2S_EXT1_LINE4	Audio data pair 4
8	DGND	Digital ground
9	I2S_EXT1_LINE5	Audio data pair 5
10	DGND	Digital ground
11	I2S_EXT1_LINE6	Audio data pair 6
12	GPIO_EXT11	GPIO or I2C
13	IS_EXT1\	Detect expansion board
14	GPIO_EXT10	GPIO or I2C
15	CS_EXT1\	SPI Chip Select
16	MISO_EXT1	SPI Master In Slave Out
17	SCLK_EXT1	SPI Serial Bit Clock
18	MOSI_EXT1	SPI Master Out Slave In
19	I2S_EXT1_FST	I2S Frame Sync Transmit
20	+3.3V	Power supply (out)
21	I2S_EXT1_CKT	I2S Serial Clock Transmit
22	DGND	Digital ground
23	I2S_EXT1_CKR	I2S Serial Clock Receive
24	DGND	Digital ground
25	I2S_EXT1_FSR	I2S Frame Sync Receive
26	DGND	Digital ground
27	EXT_CLK	External master clock input
28	+5VD	Power supply (out)

Arduino

Pin 3

Pin 8

Pin 4

Pin 5