



CSS: softstart, sets max Freq and start-up-freq-shift.

DELAY: Overcurrent-shutdown-delay.

CF: Timing CAP

RF-Min: Min Osc. Pin at 2V.  
Current trough R50/R51 determines MIN-freq.  
Current trough R55 to FB determines MAX-freq

STBY: Burst-mode threshold.  
Standby-mode if voltage is lower than 1.25V  
R60 realize burst-mode.  
R61 turns on together with LINE

ISEN: Connect to GND to disable

LINE: IC shut down at voltage lower than 1.25V.  
Bias between 1.25V and 6V  
Enable: 75V - 360V

DIS: Latched shutdown if voltage goes above 1.85V  
Sense overvoltage with R28 and D19  
(43V-15V)\*(3k3/(3k3+47k))=1.85V

DIS: NTC-temp shutdown: 10K-ntc = 2.5K@60degC.  
12\*(3k3/(3k3+15k+2k5))=1.9V

External disable:  
- Mount R42 for DISABLE: latched shutdown  
-Mount R34 for un-latched shutdown

IDEAs:

- OverCurrent detect with an extra winding on res-coil: see QSC PLX PSU:
- NO: prototype dos not work
- switch Tx and reas-coil for better EMV/EMC (HF removed by res-coil before TX?)
- DONE