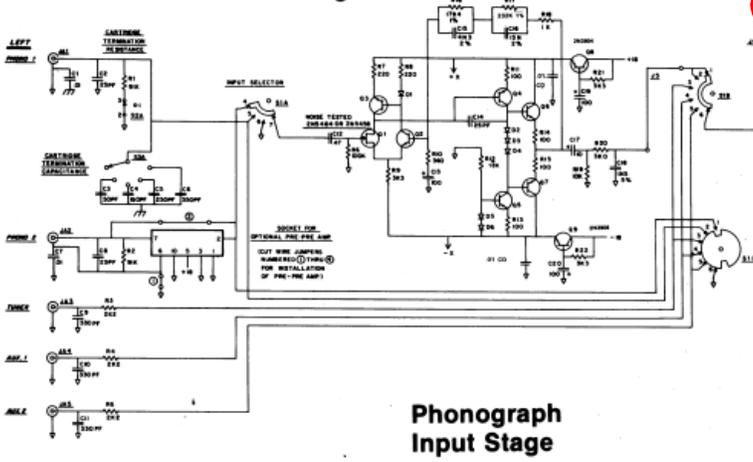
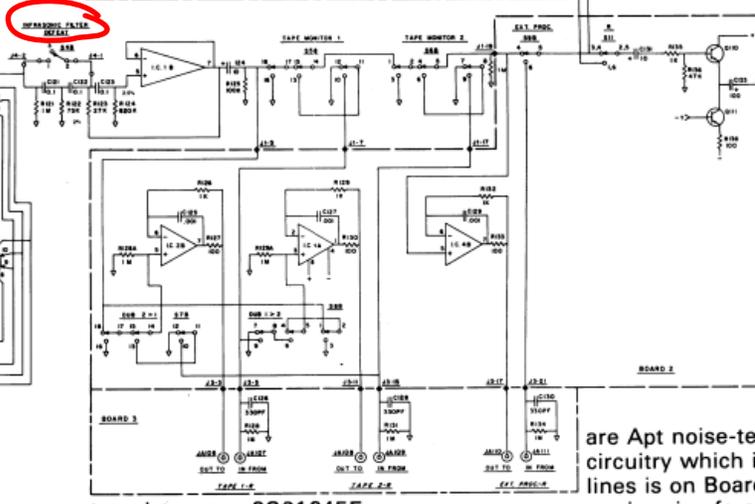
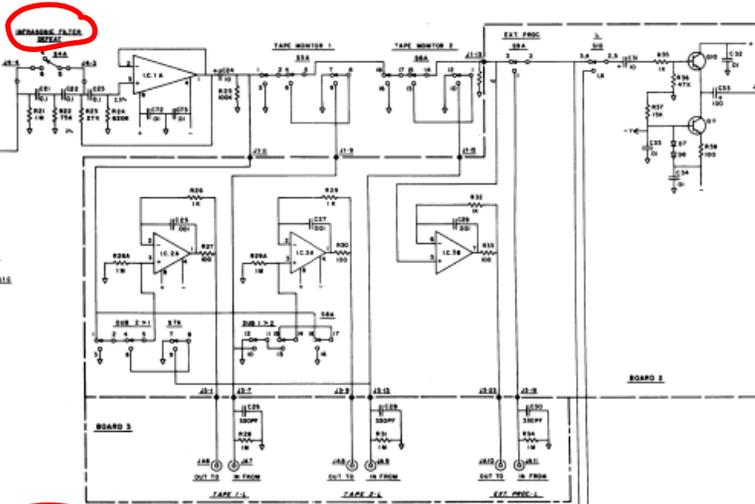


# 10 Schematic Diagram



**Phonograph Input Stage**

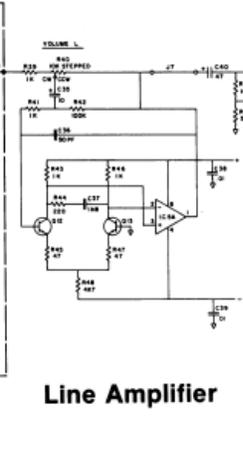


Schematic Notes: Switch S1 shown in Phono 2 position as viewed from rear. Front panel pushbutton switches are shown in *out* position; rear panel pushbuttons are shown in *in* position; toggle switches are shown in *down* position. J1, J2, J3 are multiwire board-to-board jumpers; other jumpers are point-to-point on individual boards. All *nnp*

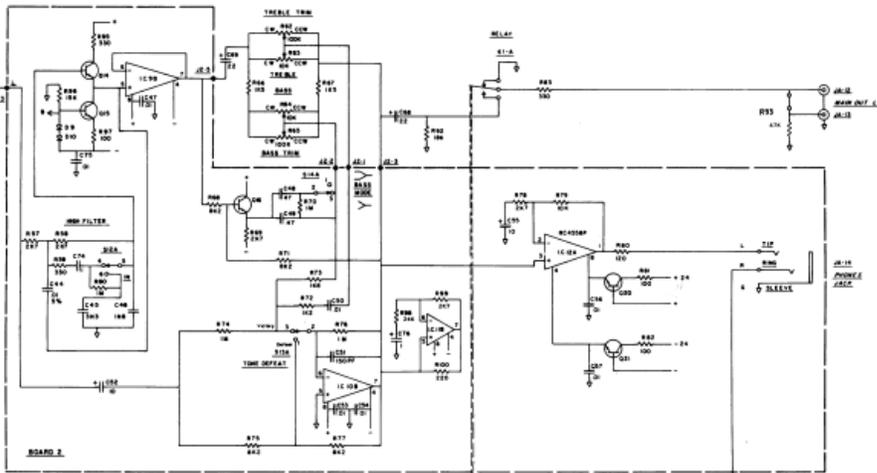
transistors are 2SC1345E or equivalent except as noted; all *pn*p transistors are 2N5087; all integrated circuits are TL072CP except as noted. Pin 1 and pin 7 of all integrated circuits are at 0 Vdc +1 Vdc. Phono preamp input FETs

are Apt noise-test circuitry which is on Board 1. Lines marked with "N" are in microfarads (1N5 = 1.5 nanofarads). For resistors, M = megohms, K = kilohms, pF = picofarads.

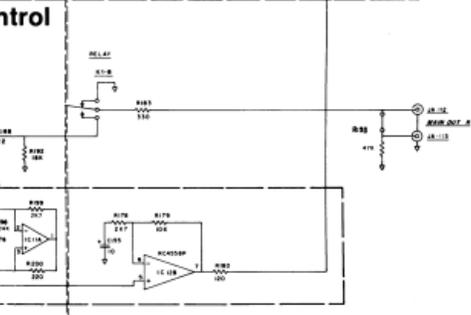
### Line Amplifier



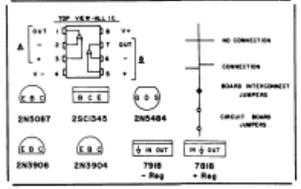
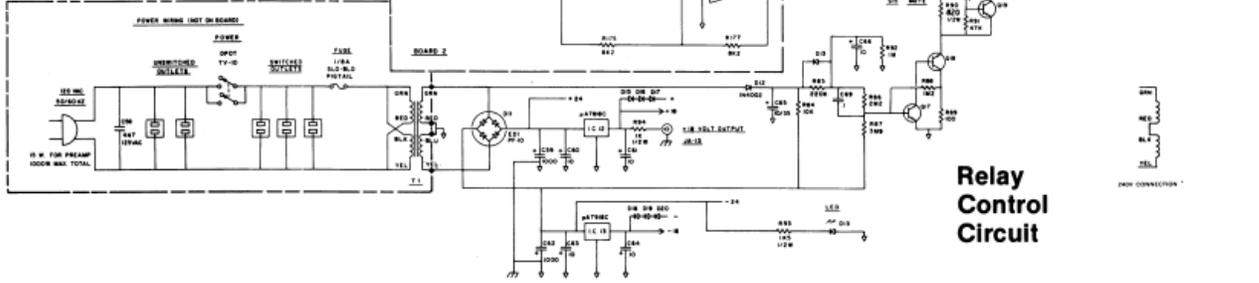
### Tone Control



### Headphone Amplifier



### Relay Control Circuit



...ted types. All  
... not within dotted  
... 1. All capacitors  
...s except those  
... (arads) and those  
... as a decimal point  
... arads = 1500  
... resistors, K =  
... ohms. Thus 3M3 =