

Phonograph Input Stage

This schematic diagram illustrates the Phonograph Input Stage, which is divided into three boards: BOARD 1, BOARD 2, and BOARD 3.

BOARD 1: The input stage begins with an **INPUT SELECTION** switch (S1) that routes the signal from either the **LEFT** or **RIGHT** channels to the **PHONO** input. The signal then passes through a **PHONO** input transformer (T1) and a **PHONO** input capacitor (C1). The signal is then amplified by a **PHONO** pre-amplifier (IC1) and a **PHONO** amplifier (IC2). The output of the pre-amplifier is connected to the **PHONO** input of BOARD 2.

BOARD 2: The signal from BOARD 1 is connected to the **PHONO** input of BOARD 2. It then passes through a **PHONO** input transformer (T2) and a **PHONO** input capacitor (C2). The signal is then amplified by a **PHONO** pre-amplifier (IC3) and a **PHONO** amplifier (IC4). The output of the pre-amplifier is connected to the **PHONO** input of BOARD 3.

BOARD 3: The signal from BOARD 2 is connected to the **PHONO** input of BOARD 3. It then passes through a **PHONO** input transformer (T3) and a **PHONO** input capacitor (C3). The signal is then amplified by a **PHONO** pre-amplifier (IC5) and a **PHONO** amplifier (IC6). The output of the pre-amplifier is connected to the **PHONO** input of BOARD 4.

Key Components:

- IC1, IC2, IC3, IC4, IC5, IC6:** Integrated circuits for the pre-amplifier and amplifier stages.
- T1, T2, T3:** Transformers for the input and output stages.
- C1, C2, C3:** Capacitors for the input and output stages.
- S1:** Input selection switch.

Notes:

- The **PHONO** input transformer (T1) is specified as **1000 OHMS**.
- The **PHONO** input capacitor (C1) is specified as **100 PF**.
- The **PHONO** input transformer (T2) is specified as **1000 OHMS**.
- The **PHONO** input capacitor (C2) is specified as **100 PF**.
- The **PHONO** input transformer (T3) is specified as **1000 OHMS**.
- The **PHONO** input capacitor (C3) is specified as **100 PF**.

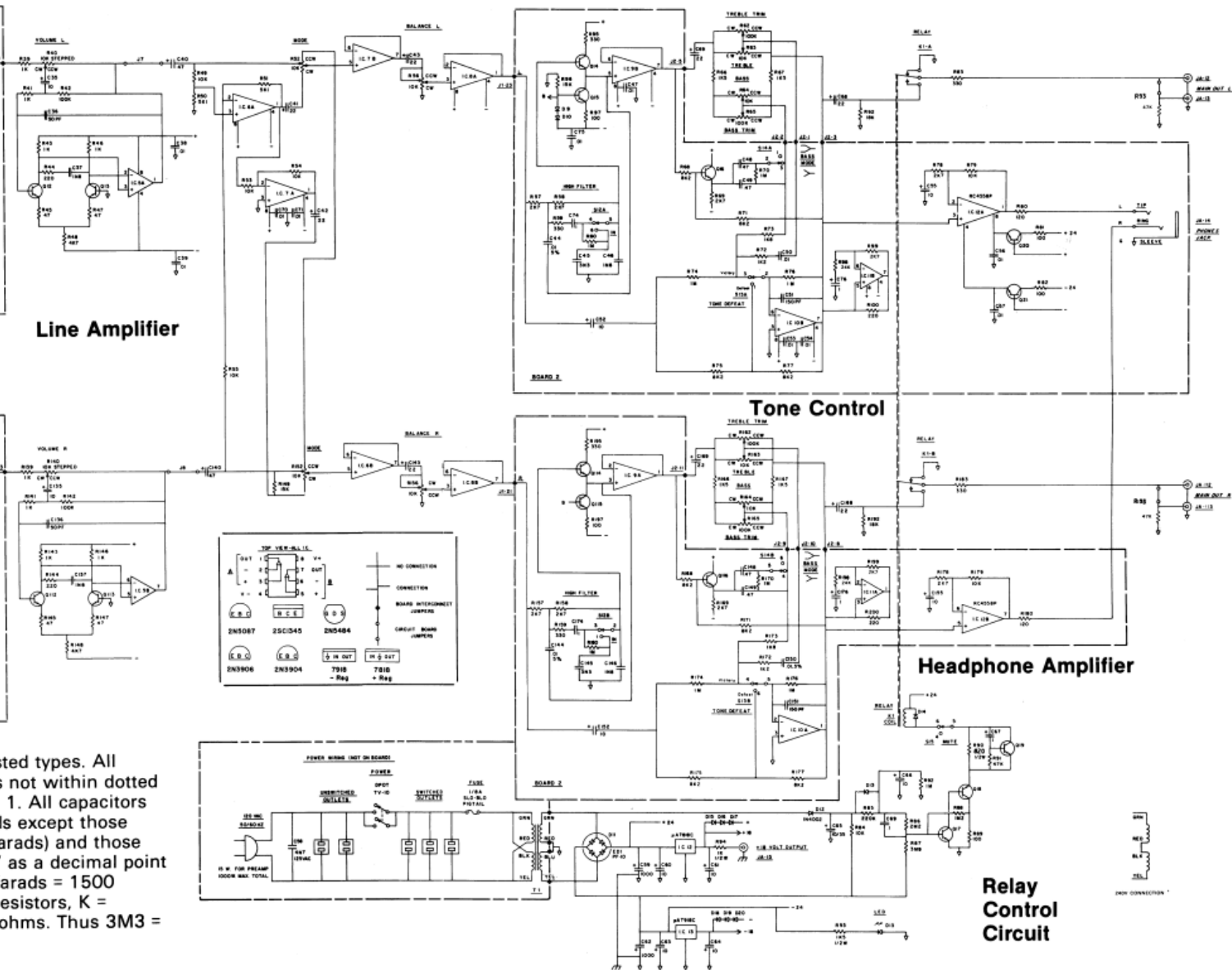
Schematic Notes: Switch S1 shown in Phono 2 position as viewed from rear. Front panel pushbutton

are Apt noise-test circuitry which is lines is on Board

transistors are 2SC1345E or equivalent except as noted; all *pnp* transistors are 2N5087; all integrated circuits are TL072CP except as noted. Pin 1 and pin 7 of all integrated circuits are at 0 Vdc +1 Vdc. Phono preamp input FETs

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Line Amplifier



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ohms. Thus 3M3 =

Relay Control Circuit