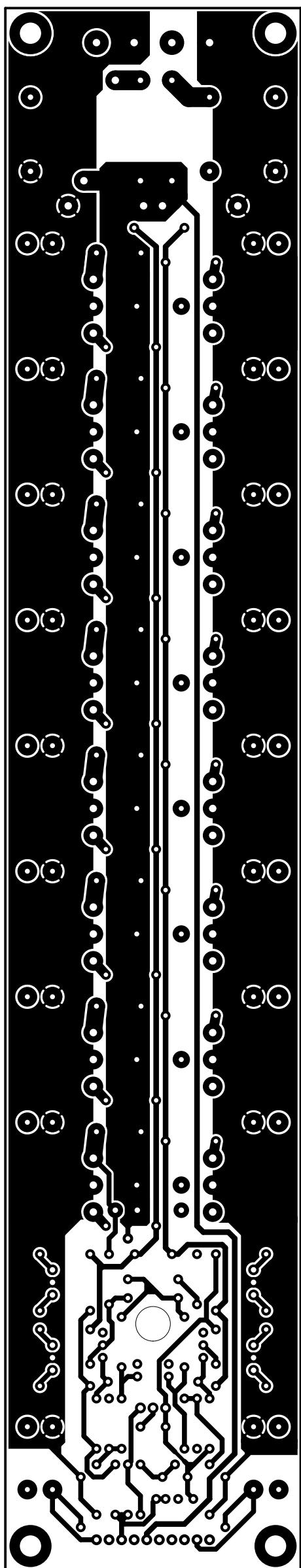
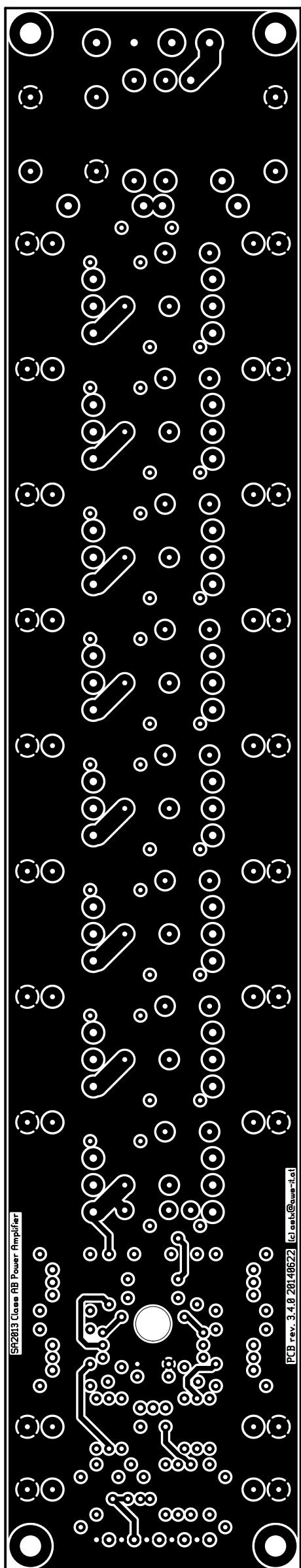
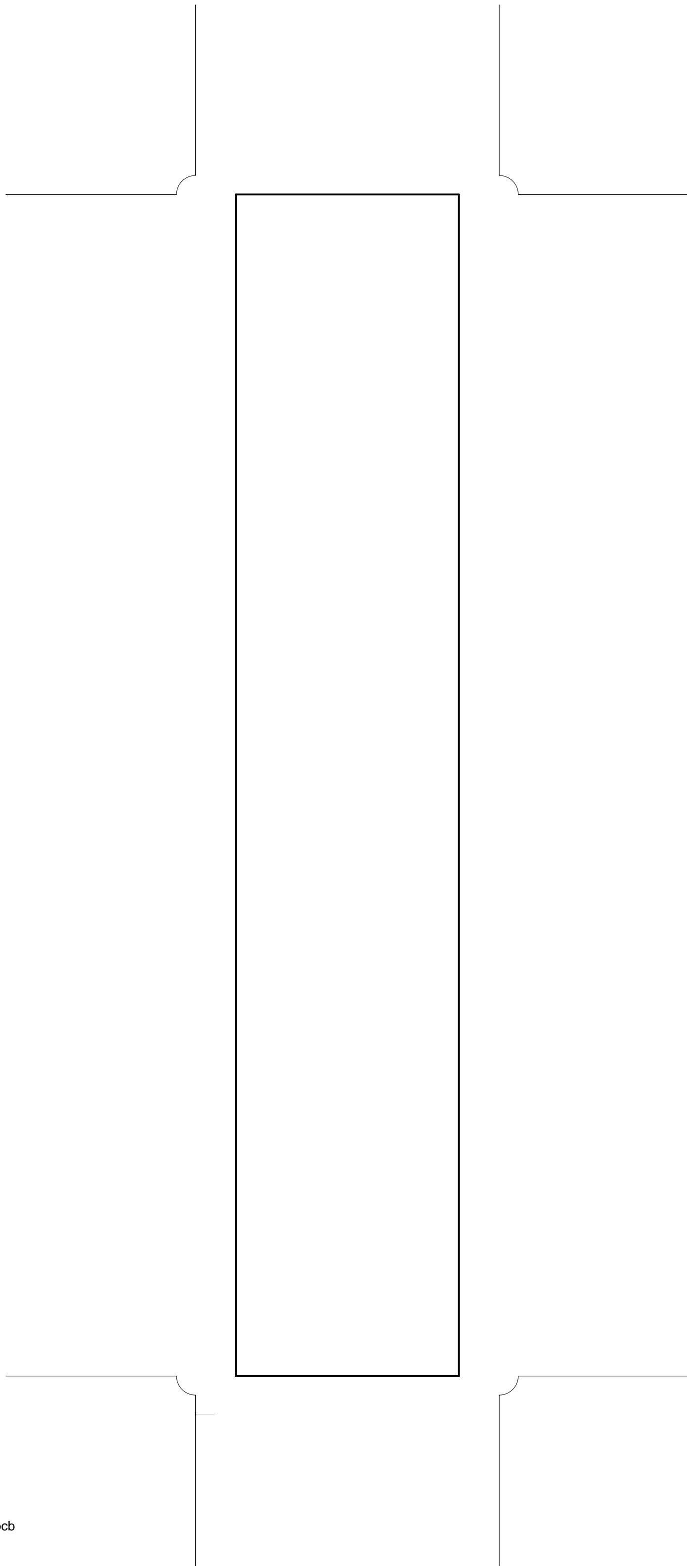


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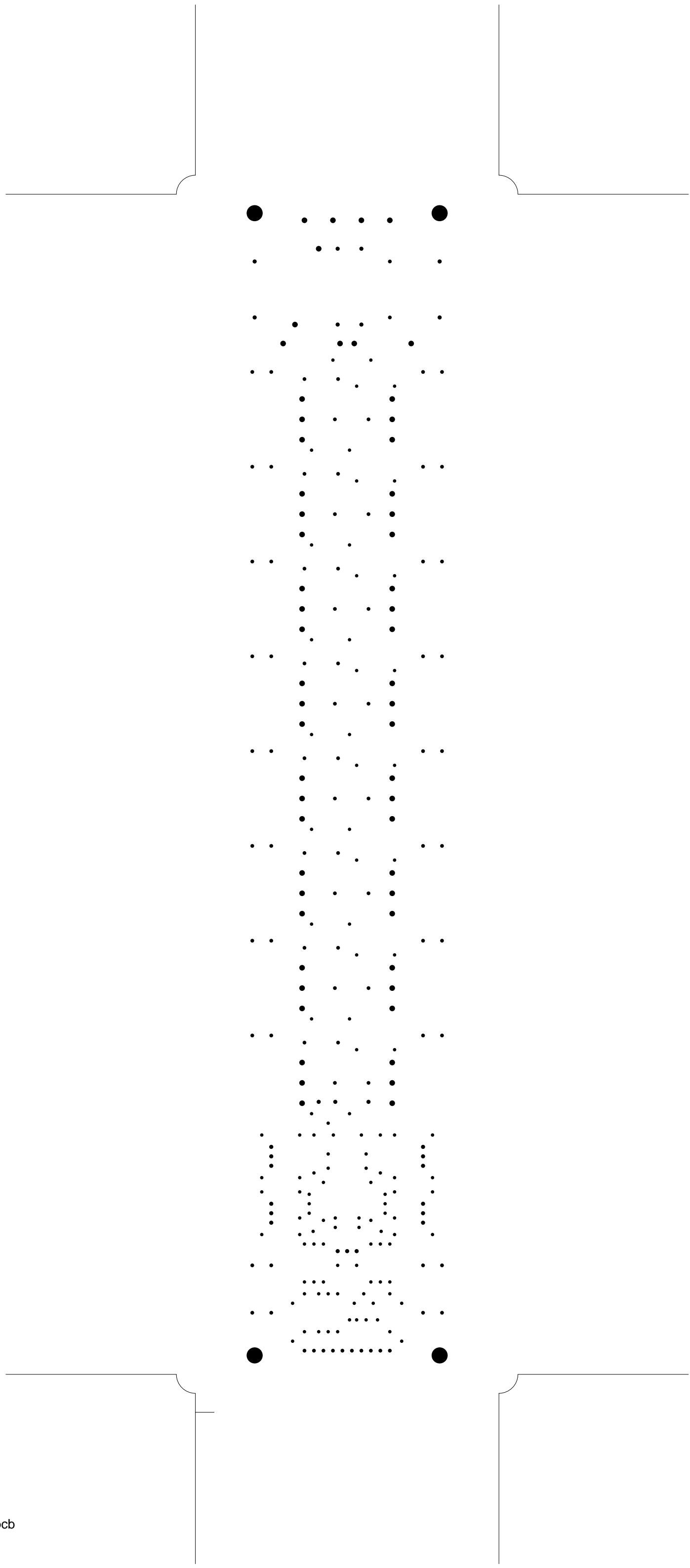


top, scale = 1:1.000
sa2013_bias_output_stage.pcb





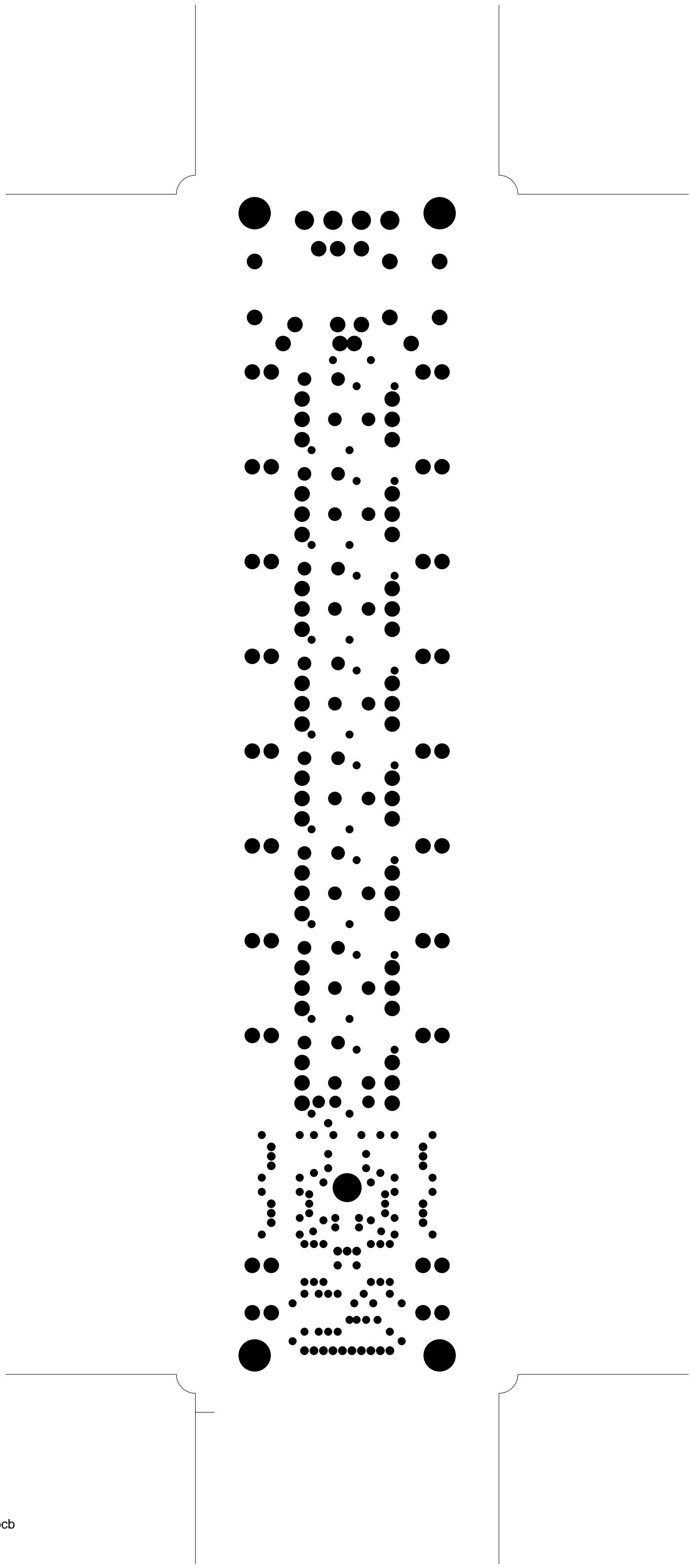
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sa2013_bias_output_stage.pcb



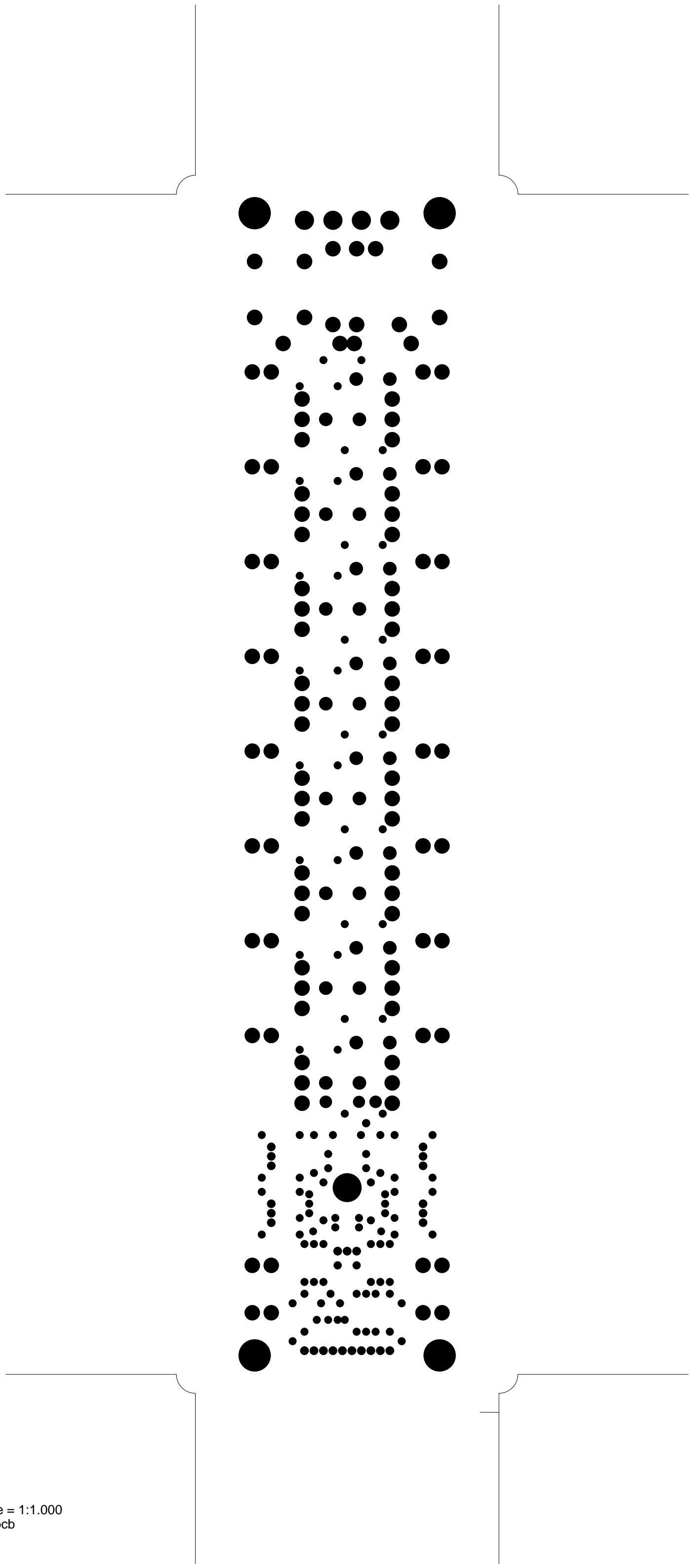
plated-drill, scale = 1:1.000
sa2013_bias_output_stage.pcb



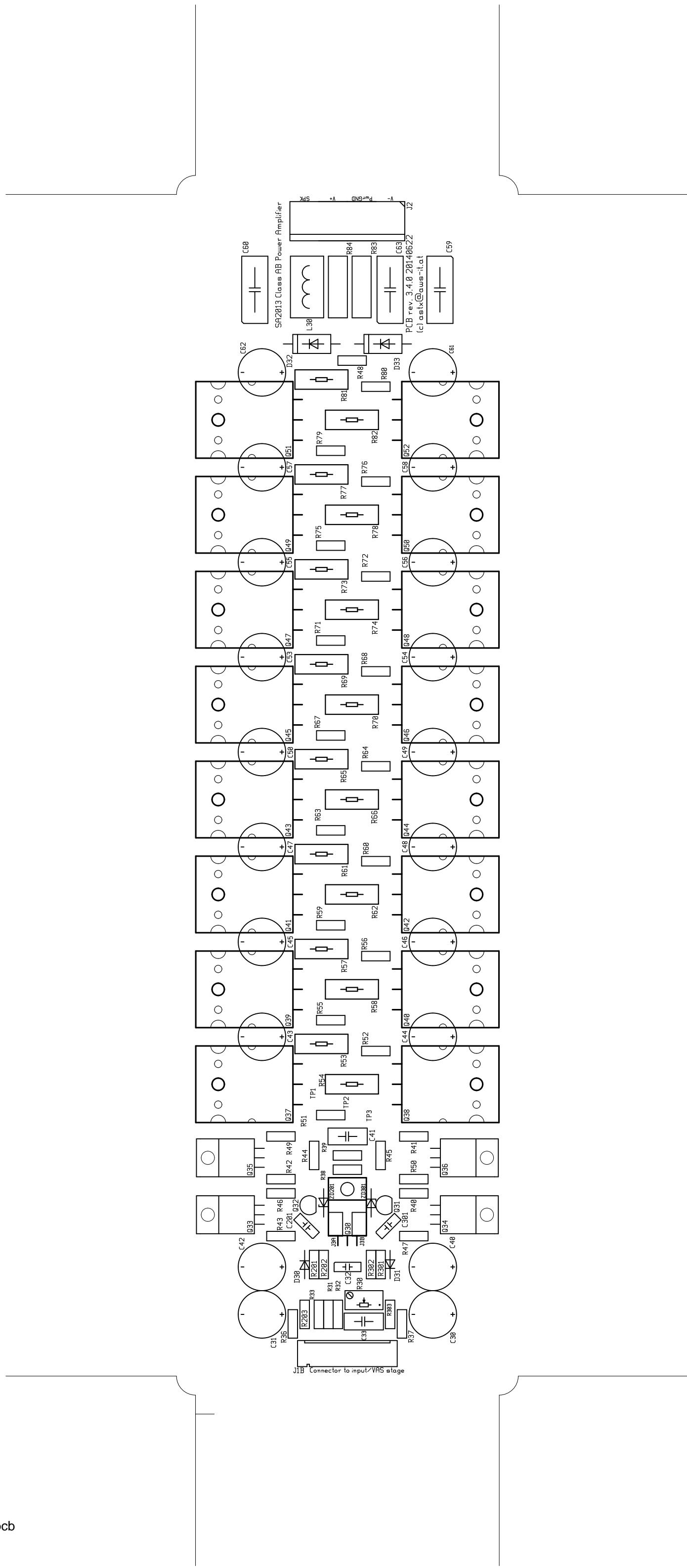
unplated-drill, scale = 1:1.000
sa2013_bias_output_stage.pcb

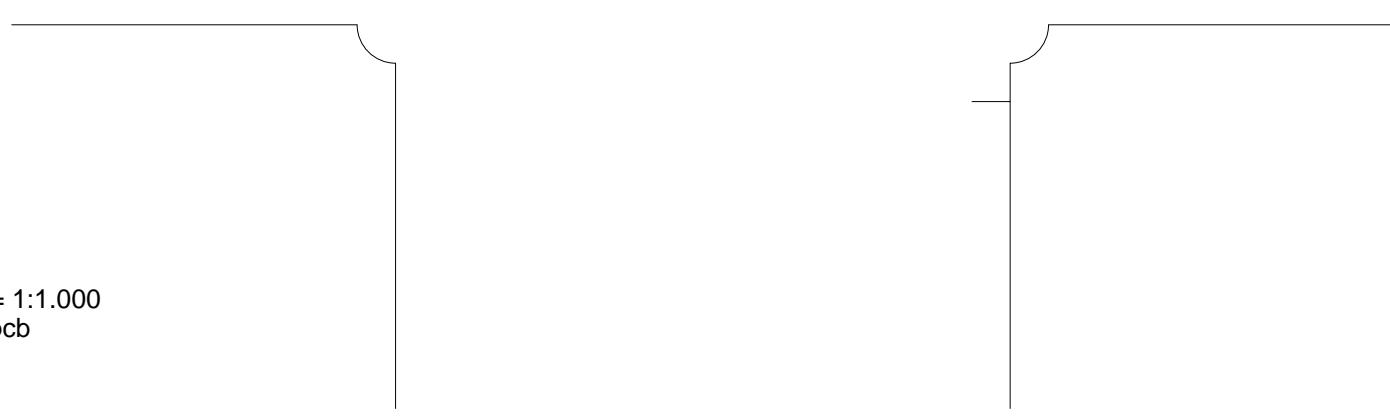


topmask, scale = 1:1.000
sa2013_bias_output_stage.pcb

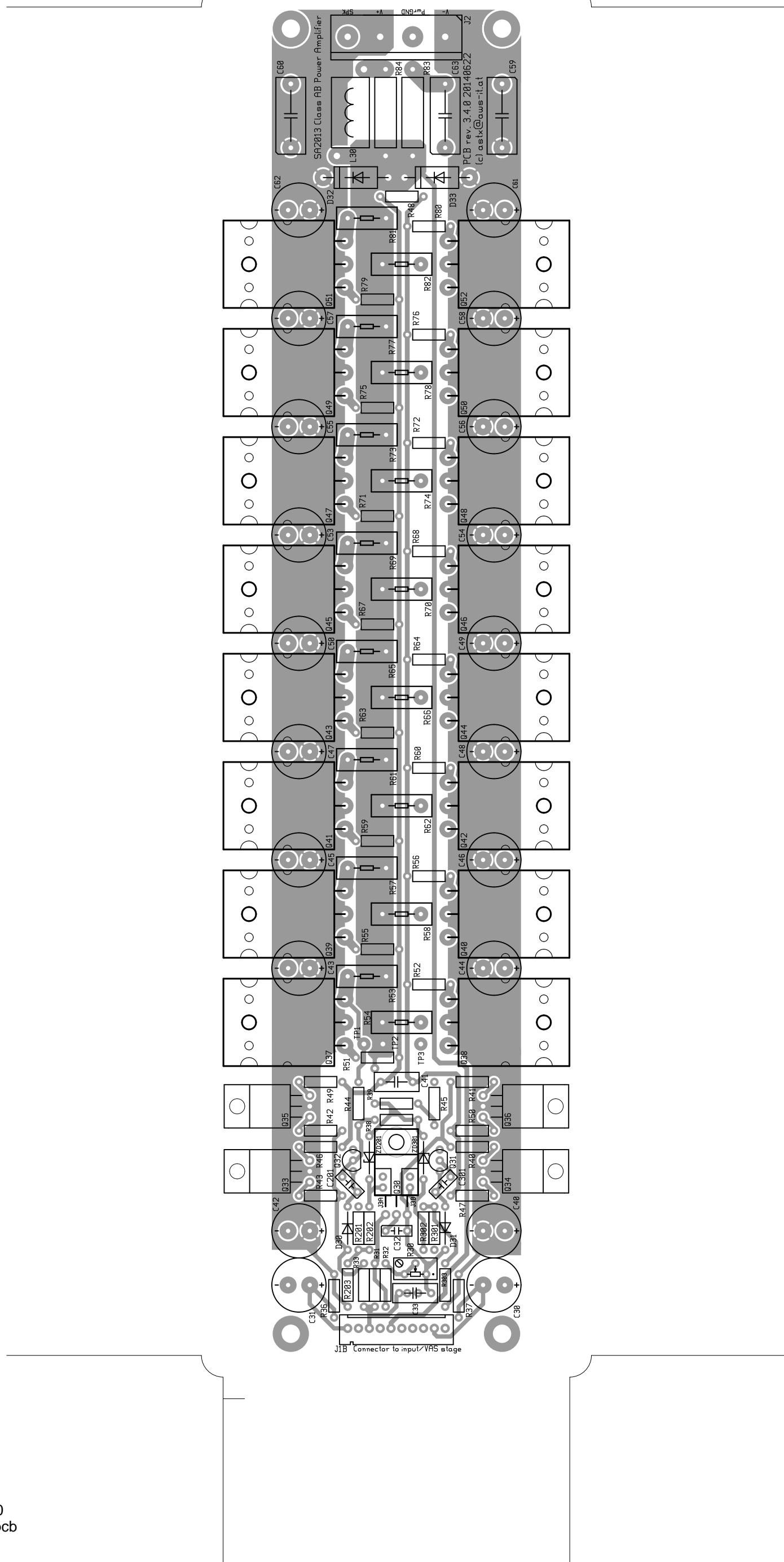


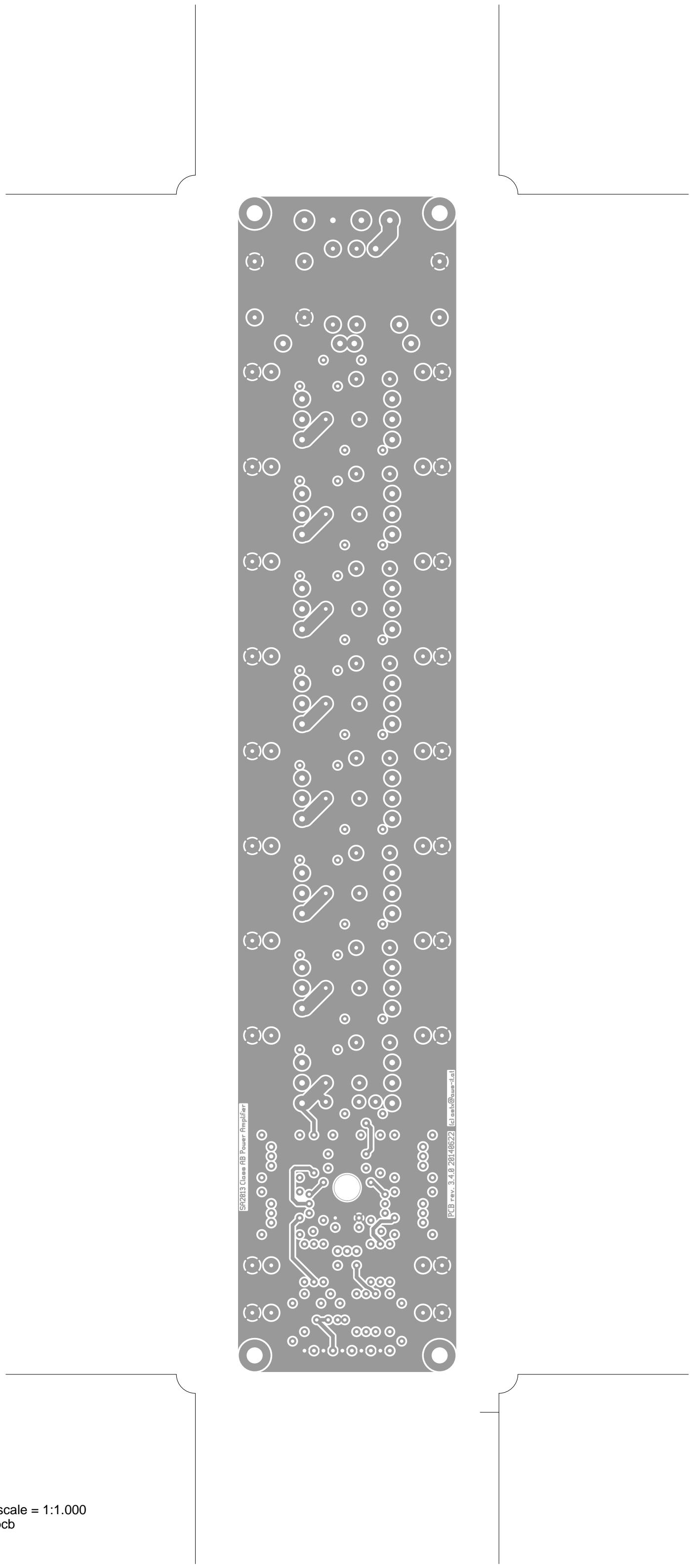
bottommask (mirrored), scale = 1:1.000
sa2013_bias_output_stage.pcb





bottomsilk (mirrored), scale = 1:1.000
sa2013_bias_output_stage.pcb



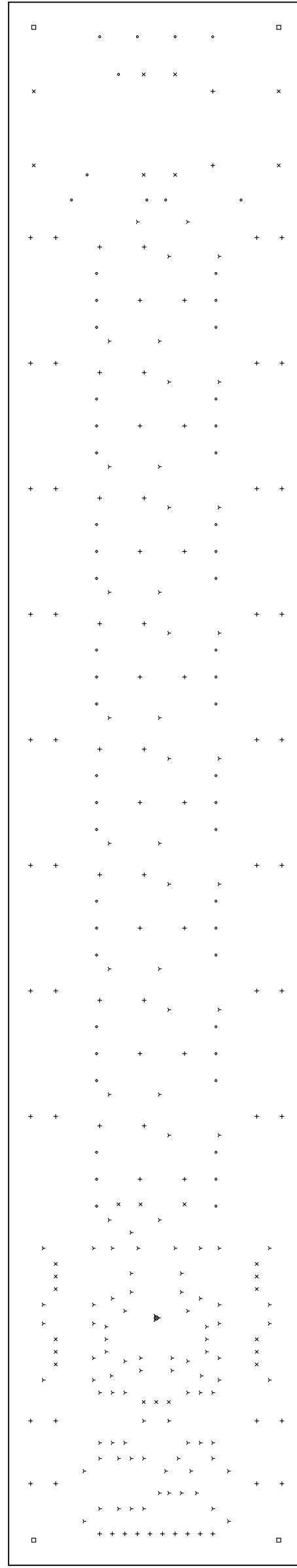


bottomassembly (mirrored), scale = 1:1.000
sa2013_bias_output_stage.pcb

There are 6 different drill sizes used in this layout, 287 holes total.

Symbol	Diam. (Inch)	Count	Plated?
y	0.035	114	YES
+	0.039	84	YES
x	0.043	26	YES
*	0.059	58	YES
□	0.169	4	YES
Ψ	0.276	1	NO

Title: [unknown] - Fabrication Drawing
Author: tom
Date: Mon Jun 23 11:29:41 2014 UTC



Board outline is the centerline of this path