



| DRAWING REVISION RECORD | | | |
|-------------------------|-----------------|----------|----------|
| REV | DESCRIPTION | DATE | APPROVED |
| A | PRODUCT RELEASE | 02/08/04 | R J P |
| B | ECO #6959 | 03/30/04 | |
| C | ECO #6980 | 03/30/04 | |
| D | ECO #7049 | 04/01/04 | R J P |

| SIGNATURES | | DATE | ROCKFORD CORPORATION | |
|------------|-----|----------|----------------------|--|
| DRAWN | RJP | 03/30/04 | FOSGATE DIVISION | |
| CHECKED | | | TEMPE, AZ | |
| DESIGN | | | DRAWING TITLE: | |
| ENG | | | 2004 PUNCH - 6001BD | |
| MAN | | | POWER SUPPLY | |
| CHK | | | SIZE | |
| FILE | | | FILE NAME: | |
| DESIGN | | | DWG/PART NO: | |
| ENG | | | REV | |
| MAN | | | D | |
| CHK | | | SCALE: | |
| FILE | | | DO NOT SCALE DRAWING | |
| DESIGN | | | SHEET 1 OF 3 | |

5. TESTPOINTS MEASURED WITH RESPECT TO GROUND WITH A DVM
MEASUREMENTS MAY VARY +/- 20%
TEST CONDITIONS:
VB+ = 13.8 VDC, XOVER SET TO ALL PASS, GAIN SET TO MINIMUM,
REMOTE BASS BOOST NOT CONNECTED, LOGO ASSEMBLY CONNECTED.
DC TEST POINTS MEASURED WITH NO INPUT SIGNAL
AC TEST POINTS MEASURED WITH 500 mVAC 1kHz UNBALANCED SINE
WAVE AT INPUT JACK. 2 OHM LOAD PER CHANNEL AT OUTPUT
CONNECTOR.
4. ALL CAPACITORS 20% TOLERANCE.
3. ALL RESISTORS IN OHMS, 1% TOLERANCE.
2. THIS SCHEMATIC IS FOR THE FOLLOWING PCB ASSEMBLIES: WP-4706
1. THIS SCHEMATIC IS FOR THE FOLLOWING PCB FABRICATION: PC-4706
NOTES: (UNLESS OTHERWISE SPECIFIED)

6. MAXIMUM OUTPUT TEST CONDITIONS:
VB+ = 14.4 VDC, XOVER SET TO MAX, GAIN SET TO MINIMUM,
REMOTE BASS BOOST NOT CONNECTED, LOGO ASSEMBLY CONNECTED.
4.19 VAC 100Hz SINE WAVE AT INPUT JACK, 2 OHM LOAD PER
CHANNEL AT OUTPUT CONNECTOR.
VOUT = 34.6 VAC, POWER OUT = 600 mW/CHANNEL
THD+N <= 0.8%, 10Hz - 22kHz FILTERED.





