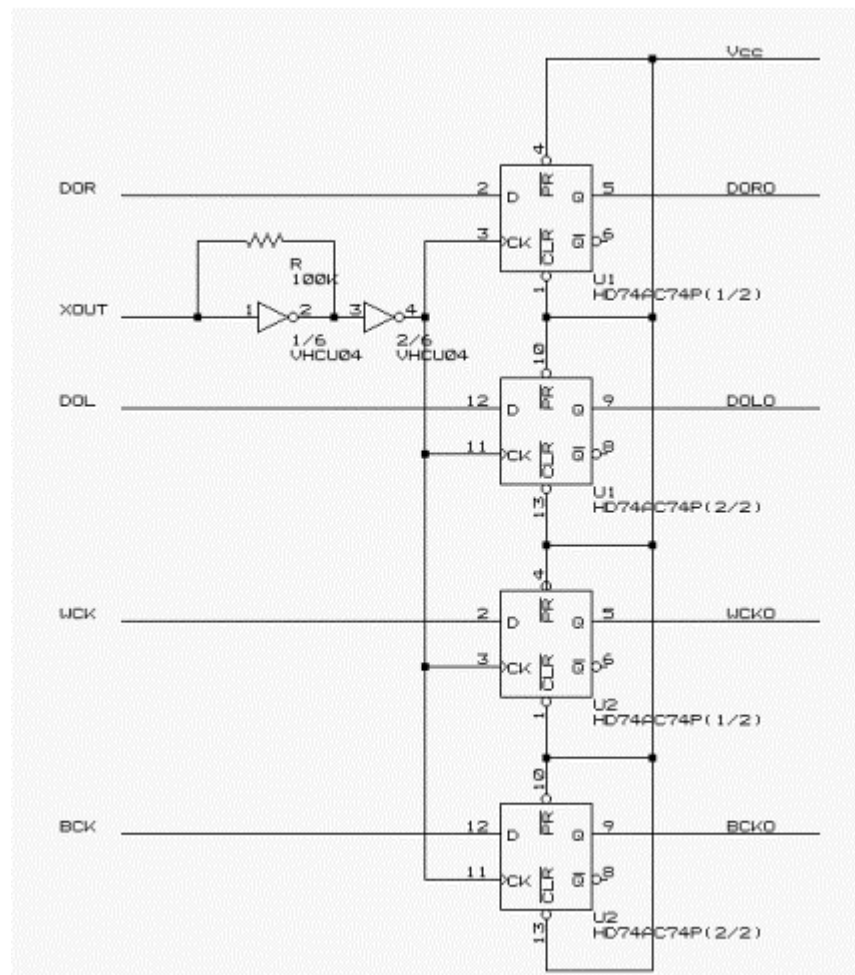


## TCXO»PReclocked<sup>ao</sup>¶[¤J

!³¤F³D¥·CD-ROM<sup>ao</sup>gÅç;A¥un§ó'«<sup>ao</sup>ë·Ç«×»P<sup>oa</sup>Ã-  
©w«×<sup>ao</sup>®¶Àú¾4!;AÄnµ°·¤Wln¤W¤@¤j,`;C§Ú-O¥ý¥Î+/-  
1ppm<sup>ao</sup>TCXO·ú¥N8412¤³,ao®¶Àú°j,ô;A°µ<sup>ak</sup>«ÜÂ<sup>23</sup>æ;A<sup>a1</sup>½±µ±N8412<sup>ao</sup>  
MCK»PDF1700<sup>ao</sup>XTI»É°ä¤ÁÂ\_¡A!A±N<sup>TCXO</sup><sup>ao</sup>¿é¥X<sup>a1</sup>½±µÄé¤JDF17  
00<sup>ao</sup>XTI'N!æ¤F;A¥ú-O³oÓ·BÆJ'NÁýÁnµ<sup>a</sup>¿{³s¤É¤T- Å;C

±µ¤U·Ó'N-OReclocked<sup>1</sup>q,ô<sup>ao</sup>¥[¤J;A³o³;¥÷!Lµ{ÆZ§ç©V<sup>ao</sup>;A¥ý-O°Ñ·  
Ó¤é¥»DIY°ô-,©Ò¤¹½§G<sup>ao1</sup>q,ô;A±NDF1700<sup>ao</sup>DOR;BDOL;BWCKO»P  
BCKO»P¥D@É°ò;]11.2896MHz;^reclocked«ájA¤Å§O°e¤JPCM63<sup>ao</sup>D  
ata;BCLK»PLE;Aµ<sup>2a</sup>G³£-OÄø-  
µ;AµL<sup>a</sup>k¥¿±¤¤u§@;A¤£¹L¤é¥»¤H-O¥Î!bPMD100»PPCM1704¶j;A§Ú  
µL<sup>ak</sup>½T©w³o°ÝÃD-O¤£-O!]-°DAC-[°c¤£!P©Ò³y!·<sup>ao</sup>;A!³°ô¤¿,Ö¹L¥Î!  
bSM5842»PPCM1702¶j;µL<sup>ak</sup>¤¤u§@;A¤£±o¤w¥u!n©ñ±ó;C<sup>a1</sup>½·î³!¤ñ!b°  
ô,ô¤W§ä·¤¤@½²gNils Strom©Ò¼g<sup>ao</sup>;[µHigh End Digital to Analog  
Converter](#);y±o·¤¤@·Ç±Ö¥Ü»Pµ<sup>a</sup>®×;A°K¿ý·ä¤¤¤¤-q¤¤¤rµ¹¤j®a°Ñ!Ò;  
C

The PCM63's analog output is clocked on the rising edge of the word clock (LE).  
The PCM1702's analog output is clocked on the fourth data clock pulse (BCK) after LE.



!¹q,ô-O·ú!Ü¤é¥»DIY°ô-,A¥Î!bPDF1700»PPCM63¶jµL<sup>a</sup>k¥¿±¤¤u§@;C

3I«á©w®×<sup>ao</sup>Reclocked<sup>1</sup>q,ô;C



¥İ¬¬}¬}ªO·FººReclocked¹q,ô¹C

- [http://home.kimo.com.tw/prbstech/DAC\\_reclocked/Reclocked-1.htm](http://home.kimo.com.tw/prbstech/DAC_reclocked/Reclocked-1.htm)
- [\[Logo\]](#)