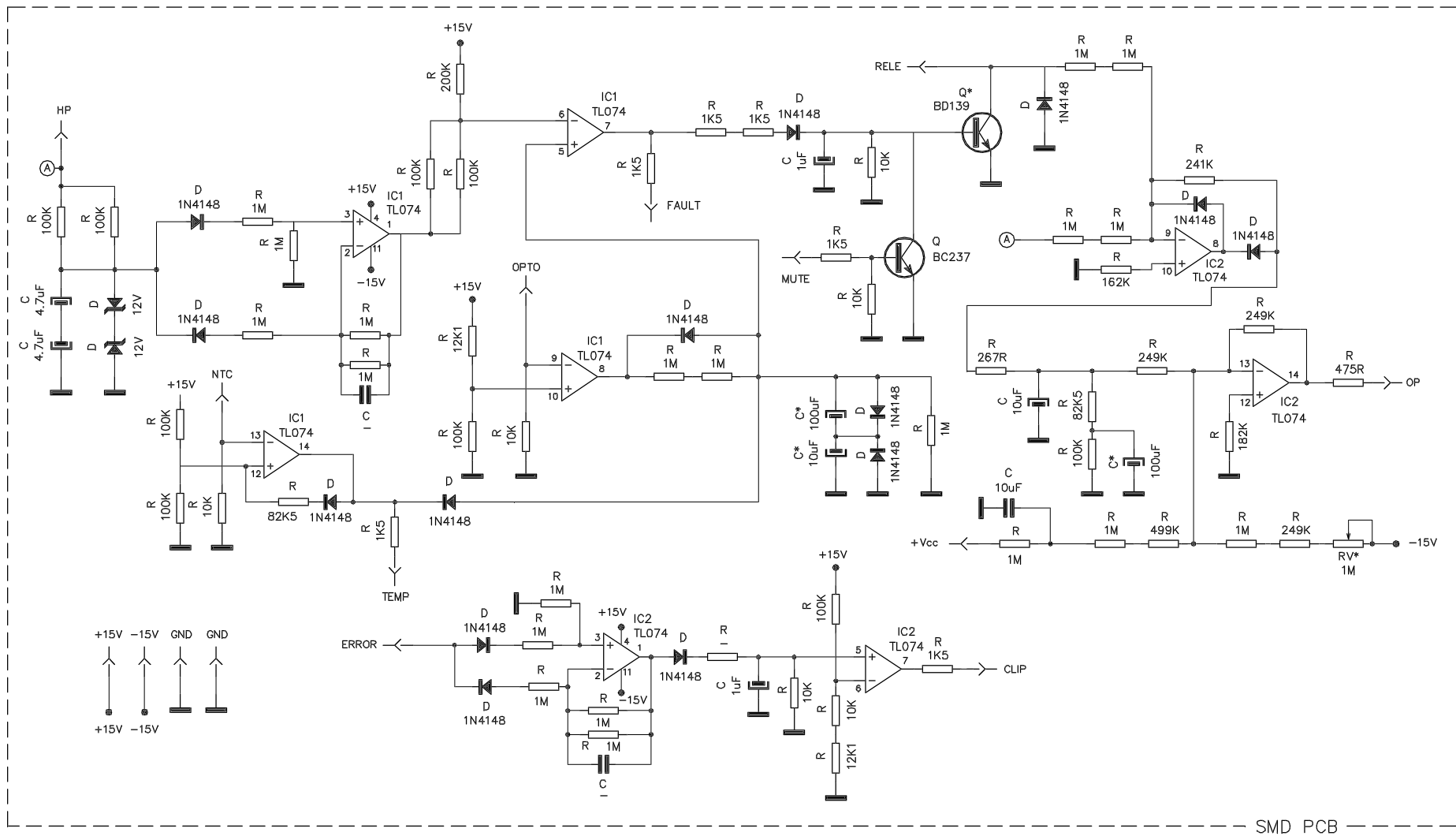


MAIN PCB NOTES

1. All resistors 1/4W, 1% unless noted.
2. All capacitors 50V unless noted.
3. All diodes 1N4148 unless noted.
4. CH-B identical to CH-A.

DRAWN	DATE	<div> </div>	
DESIGN	3/00		
REPLACE	2/00	REF.	
REPLACED BY		MAIN & INPUT PCB	
		SIZE	DWG NO.
		A4	SCHBUX.DWG
			SHEET 1 OF 4



SMD PCB

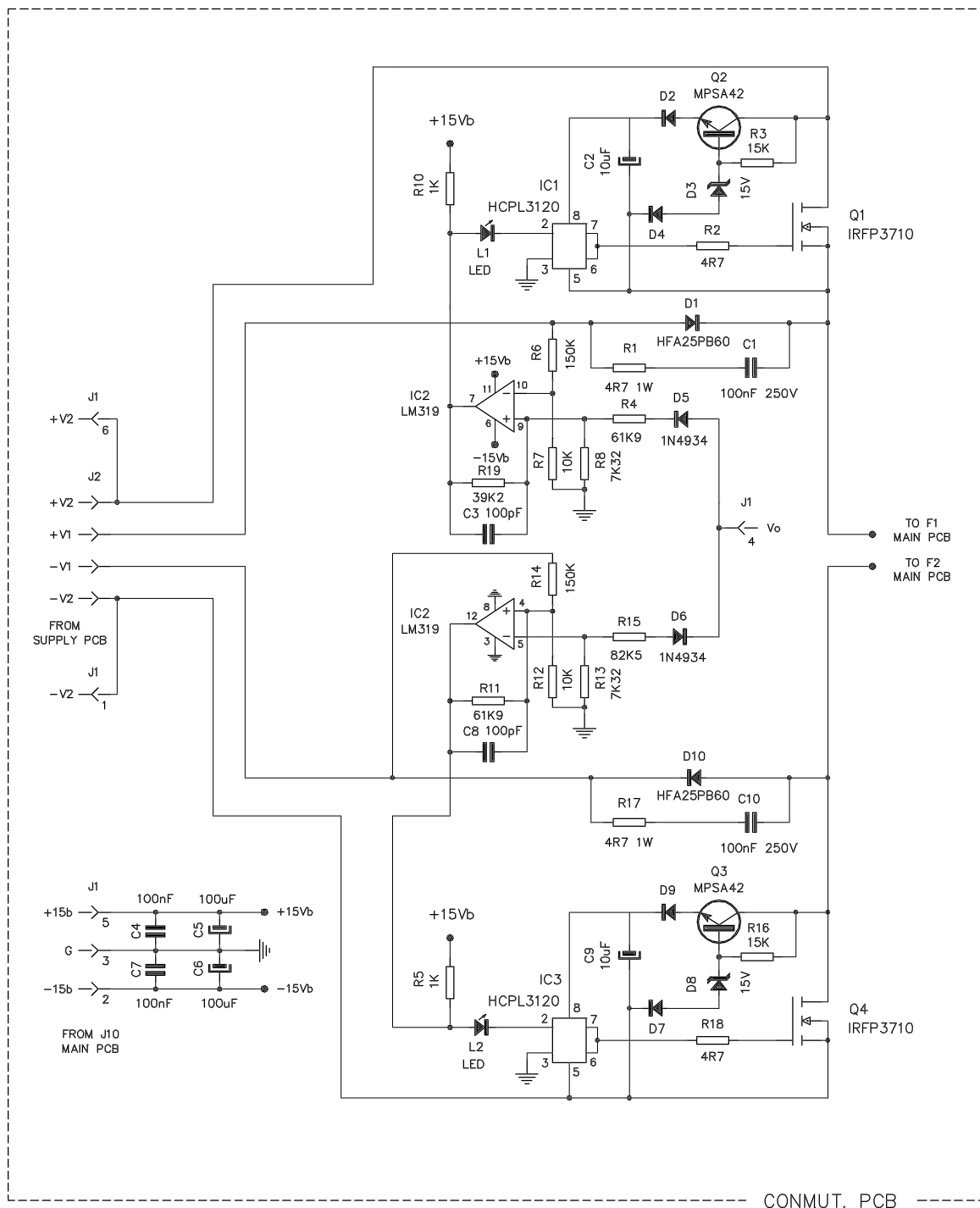
SMD PCB NOTES

- * Ref are non SMD components.
- MAIN to SMD PCB connectors signal names:

J4	J5	J6	J7
HP	TEMP	+15V	ERROR
FAULT	NTC	-15V	GND
MUTE	OPTO	OP	GND
RELE		+VCC	CLIP

DRAWN		DATE	3/00	RAM BUX SERIES
DESIGN			2/00	
REPLACE				REF.
REPLACED BY				SMD PCB
		SIZE	DWG NO.	
		A4	SCHBUX.DWG	SHEET 2 OF 4

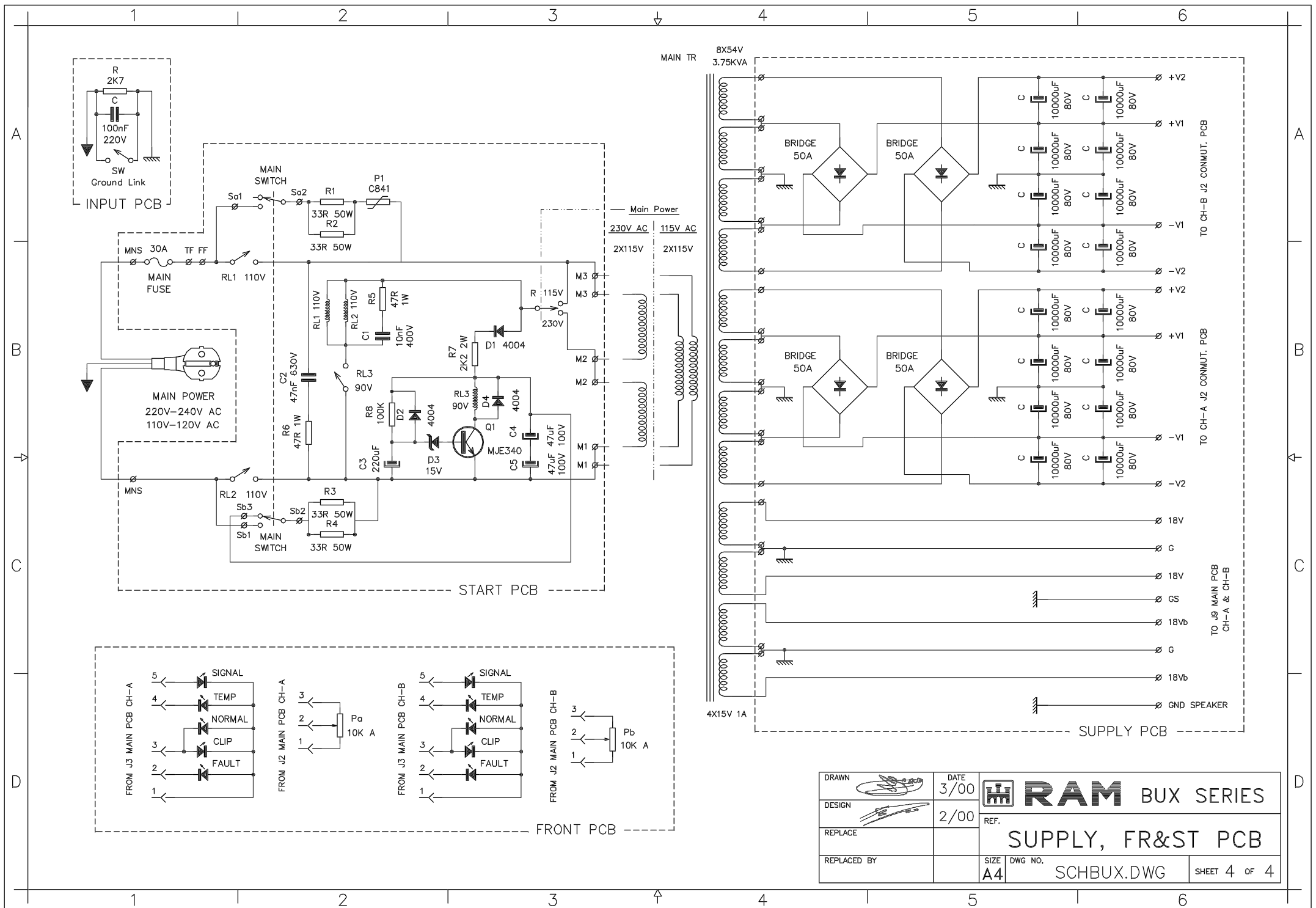
REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED



DRAWN	DATE	 RAM BUX SERIES		
DESIGN	3/00			
REPLACE	2/00	CONMUTATION PCB		
REPLACED BY				
		SIZE	DWG NO.	SHEET 3 OF 4
		A4	SCHBUX.DWG	

CONMUT. PCB NOTES

1. All resistors 1/4W, 1% unless noted.
2. All capacitors 50V unless noted.
3. All diodes 1N4148 unless noted.



DRAWN		DATE	3/00	RAM BUX SERIES REF. SUPPLY, FR&ST PCB SIZE DWG NO. SCHBUX.DWG SHEET 4 OF 4
DESIGN		2/00		
REPLACE				
REPLACED BY				