

# Schematic

quasi rmos 350 20060506

John L. Males 20061215 15:30 added Balanced XLR Input

Transistors T6, T7, T9 & T10 must be mounted on a small common heatsink.

Output FETs are mounted on the main heatsink.

Transistor T8 is mounted on the main heatsink as close as possible to the output FETs

