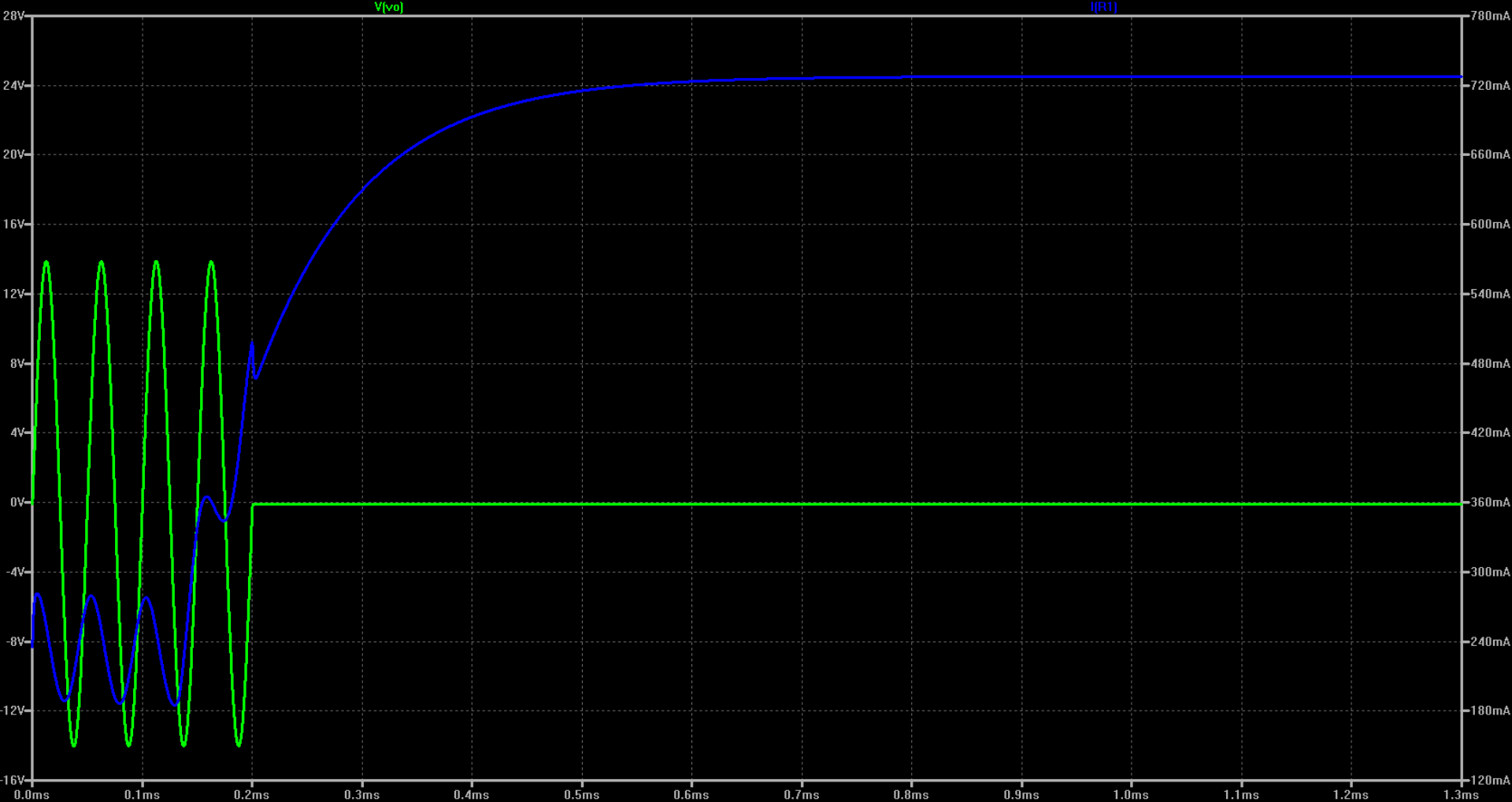


```
.step param Rset 200 1e6 999800
.options plotwinsize= 0
.tran 0 200e-6 0 1e-9
;ac dec 100 1 100e6
.fourier 20k V(Vo)
.step param Rfb 100 300 50
SINE(0 1 20e3 0 0 0 4)
.options numdgt= 15 ;dc V1 +1 -1 1e-3
.step temp list 0 20 80
```



- This plot shows the attack time of the plateau bias is about 200us (green trace is output voltage)
- The nominal output stage bias current moves from c. 240mA to 700mA. A lower figure could also be easily set – e.g. 100mA if required.
- Not there is no signal modulation in this simulation after 200us – hence the flat output bias current (blue trace)



- The high bias condition - which is what will be happening whilst music is playing and the signal is at least 2 or 3 Vpk~pk – comes to an end about 150ms after the output signal level drops
- The attack and rest slope for the bias current are smooth and free of any parasitics or switching anomalies