

## Pizzicato input stage Prot\_2a component variation test

Note: No pot limiter resistors.  
Use single-turn so setting can be determined

;.step temp -5 45 10  
.param tempnom 27

```
.param Freq=20K
.param numcyc=10
.param dlycyc=20
.param FFT=2**16
.param simtime=numcyc/Freq+dlytime
.param dlytime=dlycyc/Freq
.param timestep=(simtime-dlytime)/FFT
four (Freq) V(out)
;.four (Freq) 6 V(Vin) V(out)

.options plotwinsize=0
.options numdgt=15

.ac oct 500 10 800Meg
;tran 0 {simtime} {dlytime} {timestep}
```

