

PF5102 IDSS



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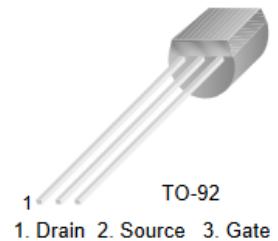


PF5102

N-Channel Switch

Features

- This device is designed for low level analog switching, sample and hold circuits and chopper stabilized amplifiers.
- Sourced from process 51.
- See J111 for characteristics.



Absolute Maximum Ratings* $T_a = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Value	Units
V_{DG}	Drain-Gate Voltage	40	V
V_{GS}	Gate-Source Voltage	-40	V
I_{GF}	Forward Gate Current	50	mA
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150	°C

9VDC

PF5102 IDSS test

$R_1 = 100\text{ohm}$
 $1\text{VDC} = 10\text{mA}$

PF5102 between 0.6 – 0.8VDC for Salas DCSTB

