

CCHD-575 has much worse phase noise compared to NDK SD oscillator. NOTE: this is an ordinary SD type I'm talking about here.

NDK specifies the phase noise performance starting at 1Hz.

Crystek specifies the phase noise performance starting at 10Hz. Specifying the performance at 1Hz would be too embarrassing...

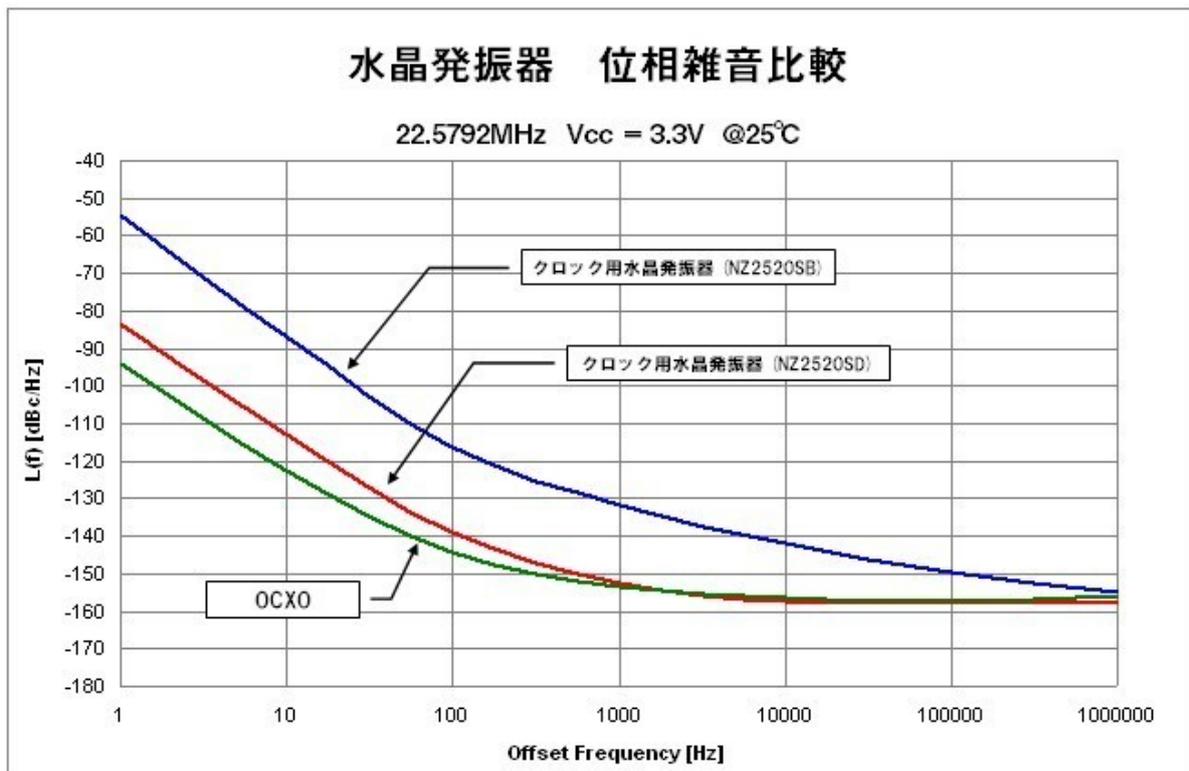
10Hz NDK SD: -112.5dBc/Hz

10Hz Crystek CCHD-575: -90.1dBc/Hz

Note, once again: this is "ordinary" SD performance specs listed above – NOT an SDA.

SDA should have the phase noise improvement of approximately -10dBc/Hz across the whole range, over an SD.

NDK:



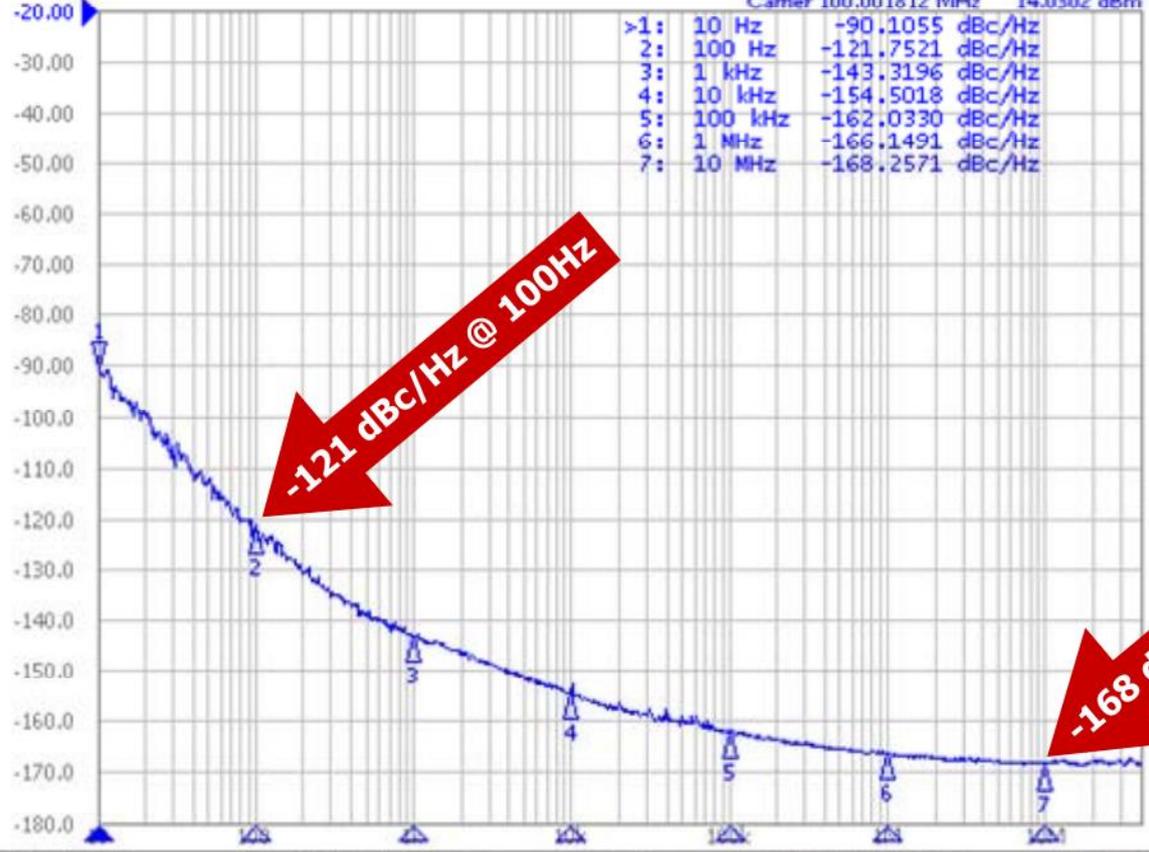
Agilent E5052A Signal Source Analyzer

CCHD-575-100.000

Phase Noise 10.00dB/ Ref -20.00dBc/Hz

Carrier 100.001812 MHz 14.0302 dBm

>1:	10 Hz	-90.1055 dBc/Hz
2:	100 Hz	-121.7521 dBc/Hz
3:	1 kHz	-143.3196 dBc/Hz
4:	10 kHz	-154.5018 dBc/Hz
5:	100 kHz	-162.0330 dBc/Hz
6:	1 MHz	-166.1491 dBc/Hz
7:	10 MHz	-168.2571 dBc/Hz



IF Gain 20dB Freq Band [99M-1.5GHz] Omit LO Opt [<150kHz] 853pts

Phase Noise Start 10 Hz Stop 40 MHz 16/16