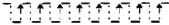
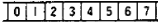


● Terminal Names and Functions

Terminal No.	Terminal Code	Terminal Name	I/O	Reset	Explanation
1	Vcc	—	—	—	GND
2	XTAL	—	—	—	(OPEN)
3	EXTAL	—	—	—	Internal clock circuit output
4	MPO	—	Input	—	+ 5V
5	MP1	—	Input	—	+ 5V
6	$\overline{\text{RES}}$	—	Input	—	CPU reset input Reset RUN
7	$\overline{\text{STBY}}$	—	Input	—	CPU standby input Stand-by RUN
8	$\overline{\text{NIMI}}$	SCOR	Input	—	Sub code synch input Synch
9	P20	LOK	Input	—	Focus OK NG OK
10	P21	$\overline{\text{XLT}}$	Ooutput	H	LSI control data deep pulse Execute
11	SCLK	CLK	Output	H	Serial transmission clock 
12	Rx	SUBQ	Input	—	Sub-code Q data input
13	Tx	DATA	Output	L	Serial data output 
14	P25	SENS	Input	—	LSI multi-mode input
15	P26	MUTE	Output	H	Muting output (digital section) OFF ON
16	P27	GFS	Input	—	Frame synch lock NG LOCK
17	P50	$\overline{\text{LDON}}$	Output	H	Laser diode ON/OFF ON OFF
18	P51	$\overline{\text{DEMP}}$	Output	H	Deemphasis ON/OFF ON OFF
19	P52	$\overline{\text{TEST}}$	Input	—	Test mode switch input TEST NORMAL
20	P53	Not in use	Output	L	(OPEN)
21	P54	AMUTE	Output	H	Muting output (analog section) OFF ON
22	P55	$\overline{\text{LCCS}}$	Input	—	Local circuit cancel input CANSER NORMAL
23	P56	Not in use	Output	L	(OPEN)
24	P57	Not in use	Output	L	(OPEN)
25	P60	$\overline{\text{JTMS}}$	Input	—	Jump delay time SHORT LONG
26	P61	Not in use	Output	L	(OPEN)
27	P62	Not in use	Output	L	(OPEN)
28	P63	LOAD	Input	—	Loading complete LOAD-IN NOT
29	P64	CLOP	Input	—	Clamp-up complete CLAMP-UP NOT
30	P65	OPEN	Input	—	Tray open complete OPEN NOT
31	P66	CLMP	Input	—	Clamp complete CLAMP NOT
32	P67	Not in use	Output	L	(OPEN)
33	Vcc	—	—	—	+ 5
34	P47	$\overline{\text{ALAT}}$	Output	H	Attenuation level latch pulse output Execute