

## Performance

Output power (with 30V supply)	10W r.m.s.
Sensitivity for full output	430mV
Frequency response at 2W output (-3dB points)	20Hz to 35kHz
Input resistance	90k $\Omega$
Distortion at 10W output (see Fig. 78)	<0.1%
Quiescent current of output transistors	15mA

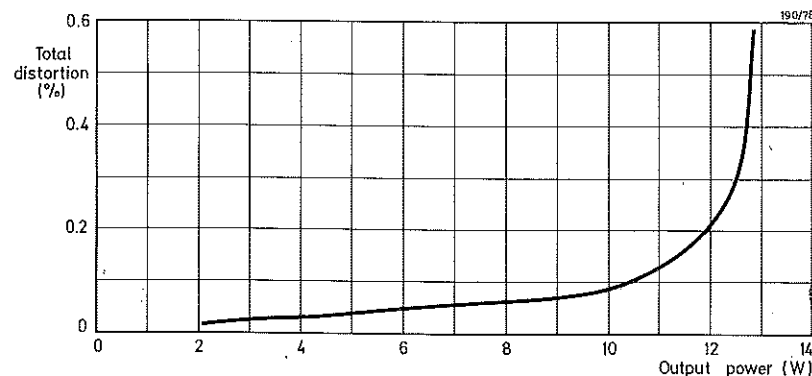


Fig. 78—Variation of total harmonic distortion with output power

## 15/20W AUDIO AMPLIFIER

A high-quality 15/20W audio amplifier circuit is shown in Fig. 79. This amplifier is designed to operate in class A into an 8 $\Omega$  load, giving an output of 15W. With a 4 $\Omega$  load, however, the circuit will operate in class AB to give 20W output power. The total harmonic distortion is less than 0.1% at full output.

The amplifier will withstand normal overdrive conditions and does not require additional protection against short-circuit conditions.

## Circuit Description

The output transistors, a matched pair of BD181 devices, are driven by two BC338 transistors. These transistors are preceded by a phase-splitting stage using two BC147 transistors, and a pre-amplifier stage, a BC158.

A BC158 transistor is used in the pre-amplifier stage. A.C. and d.c. feedback is applied, giving an input impedance of 150k $\Omega$ . This stage also provides stabilisation of the mid-point voltage, which is set by adjustment of the preset resistor R<sub>1</sub>.

Phase splitting is achieved by transistors TR<sub>2</sub> and TR<sub>3</sub> connected in a long-tail-pair configuration, and local feedback is applied by resistor R<sub>11</sub>. A supply voltage higher than that applied to the output stage is necessary to increase the voltage swing available.

The driver transistors TR<sub>4</sub> and TR<sub>5</sub> are bootstrapped by capacitors C<sub>11</sub> and C<sub>12</sub> to reduce the dissipation, especially under overdrive and short-circuit conditions. The output transistors TR<sub>6</sub> and TR<sub>7</sub> are matched BD181 transistors designed for high power dissipation at high supply voltage. Resistors R<sub>20</sub> and R<sub>21</sub> ensure that these transistors can operate up to their V<sub>CER</sub> rating.

## Performance

Input impedance	150k $\Omega$
Input sensitivity	
for 15W output into an 8 $\Omega$ load	360mV
for 20W output into a 4 $\Omega$ load	295mV
Total harmonic distortion	see Fig. 80
Frequency response	
-1dB points at $\frac{1}{4} \times$ full-power output	40Hz to 16kHz
-3dB points at $\frac{1}{4} \times$ full-power output	20Hz to 30kHz
Signal-to-noise ratio, at 50mW output power and source impedance 1k $\Omega$	> 80dB

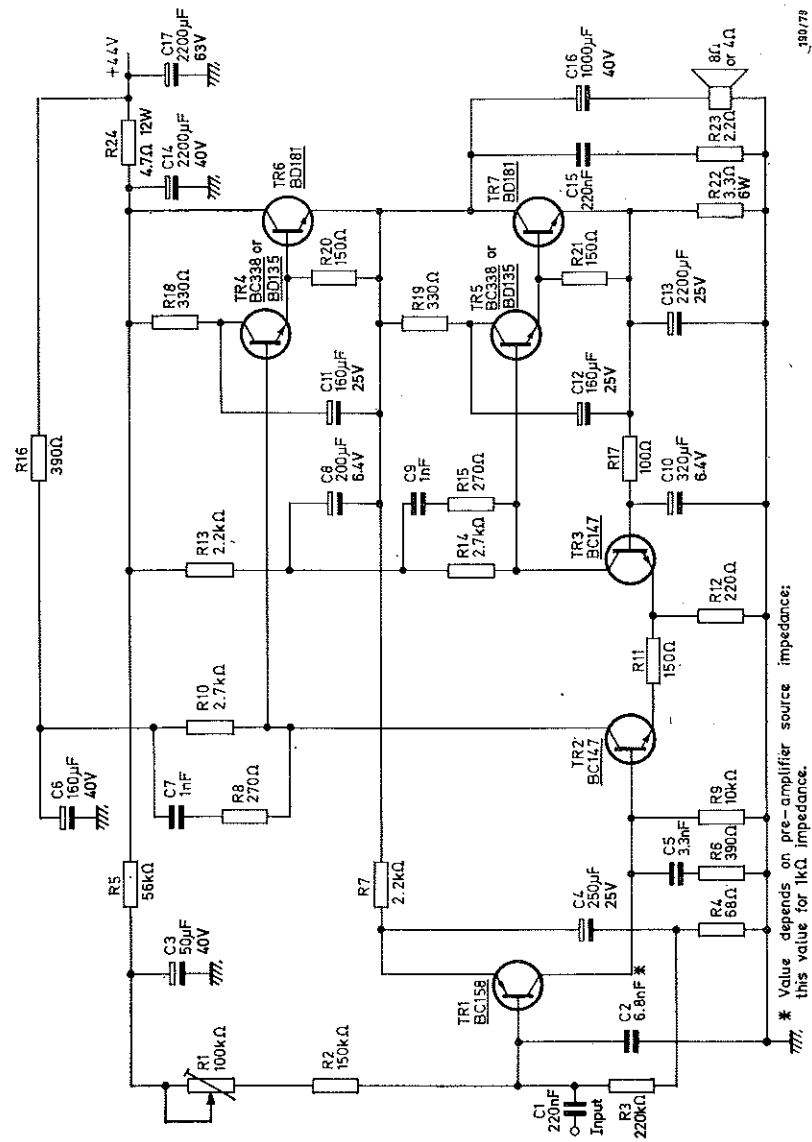


Fig. 79—15/20W high-quality audio amplifier circuit

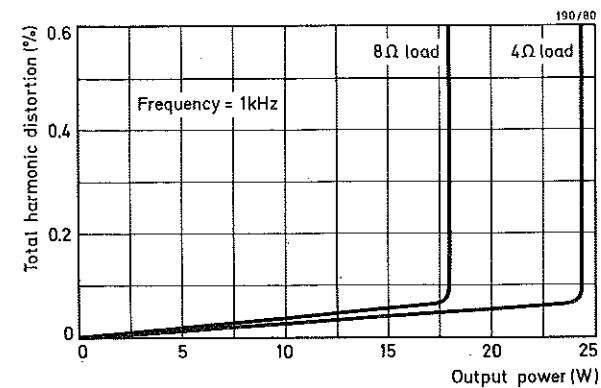


Fig. 80—Variation of total harmonic distortion with output power