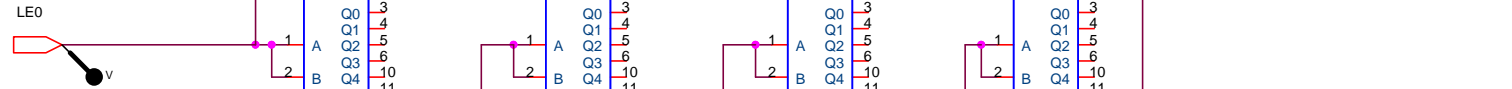
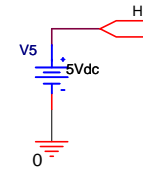
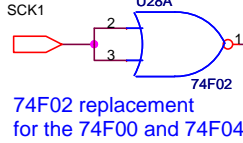


I2S LRCK

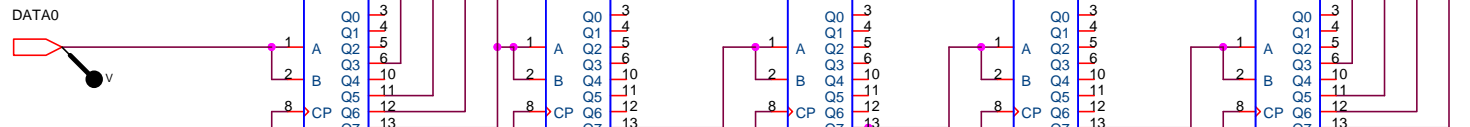


I2S SCK



## I2S to ADxxx and PCMxx DACs

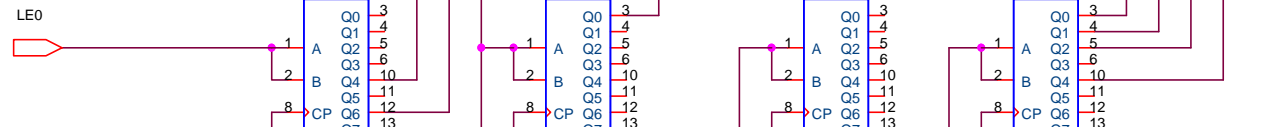
I2S DATA



I2S SCK



I2S LRCK



I2S SCK

