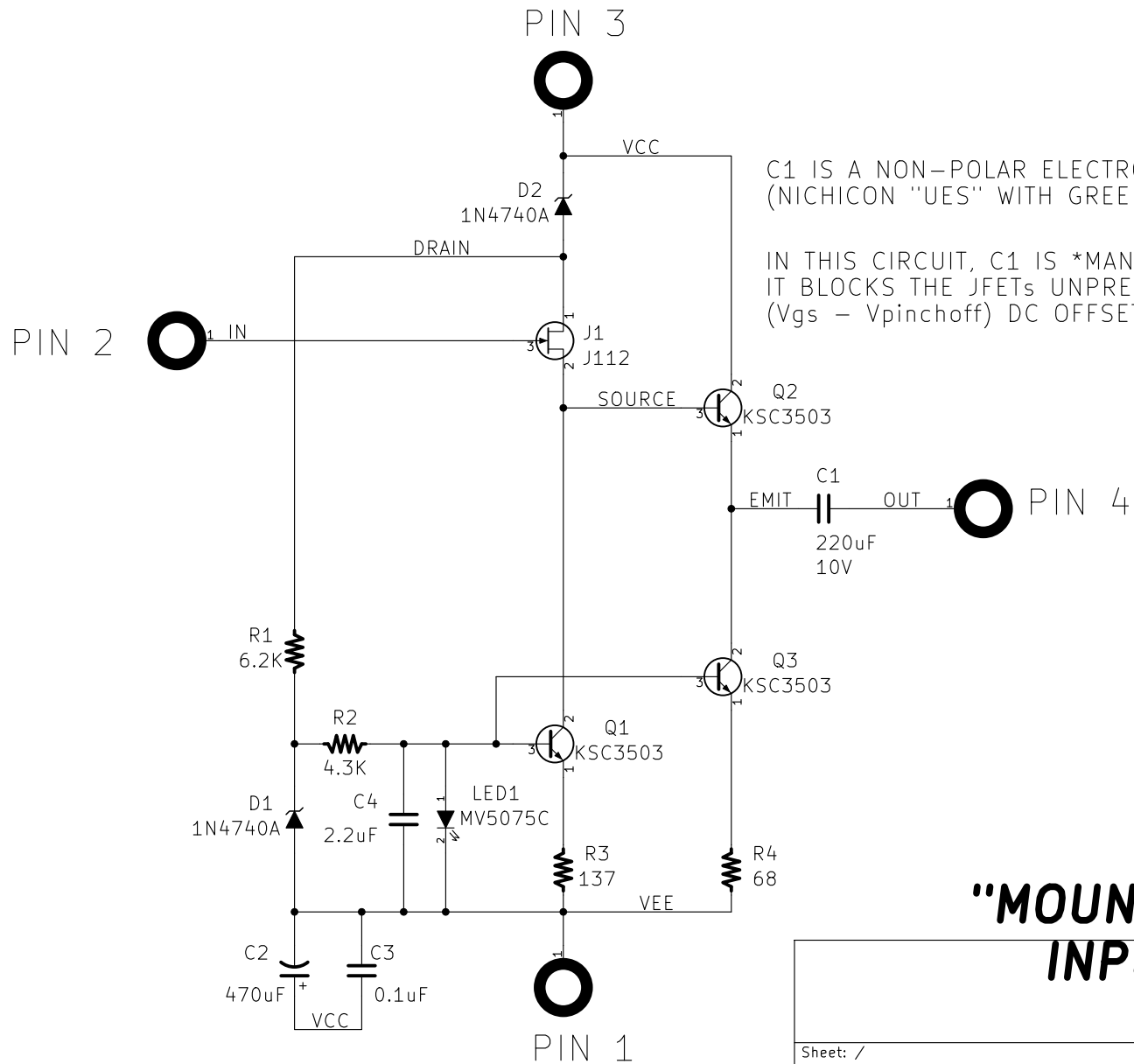


(OFFSET TOWARDS
PIN 4 FOR KEYING)



C1 IS A NON-POLAR ELECTROLYTIC CAPACITOR
(NICHICON "UES" WITH GREEN SLEEVE IS PREFERRED)

IN THIS CIRCUIT, C1 IS *MANDATORY*
IT BLOCKS THE JFETs UNPREDICTABLE
($V_{gs} - V_{pinchoff}$) DC OFFSET VOLTAGE

"MOUNTAIN VIEW" INPUT STAGE V.1

31 March 2018

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