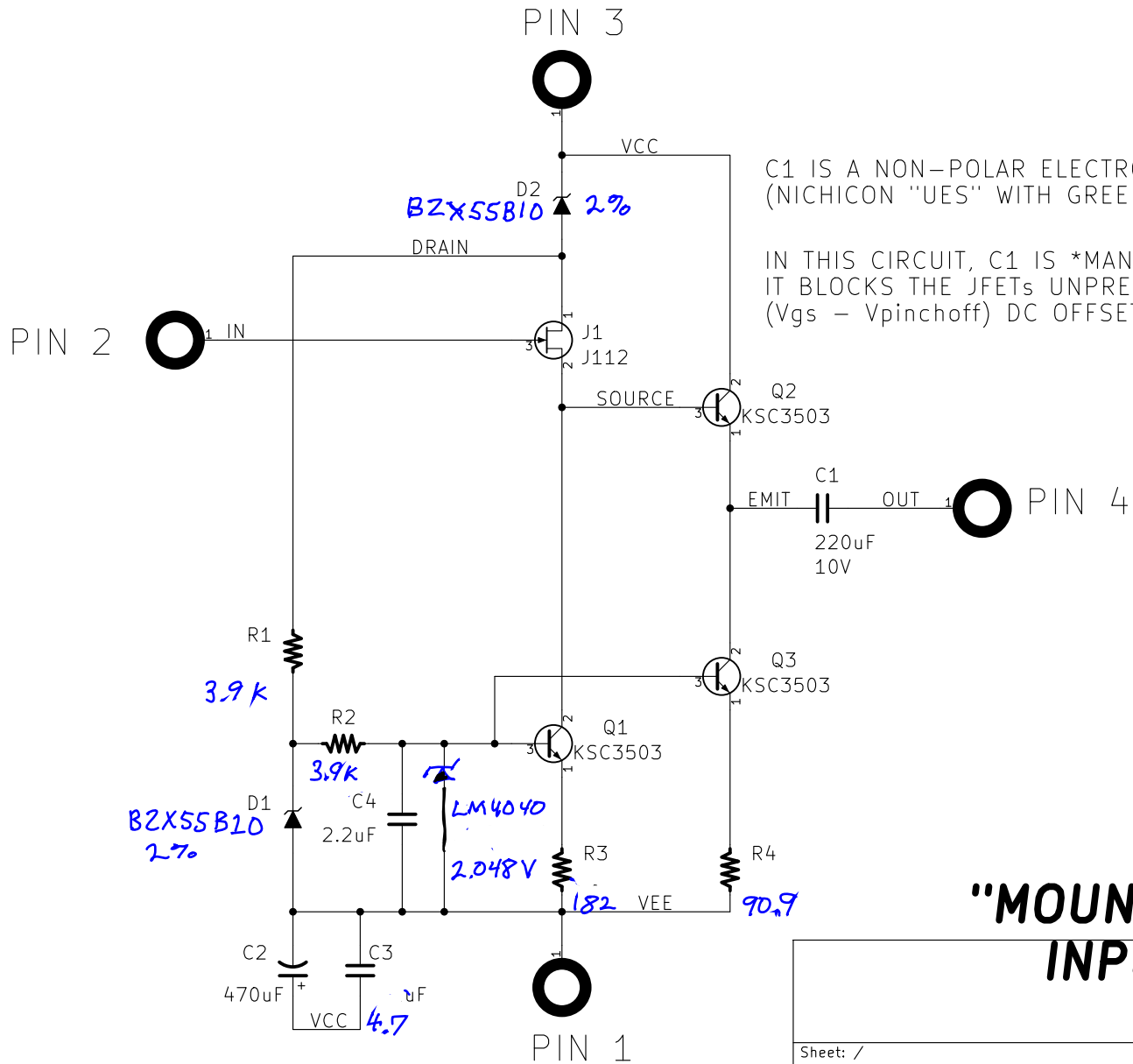


(OFFSET TOWARDS  
PIN 4 FOR KEYING)



C1 IS A NON-POLAR ELECTROLYTIC CAPACITOR  
(NICHICON "UES" WITH GREEN SLEEVE IS PREFERRED)

IN THIS CIRCUIT, C1 IS \*MANDATORY\*  
IT BLOCKS THE JFETs UNPREDICTABLE  
(Vgs - Vpinchoff) DC OFFSET VOLTAGE

## "MOUNTAIN VIEW" INPUT STAGE V.2

31 March 2018

Sheet: /		
File: M2_ips_j109.sch		
Title:		
Size: USLettr	Date:	Rev:
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