Above is a schematic showing a pair of J113 Jfets used as a buffer for crossover filters. The two devices have been matched and then a resistor R1 has been used to set the bias current and another resistor to load the output of the buffer at a specific value. Between the characteristics of the Jfets and the values of these two resistors (plus choice of supply voltages) a "sweet spot" can be found which optimizes the circuit for lower distortion. (for lots more discussion of the sweet spot check out http://www.firstwatt.com/pdf/art_sweet_spot.pdf)