

**SANYO**

No.1636D

**LC7818**

Function Switch

**Use**

Function switchover of amplifier, receiver, etc. and tape monitor control

**Features**

- (1) 2-channel 5-position source select + tape monitor on chip
- (2) Control input pins of input/output common type (Key input and LED display)
- (3) Delivers audio muting control signal.
- (4) Possible to select operation modes of backup mode, initialization mode, automatic switchover of function
- (5) Supply voltage  $\pm 20V$ , single-supply operation available

**Absolute Maximum Ratings at  $T_a = 25^\circ C, V_{SS} = 0V$** 

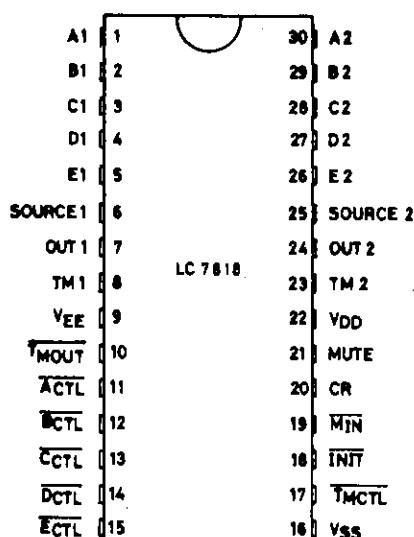
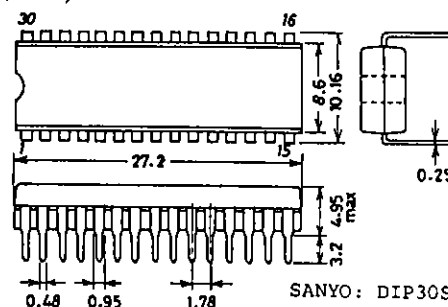
Maximum Supply Voltage	$V_{DD} \text{ max}$	$V_{DD}$	$V_{EE} \leq V_{SS}$	$V_{SS} - 0.3 \text{ to } +20$	unit
	$V_{EE} \text{ max}$	$V_{EE}$		$-20 \text{ to } V_{SS} + 0.3$	V
Output Voltage	$V_{OUT}$	$\frac{ACTL \text{ to } ECTL}{TMOUT}$		$V_{SS} - 0.3 \text{ to } V_{DD} + 0.3$	V
Output Current	$I_{OUT}$	"		30	mA
Voltage Difference at Analog Switch-ON Mode	$\Delta V_{on}$	Switch ON		0.5	V
Allowable Power Dissipation	$P_d \text{ max}$	$T_a \leq 85^\circ C$		500	mW
Operating Temperature	$T_{opg}$			$-30 \text{ to } +75$	$^\circ C$
Storage Temperature	$T_{stg}$			$-40 \text{ to } +125$	$^\circ C$

**Allowable Operating Conditions at  $T_a = 25^\circ C, V_{SS} = 0V, |V_{DD}| \geq |V_{EE}|$** 

Supply Voltage	$V_{DD1}$	$V_{DD}$	$V_{DD}-V_{EE} \geq 12V$	$V_{SS} + 6$	$V_{SS} + 18.5$	V
	$V_{EE}$ <td><math>V_{EE}</math><td></td><td><math>V_{SS} - 18.5</math><td><math>V_{SS}</math></td><th>V</th></td></td>	$V_{EE}$ <td></td> <td><math>V_{SS} - 18.5</math><td><math>V_{SS}</math></td><th>V</th></td>		$V_{SS} - 18.5$ <td><math>V_{SS}</math></td> <th>V</th>	$V_{SS}$	V
	$V_{DD2}$ <td><math>V_{DD}</math><td><math>V_{EE} \leq V_{SS}</math> backup</td><td><math>V_{SS} + 3</math></td><td><math>V_{SS} + 18.5</math></td><th>V</th></td>	$V_{DD}$ <td><math>V_{EE} \leq V_{SS}</math> backup</td> <td><math>V_{SS} + 3</math></td> <td><math>V_{SS} + 18.5</math></td> <th>V</th>	$V_{EE} \leq V_{SS}$ backup	$V_{SS} + 3$	$V_{SS} + 18.5$	V

**Pin Assignment**

Continued on next page.

**Package Dimensions 3047A**  
(unit: mm)

# LC7818

Continued from preceding page.

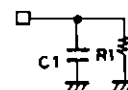
			min	typ	max	unit
Input "H" Level Voltage	$V_{IH}$	$\overline{ACTL}$ to $\overline{ECTL}$ , $\overline{TMCTL}$	$0.7V_{DD}$		$V_{DD}$	V
		$\overline{Min}$	$V_{DD}-1.0$		$V_{DD}$	V
		$\overline{INIT}$	$V_{SS}+3.0$		$V_{DD}$	V
Input "L" Level Voltage	$V_{IL}$	$\overline{ACTL}$ to $\overline{ECTL}$ , $\overline{TMCTL}$	$V_{SS}$	$0.25V_{DD}$		V
		$\overline{Min}$	$V_{SS}$	$V_{SS}+1.0$		V
		$\overline{INIT}$	$V_{SS}$	$V_{SS}+0.5$		V
Input "M" Level Voltage	$V_{IM}$	$\overline{Min}$	$0.45V_{DD}$	$0.55V_{DD}$		V
Analog Switch Input	$V_{IN}$	A1 to E1, A2 to E2	$V_{EE}$		$V_{DD}$	V
Voltage Range		SOURCE1,2 TM1,2				

Electrical Characteristics at  $T_a = 25^\circ\text{C}$ ,  $V_{SS} = 0\text{V}$ ,  $|V_{DD}| \geq |V_{EE}|$

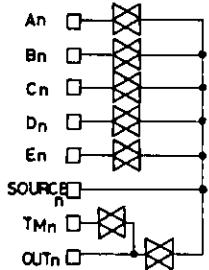
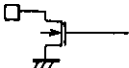
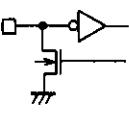
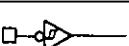


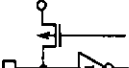
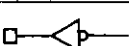
			min	typ	max	unit
Output "H" Level Voltage	$V_{OH}$	MUTE	$I_{OH} = -0.4\text{mA}$ , $V_{DD} \geq 9\text{V}$	$V_{DD}-0.5$	$V_{DD}$	V
Output "L" Level Voltage	$V_{OL1}$	$\overline{ACTL}$ to $\overline{ECTL}$ $\overline{TMOUT}$	$I_{OL} = 30\text{mA}$ , $V_{DD} = 18\text{V}$	0	2	V
	$V_{OL2}$	MUTE	$I_{OL} = 0.4\text{mA}$ , $V_{DD} \geq 9\text{V}$	0	0.5	V
Analog Switch-ON Resistance	$R_{on}$	A1 to E1, A2 to E2 TM1, TM2	$I = 1\text{mA}$ , $V_{DD} - V_{EE} = 12\text{V}$ $I = 1\text{mA}$ , $V_{DD} - V_{EE} = 18\text{V}$	120 80		$\Omega$
		OUT1, OUT2	$I = 1\text{mA}$ , $V_{DD} - V_{EE} = 37\text{V}$	70		$\Omega$
Input/Output OFF Leak Current	$I_{OFF1}$	$\overline{ACTL}$ to $\overline{ECTL}$ $\overline{TMOUT}$	$V_0 = V_{SS} + 18\text{V}$		10	$\mu\text{A}$
	$I_{OFF2}$	CR	$V_0 = V_{SS} + 18\text{V}$		1	$\mu\text{A}$
	$I_{OFF3}$	A1 to E1, A2 to E2 TM1, 2, OUT1, 2	Analog SW OFF $V_{IN} = V_0 = V_{EE} \text{ to } V_{EE} + 37\text{V}$	-1	1	$\mu\text{A}$
Total Harmonic Distortion	THD	SOURCE1, 2 OUT1, 2	$V_{IN} = 1\text{V}_{rms}$ , $f = 1\text{kHz}$ , $V_{DD} - V_{EE} = 15 \text{ to } 37\text{V}$	0.0015	0.01	%
Feedthrough	FTH	A1 to E1 SOURCE1 OUT1 A2 to E2 SOURCE2 OUT2	$V_{DD} - V_{EE} = 37\text{V}$ , $f = 10\text{kHz}$ $V_{IN} = 0.77\text{V}_{rms}$ $RL = 47\text{k}\Omega$	55		dB
Crosstalk	CT	A1 to E1 SOURCE2 OUT2 A2 to E2 SOURCE1 OUT1	$V_{DD} - V_{EE} = 37\text{V}$ , $f = 10\text{kHz}$ $V_{IN} = 0.77\text{V}_{rms}$ $RL = 47\text{k}\Omega$	75		dB
Current Dissipation	$I_{DD}$	$V_{DD}$ Operating mode	$V_{DD} - V_{EE} = 37\text{V}$		1	mA
Muting Time	$T_M$	MUTE		OSC period x 21		
Input Accept Pulse Width (Switch Select)	$T_{IN(1)}$	$\overline{ACTL}$ to $\overline{ECTL}$ $\overline{TMCTL}$		OSC period x 3		
Input Accept Pulse Width (Muting Output)	$T_{IN(2)}$	$\overline{ACTL}$ to $\overline{ECTL}$ $\overline{TMCTL}$		OSC period x 1		
External Capacitance for CR OSC	C1	CR		0.001	0.1	$\mu\text{F}$
OSC Period	T1	CR	$V_{DD} - V_{SS} = 6\text{V}$	0.4C1R1	0.7C1R1	
	T2	CR	$V_{DD} - V_{SS} = 18.5\text{V}$	0.3C1R1	0.6C1R1	
Current Dissipation	$I_{DD}$ back up $V_{DD}$		back up $V_{DD} = 5\text{V}$ , $V_{EE} = V_{SS} = 0\text{V}$		1	$\mu\text{A}$

Operation caused by combination of INIT, Min inputs

INIT	Min	Operation
H	M	Normal
H	L	Backup
H	H	Auto function
L	M	Muting
L	L	Initialize (A circuit)
L	H	Reset



## Pin Description

Pin Name	Pin No.	Input/Output Configuration	Function
VDD VSS VEE	22 16 9		<ul style="list-style-type: none"> <li>Power supply pin</li> <li>Single supply (+): <math>V_{SS}=V_{EE}=GND</math></li> <li>Dual supply (<math>\pm</math>): <math>V_{SS}=GND, V_{EE}=(-)V</math></li> </ul>
A1, B1 C1, D1 E1, TM1 A2, B2 C2, D2 E2, TM2 SOURCE1 SOURCE2 OUT1 OUT2	1, 2 3, 4 5, 8 30, 29 28, 27 26, 23 6 25 7 24		<ul style="list-style-type: none"> <li>A to E, TM: Audio signal input pin</li> <li>SOURCE: Output pin for REC</li> <li>OUT: Audio signal output pin</li> </ul>
TMOUT	10		<ul style="list-style-type: none"> <li>TM ON/OFF-state display LED driver output</li> </ul>
ACTL BCTL CCTL DCTL ECTL	11 12 13 14 15		<ul style="list-style-type: none"> <li>Input/output pin for analog switch control and its state display LED driver output</li> </ul>
TMCTL	17		<ul style="list-style-type: none"> <li>Input pin for TM control</li> </ul>
INIT	18		<ul style="list-style-type: none"> <li>Input pin for mode setting (Details are given on page 2.)</li> </ul>
MIN	19		<ul style="list-style-type: none"> <li>Input pin for mode setting (Details are given on page 2.)</li> </ul>
CR	20		<ul style="list-style-type: none"> <li>Input/output pin for clock generation</li> <li>C1, R1 are connected.</li> </ul>
MUTE	21		<ul style="list-style-type: none"> <li>Output pin for muting control</li> </ul>

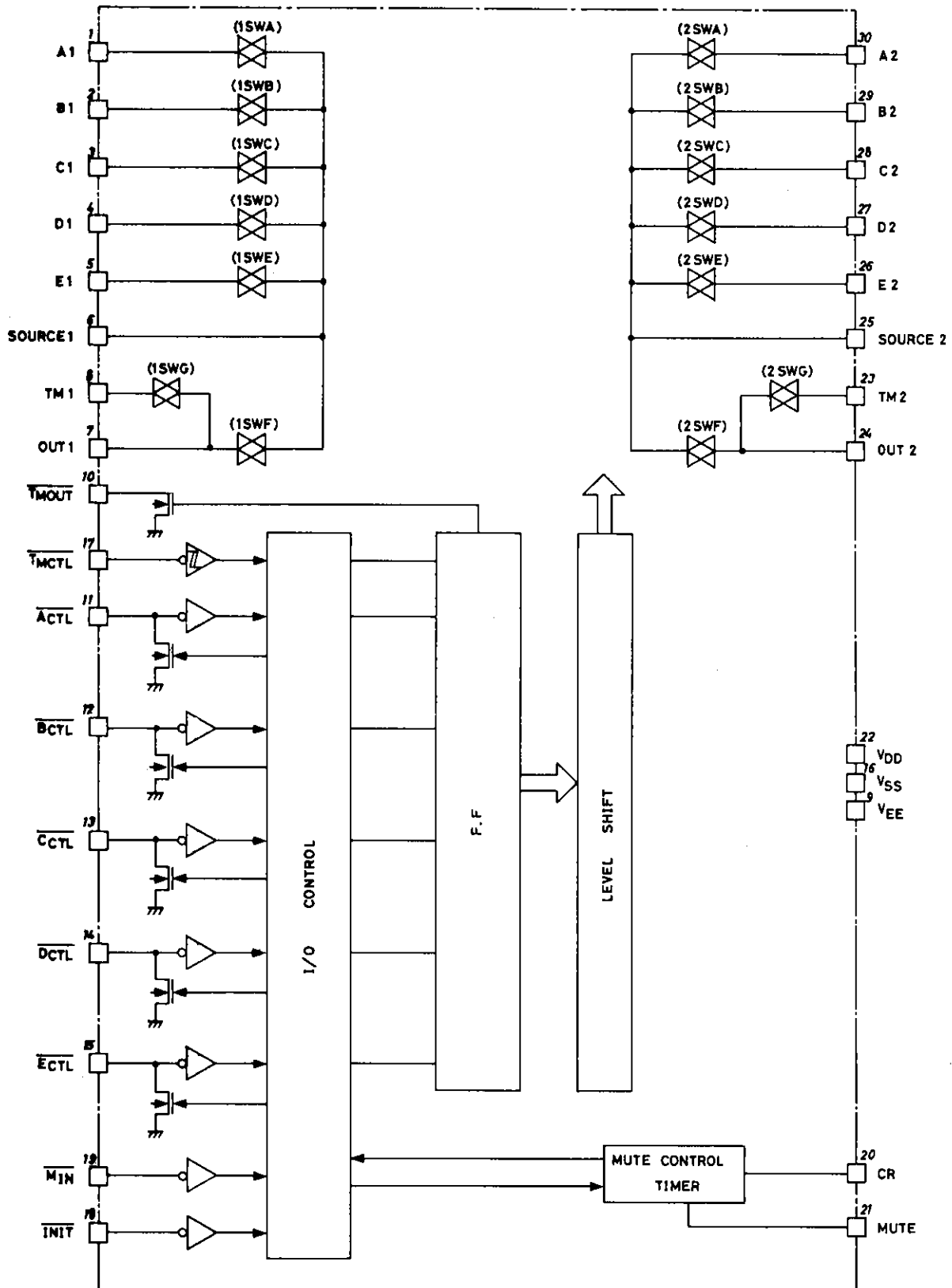
Note: Priority for simultaneous push of keys is given as shown below.

TMCTL > ACTL > BCTL > CCTL > DCTL > ECTL

The pin (ACTL to ECTL pins) whose LED driver is turned ON (function selected) does not accept key input. Key input to such pin causes no operation to occur.

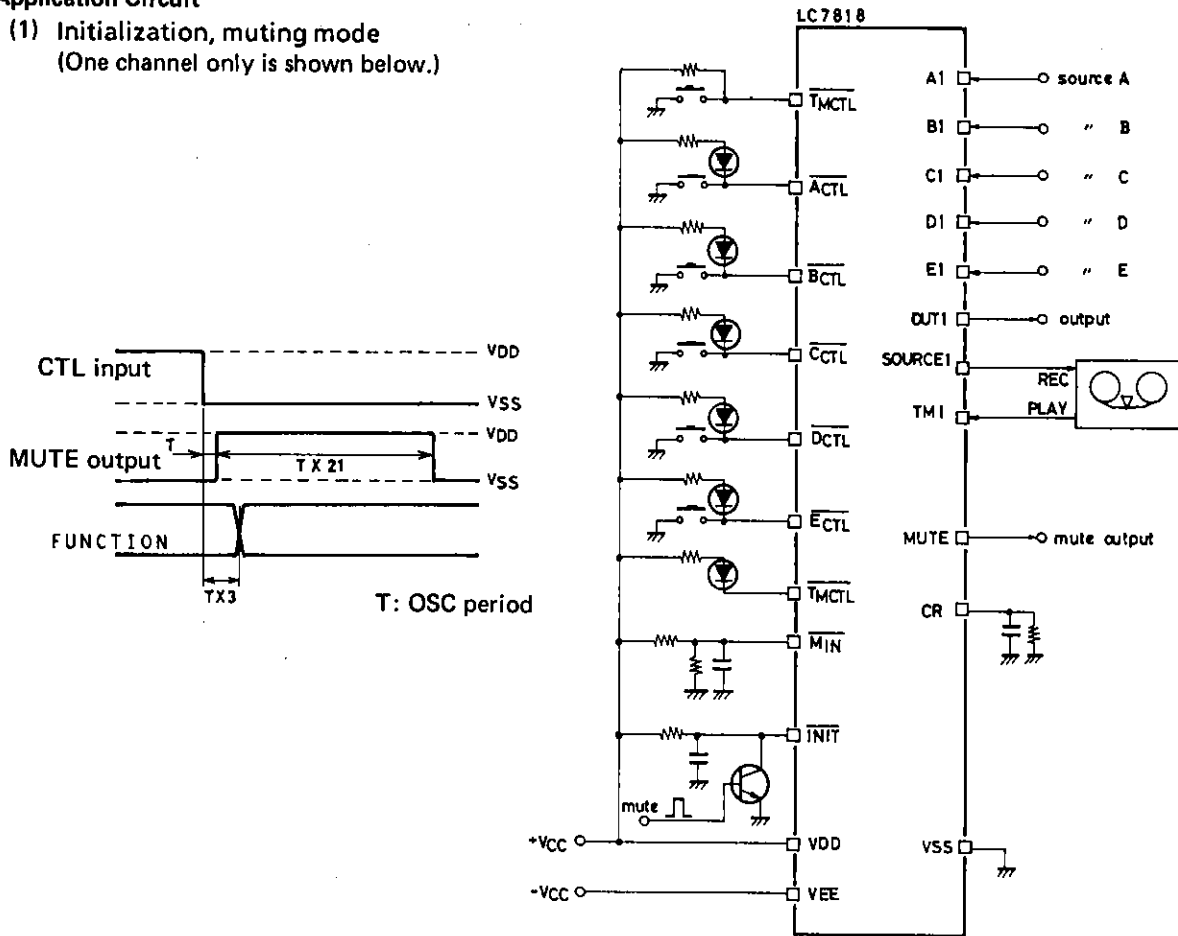
- No products described or contained herein are intended for use in surgical implants, life-support systems, aerospace equipment, nuclear power control systems, vehicles, disaster/crime-prevention equipment and the like, the failure of which may directly or indirectly cause injury, death or property loss.
- Anyone purchasing any products described or contained herein for an above-mentioned use shall:
  - ① Accept full responsibility and indemnify and defend SANYO ELECTRIC CO., LTD., its affiliates, subsidiaries and distributors and all their officers and employees, jointly and severally, against any and all claims and litigation and all damages, cost and expenses associated with such use:
  - ② Not impose any responsibility for any fault or negligence which may be cited in any such claim or litigation on SANYO ELECTRIC CO., LTD., its affiliates, subsidiaries and distributors or any of their officers and employees jointly or severally.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. SANYO believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

Equivalent Circuit Block Diagram



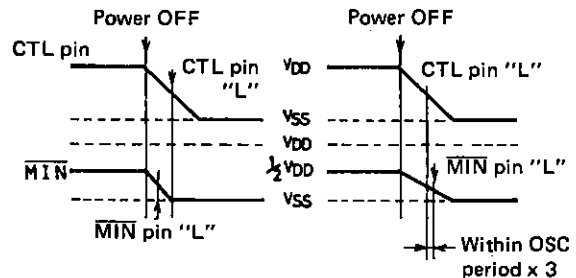
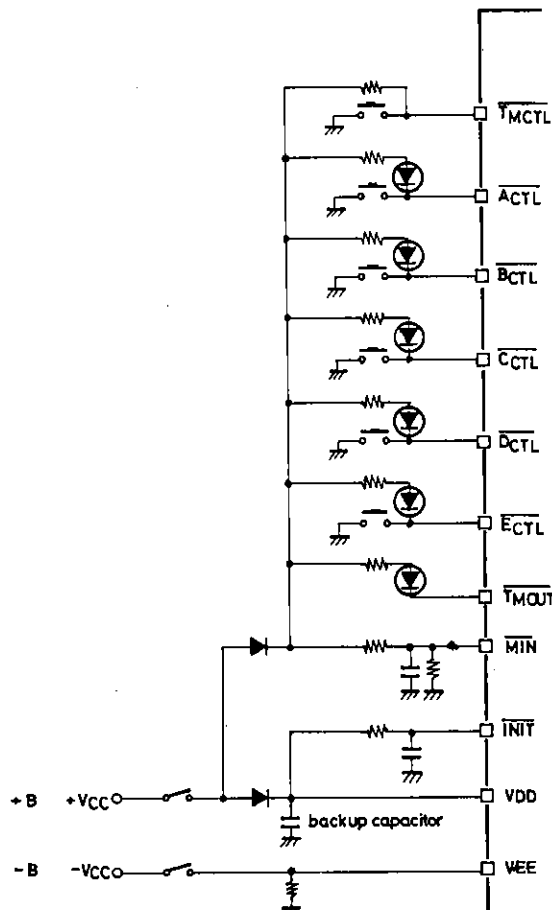
# Application Circuit

- (1) Initialization, muting mode  
(One channel only is shown below.)

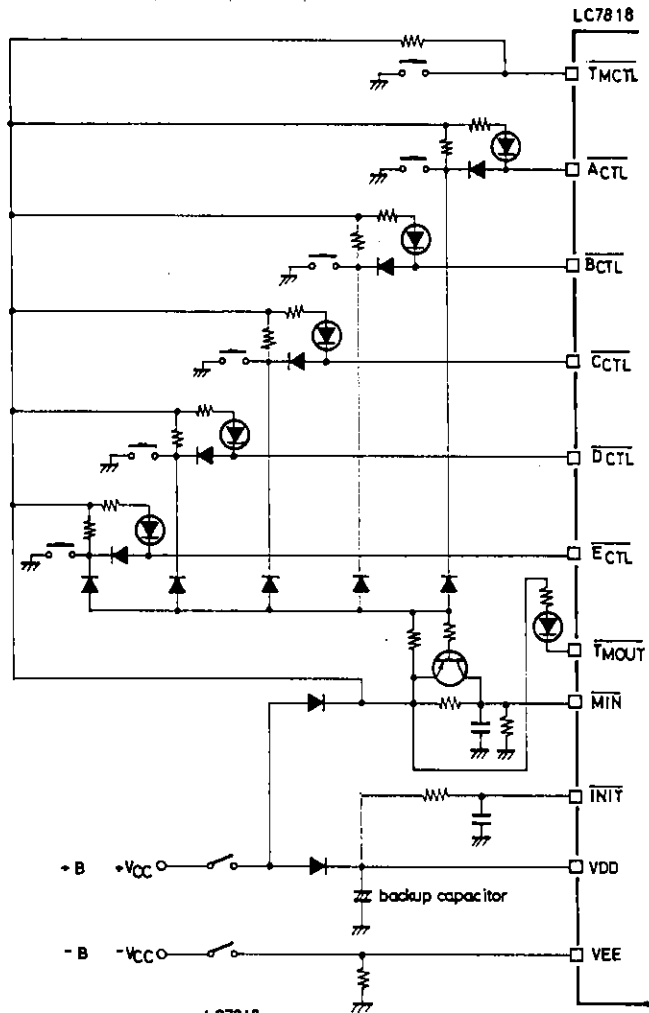


- (2) Backup mode  
(Audio section, MUTE circuit are omitted.)

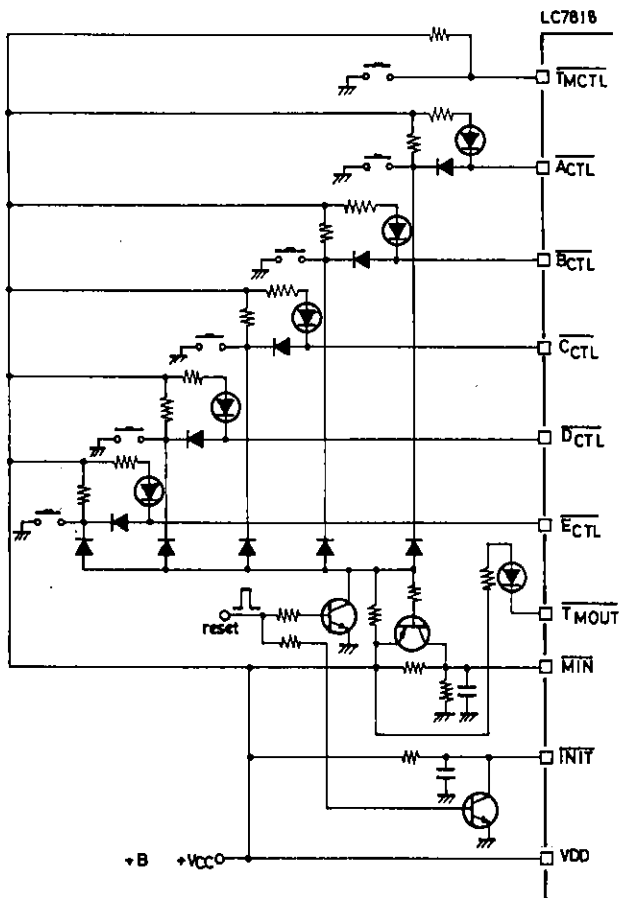
If the power switch is set to the primary side at the backup mode and it takes time for +B to fall when power is turned OFF, the MIN pin must be brought to "L" state before the ACTL to ECTL, TMCTL pins are brought to "L" state or the MIN pin must be brought to "L" state within OSC period x 3 in case the ACTL to ECTL, TMCTL pins are brought to "L" state earlier; otherwise the function may be shifted to another.

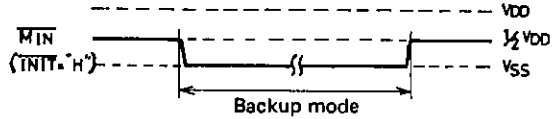
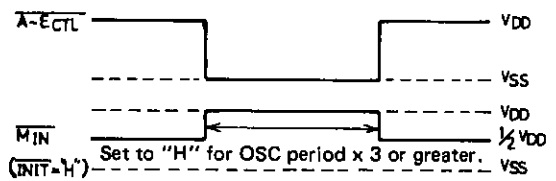
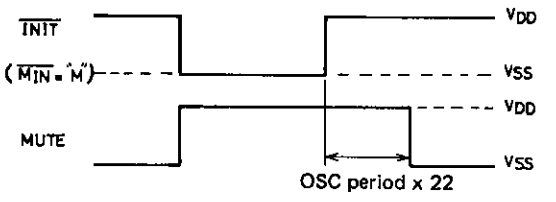



(3) Auto function, backup mode



(4) Auto function, initialization, reset mode



INIT	MIN	Operation	Description
H	M	Normal	<ul style="list-style-type: none"> <li>This state is kept at the normal operation mode.</li> </ul>
H	L	Backup mode	<ul style="list-style-type: none"> <li>The backup mode is entered at this state.</li> </ul> 
H	H	Auto function (TM reset)	<ul style="list-style-type: none"> <li>When the <math>\overline{A-ECTL}</math> to <math>\overline{ECTL}</math> input occurs, set to this state.</li> </ul> 
L	M	Muting	<ul style="list-style-type: none"> <li>When applying muting regardless of the function select key, set to this state.</li> </ul> 
L	L	Initialization (A circuit ON)	<ul style="list-style-type: none"> <li>The TM is turned OFF and the A circuit is turned ON.</li> </ul>  <ul style="list-style-type: none"> <li>To initialize, hold this state for OSC period x 3 or greater.</li> </ul>
L	H	Reset	<ul style="list-style-type: none"> <li>All input circuits are turned OFF.</li> </ul> 