

## Testing (US System)

### Voltage Assessment

Location	Target Reading(?)	My Board	Comments
Z1 Diode	120VAC	? / 120.9 VAC	TP1 This comes straight from the AC Line
TH3	120VAC	? / 120.9 VAC	TP2 Thermistor on backside of the Isolation TF
D6	165VDC (?)	? / 166 VDC	
Big Cap C58			This filters the High voltage from the Bridge. C58+ (square) to TFPin2
PDC D12			Control the voltage from the ACDC bridge
PDC DIAC			Set the voltage to the Fet Gate circuits
PDC Pin5	+15VDC	+	+/-15V to Pin1 on the PDC. Voltage set by the Zeners. This feeds the MOSFet gates to put alternating wave on transformer L2
PDC Pin8	-15VDC	-	
BASH Pin5	12VDC		Pin5 (Vcc) to Pin11 (GnD) (The BASH has full 90V Rail so be careful). If 12V the surrounding circuits are working since this is the power pin.
C31	90~97V		Large filter cap
U4 & U5	25~28.6VDC		Across the heatsinks. These set the +/- ~15VDC If you don't have this the PDC is not working. (Chk DIAC)
U4 & U5	~35V		Check the vRegs in &out. Input (Pin( )) should be ~35V
R70 & R71	~5VDC		2x 150ohm resistors feeding the Pre-Amp circuit. (Bash Board is good). At idle should have 5V w/Vol at 0. If voltage is <1V BASH not working
Spkr Term			