

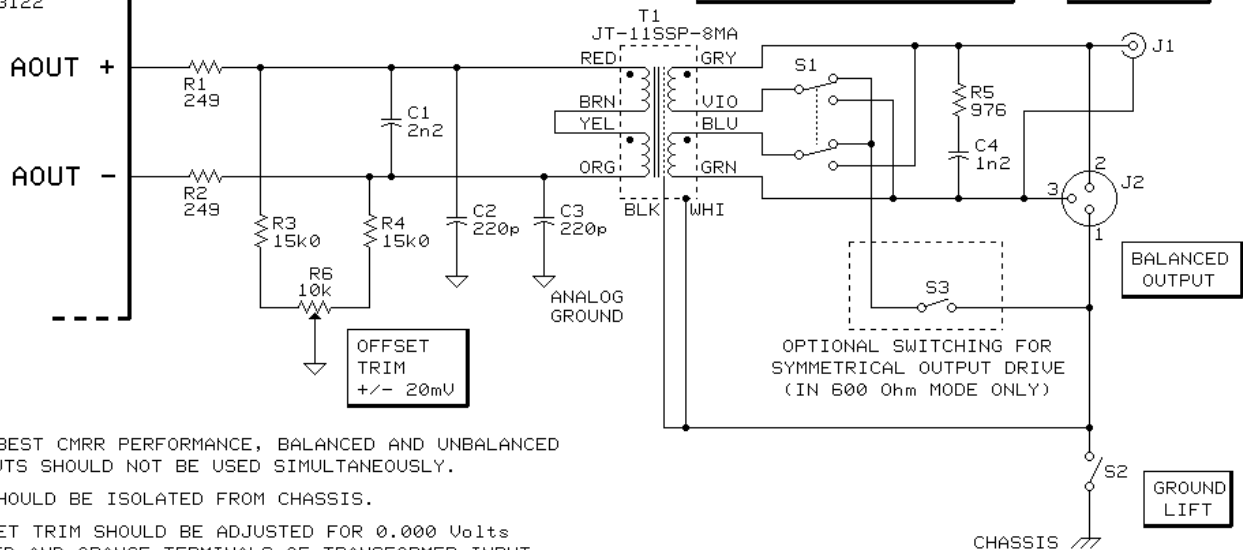
# JT-11SSP-8MA IN PASSIVE OUTPUT CIRCUIT FOR CRYSTAL D/A CONVERTERS

## CRYSTAL

CS4396 or  
CS5396 or  
CS43122

AOUT +

AOUT -



## NOTES:

- 1) FOR BEST CMRR PERFORMANCE, BALANCED AND UNBALANCED OUTPUTS SHOULD NOT BE USED SIMULTANEOUSLY.
- 2) J1 SHOULD BE ISOLATED FROM CHASSIS.
- 3) OFFSET TRIM SHOULD BE ADJUSTED FOR 0.000 Volts AT RED AND ORANGE TERMINALS OF TRANSFORMER INPUT.
- 4) S3 ALLOWS BALANCED, GROUND REFERENCED SYMMETRICAL DRIVE FOR "DUAL SINGLE-ENDED" AMPLIFIERS. SWITCH SHOULD BE "OPEN" FOR NORMAL OPERATION TO INCREASE CMRR.
- 5) GROUND LIFT SHOULD BE "CLOSED" IN NORMAL OPERATION.
- 6) ALL CAPACITORS POLYPROPYLENE OR POLYSTYRENE TYPE.
- 7) 2n2 = 2,200pF / 1n2 = 1,200pF
- 8) R1-R5 ARE 1% METAL FILM TYPES.

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7135 HAYVENHURST AVE.  
VAN NUYS, CALIFORNIA 91406  
(818) 374-5857