

Schematic Diagram

All elements of this schematic in Red are modifications.

DC Voltage readings were taken with no input attached and with 8 ohms on output

All voltage readings are referenced to power supply ground

P4 is adjusted for a voltage reading of .8 volts DC across R44

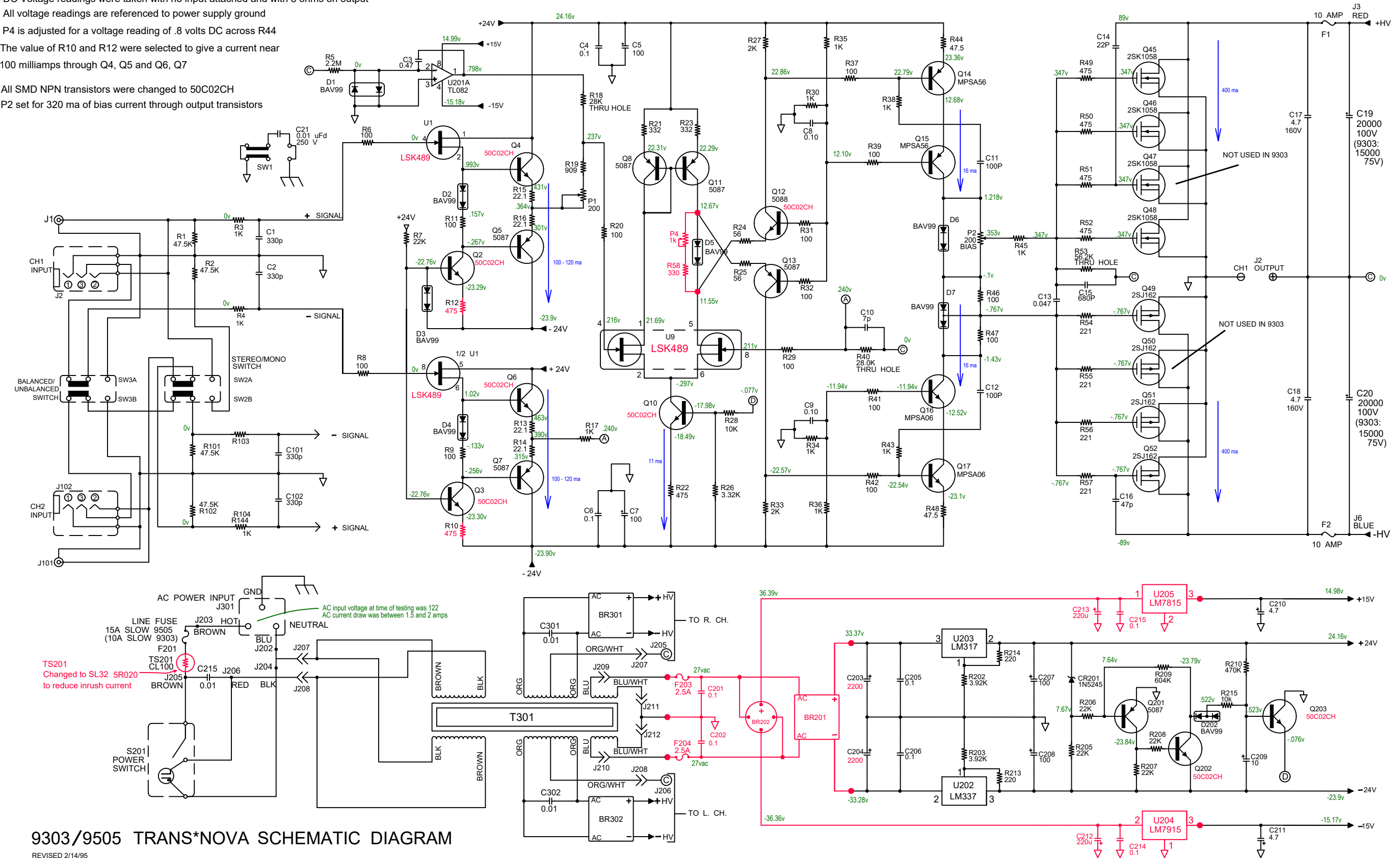
The value of R10 and R12 were selected to give a current near

100 milliamps through Q4, Q5 and Q6, Q7

All SMD NPN transistors were changed to 50C02CH

P2 set for 320 ma of bias current through output transistors

This Schematic is Modified Version 1
Drawn by Engineer Ben October 2019
The sole purpose of this drawing is to aid in repair



9303/9505 TRANS*NOVA SCHEMATIC DIAGRAM
REVISED 2/14/95