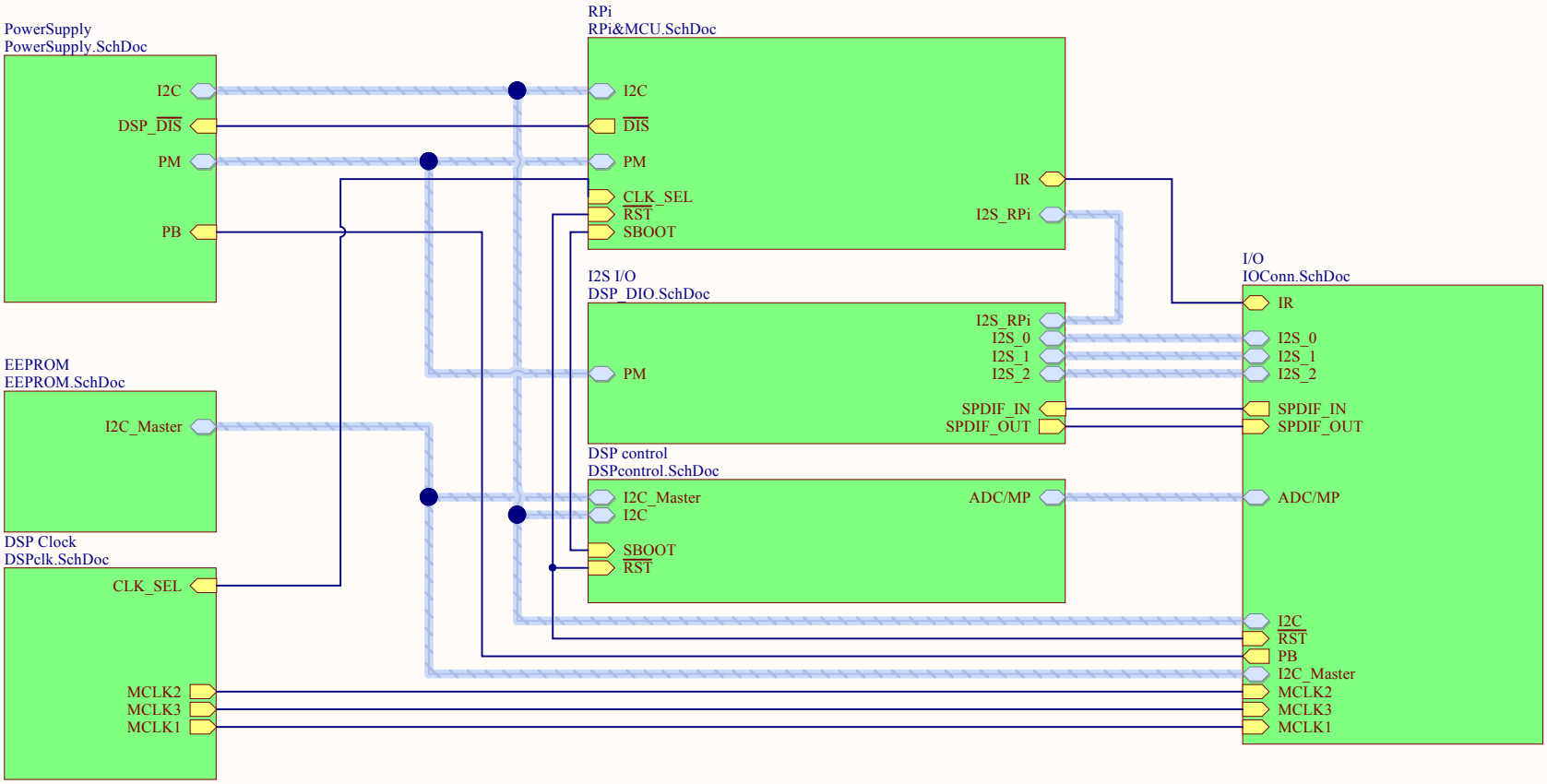


PS Decoupling
DSPPower.SchDoc

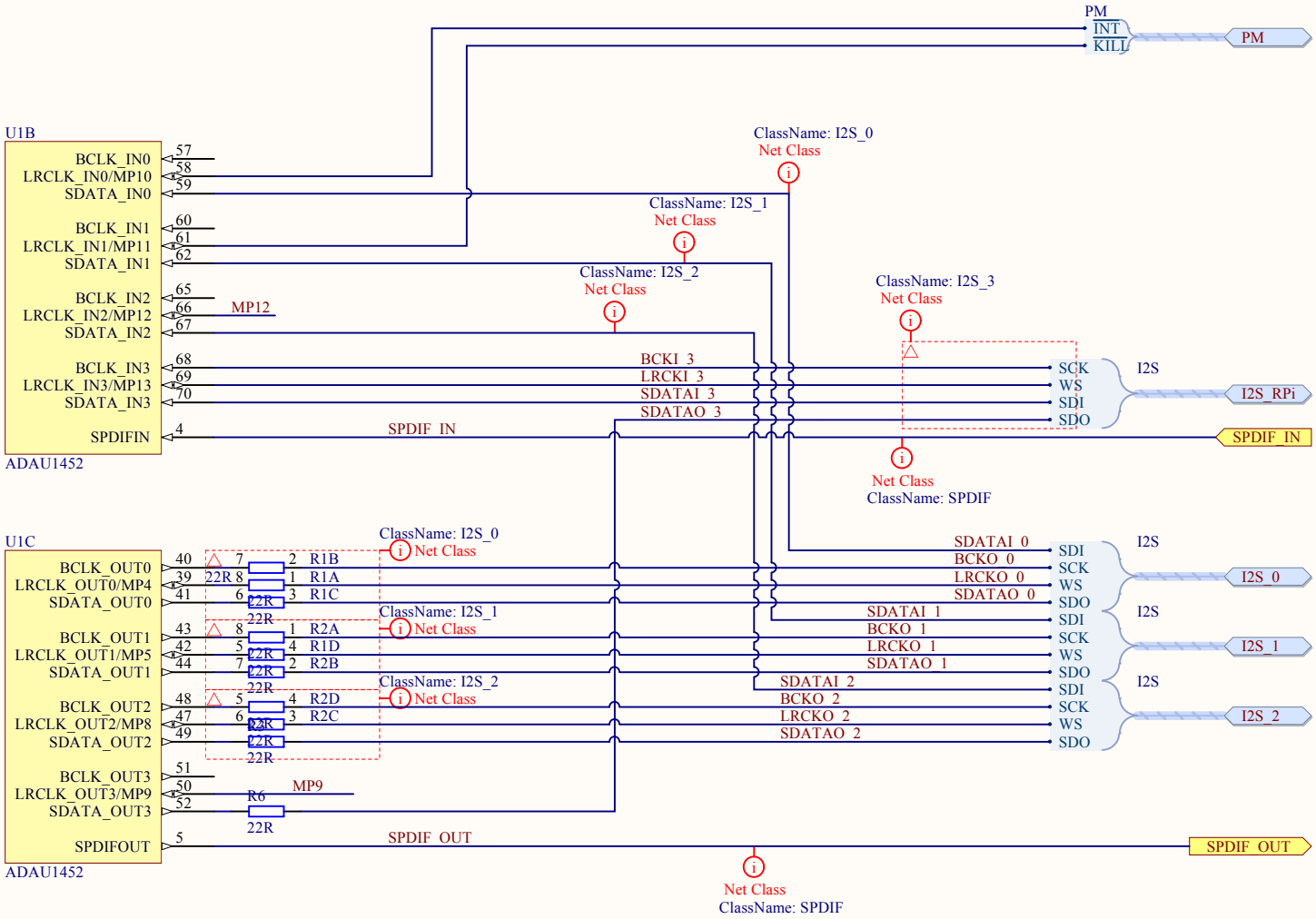
CROSSED
BIN
S1

FM1 FM2 FM3

GND

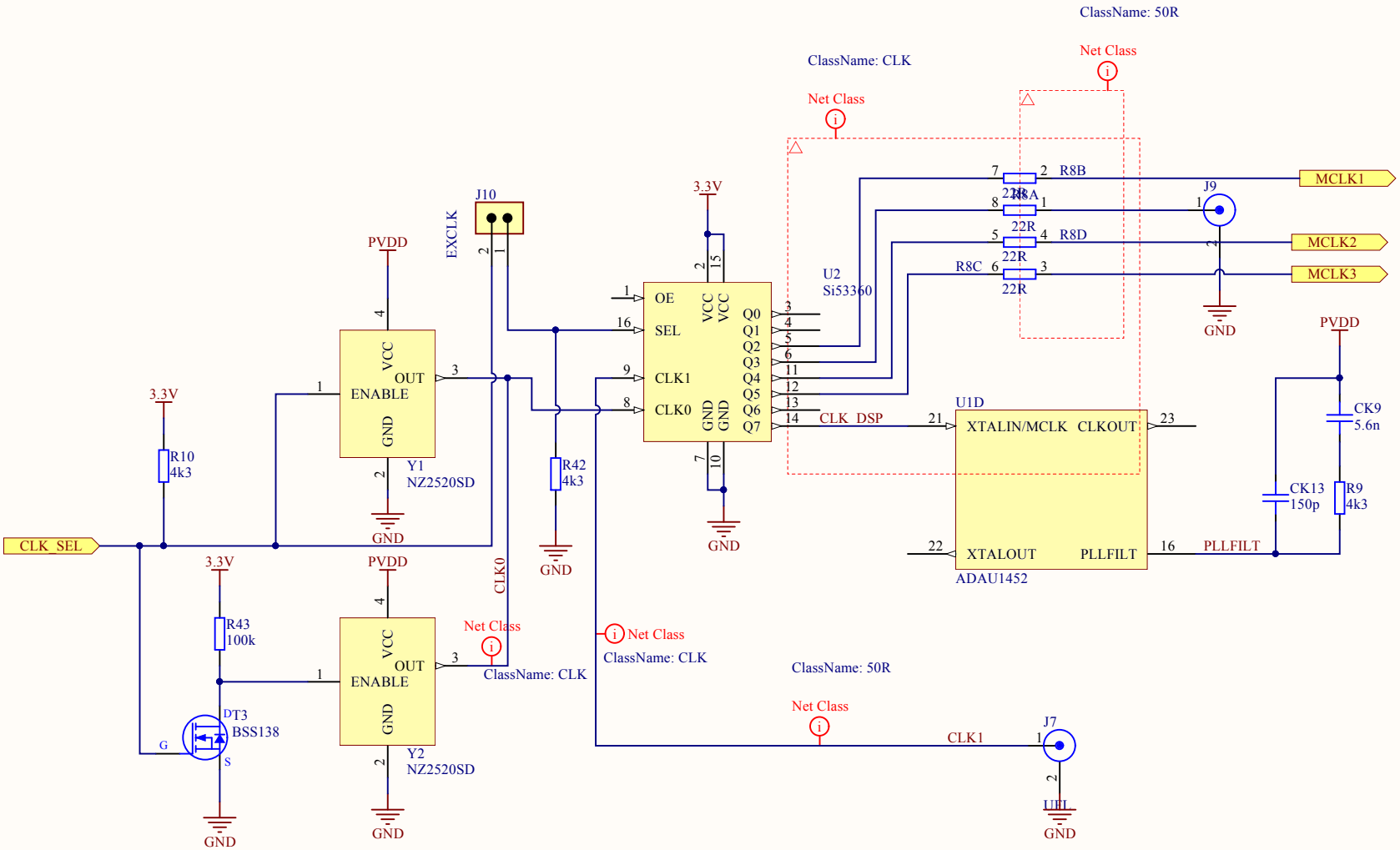
Title	*Top Sheet*		*FreeDSP*
Size: A4	Number:2	Revision:*	*
Date: 28.2.2016	Time: 14:18:16	Sheet1 of 14	*
File: C:\Projekty\ PiDSPBig\PCB\DSP.SchDoc			*

Altium

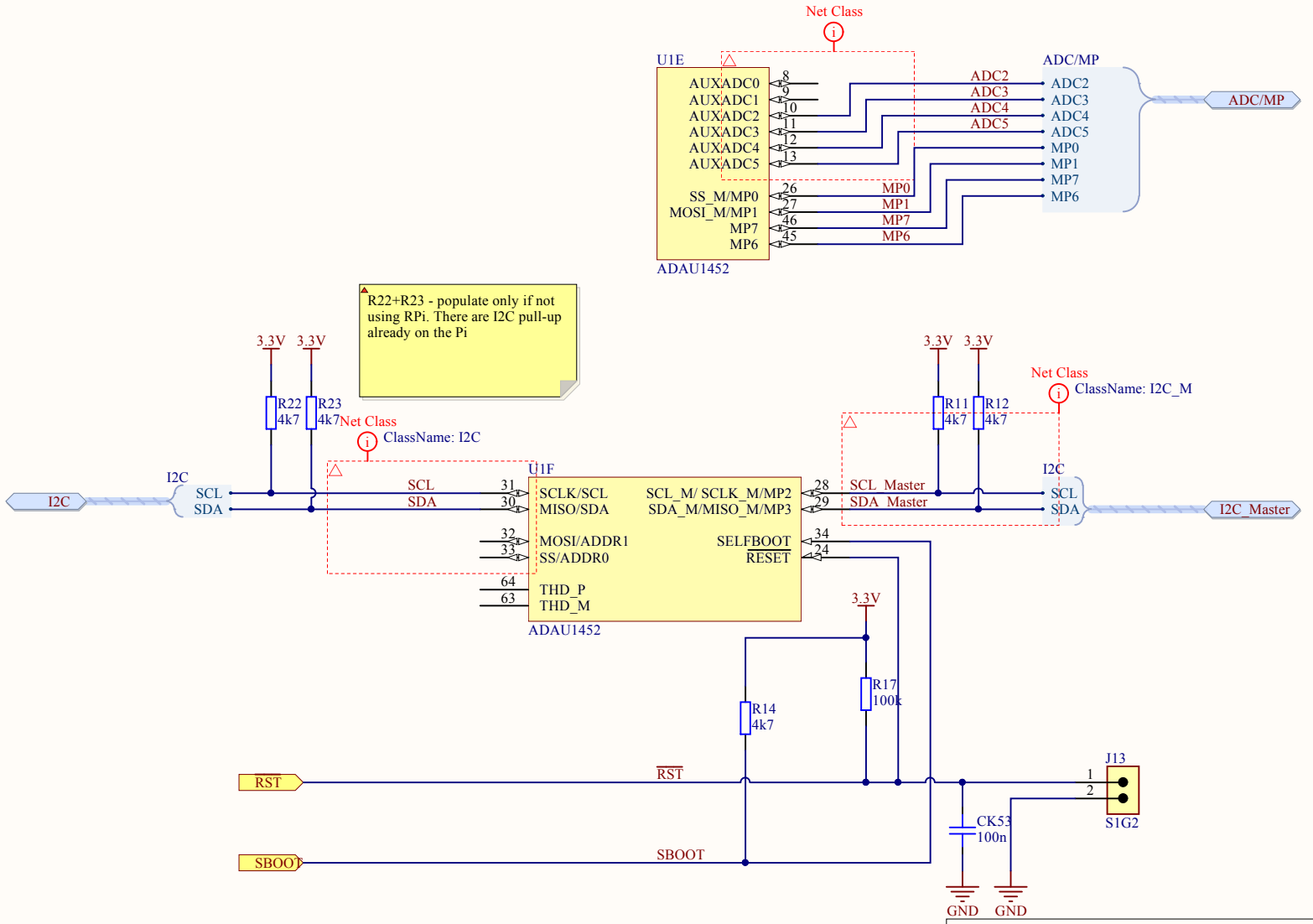


Title	***DSP I/O***		*FreeDSP*
Size: A4	Number:7	Revision:*	
Date: 28.2.2016	Time: 14:18:16	Sheet2 of 14	
File: C:\Projekty\ PiDSPBig\PCB\DSP DIO.SchDoc			

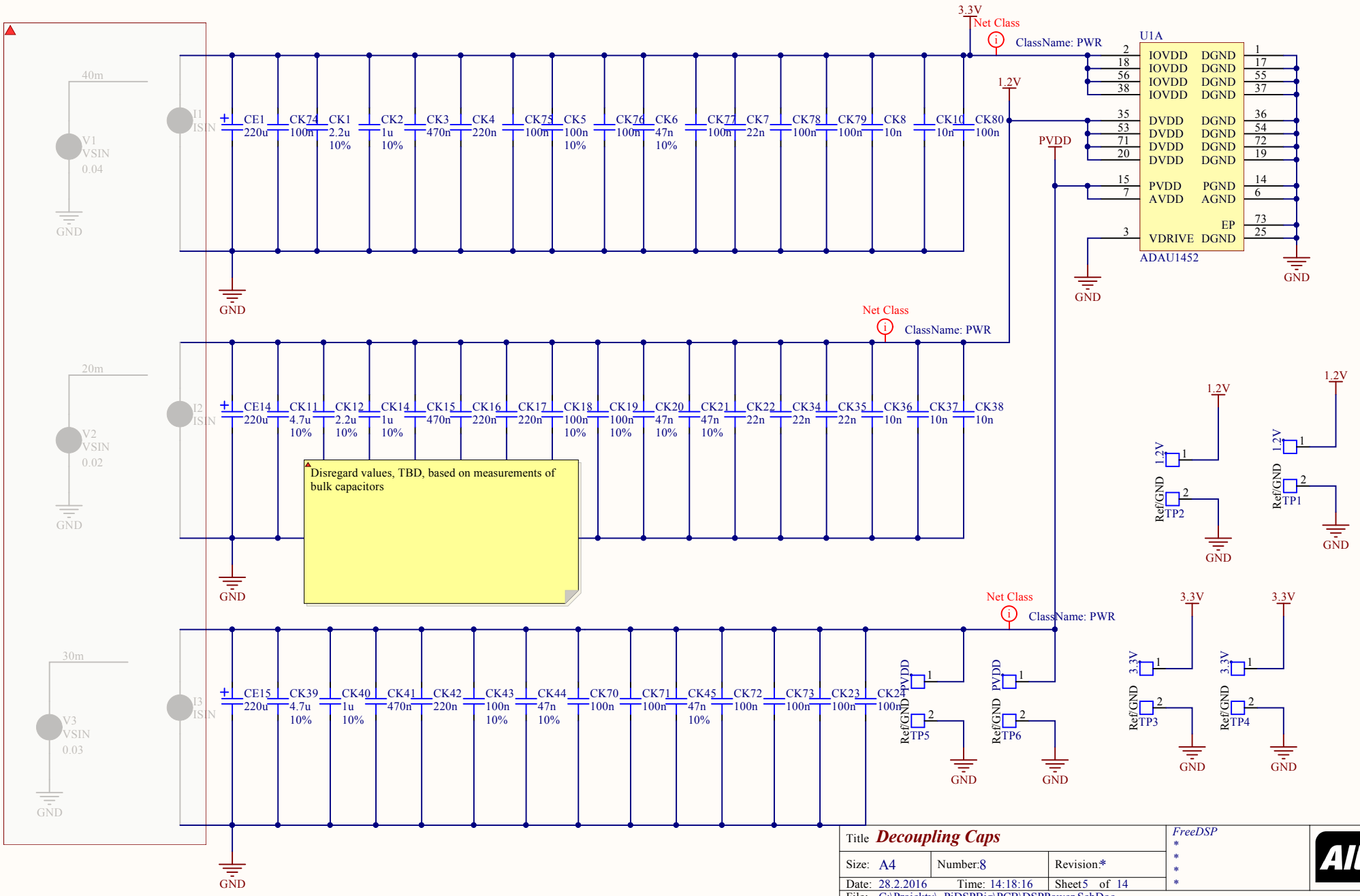
Altium

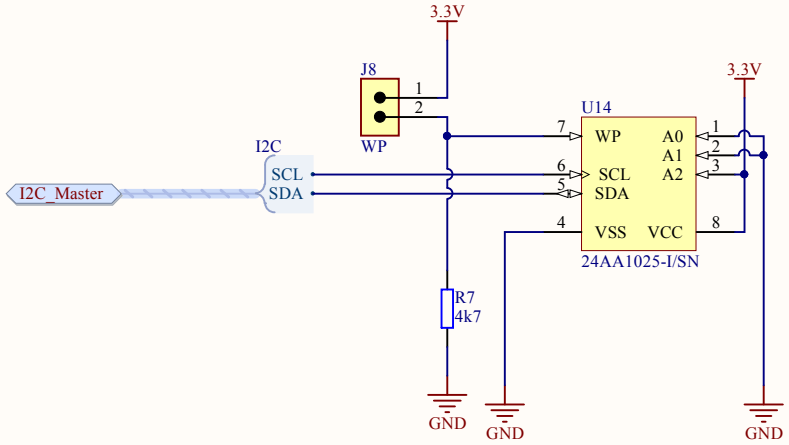


Title	**Clock Management**		FreeDSP
Size: A4	Number:5	Revision:*	
Date: 28.2.2016	Time: 14:18:16	Sheet3 of 14	
File: C:\Projekty\ PiDSPBig\PCB\DSPclk.SchDoc			



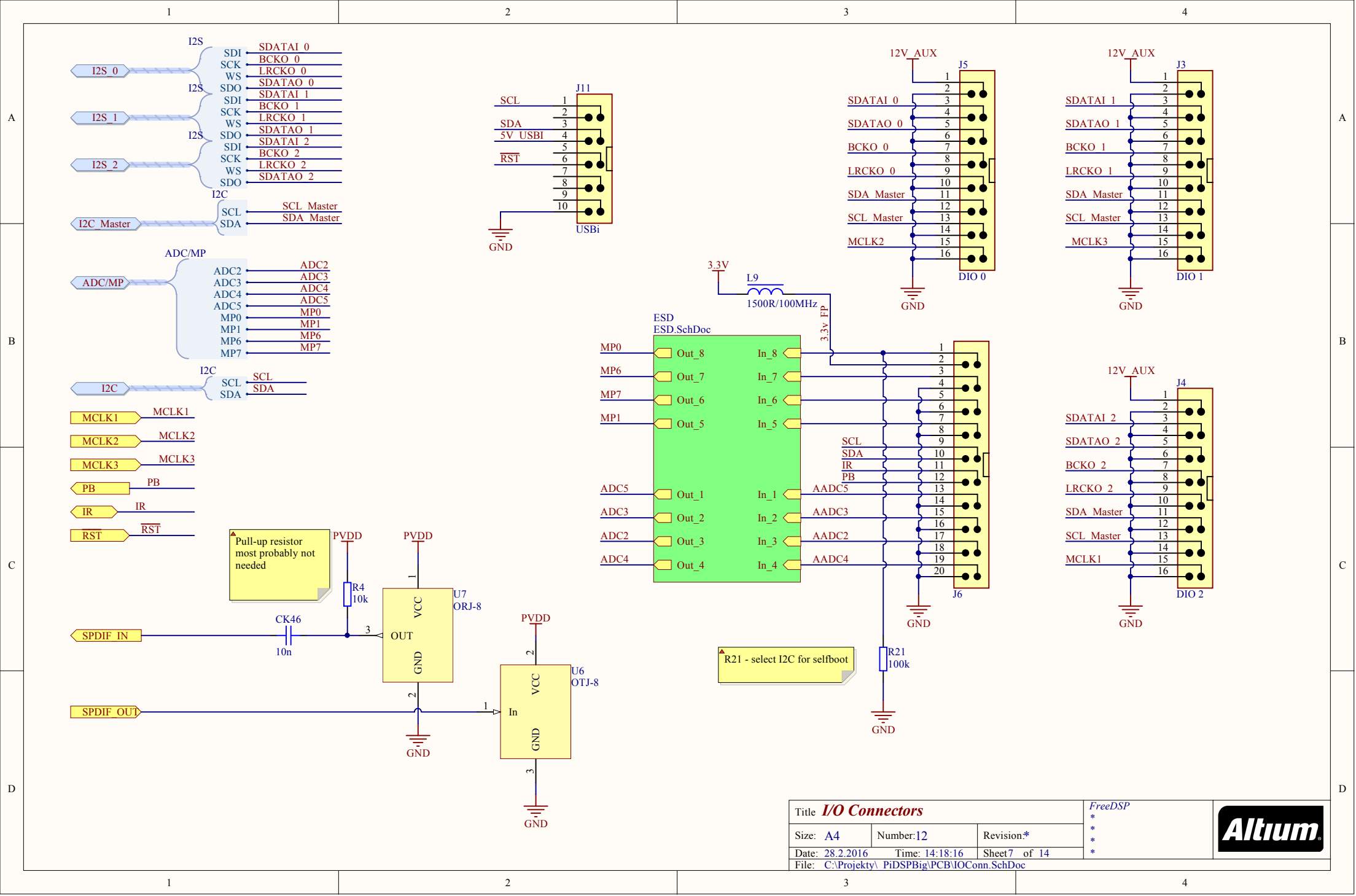
Title	***DSP Control***		*FreeDSP*
Size: A4	Number:6	Revision:*	
Date: 28.2.2016	Time: 14:18:16	Sheet4 of 14	
File: C:\Projekty\ PiDSPBig\PCB\DSPcontrol.SchDoc			


3.3V
Net Class
ClassName: PWR
U1A
ADAU1452
1.2V
PVDD
GND
Net Class
ClassName: PWR
Disregard values, TBD, based on measurements of bulk capacitors
Net Class
ClassName: PWR
Title Decoupling Caps
Size: A4
Number:8
Revision:*
Date: 28.2.2016
Time: 14:18:16
Sheet5 of 14
File: C:\Projekty\ PiDSPBig\PCB\DSPPower.SchDoc
FreeDSP
Altium

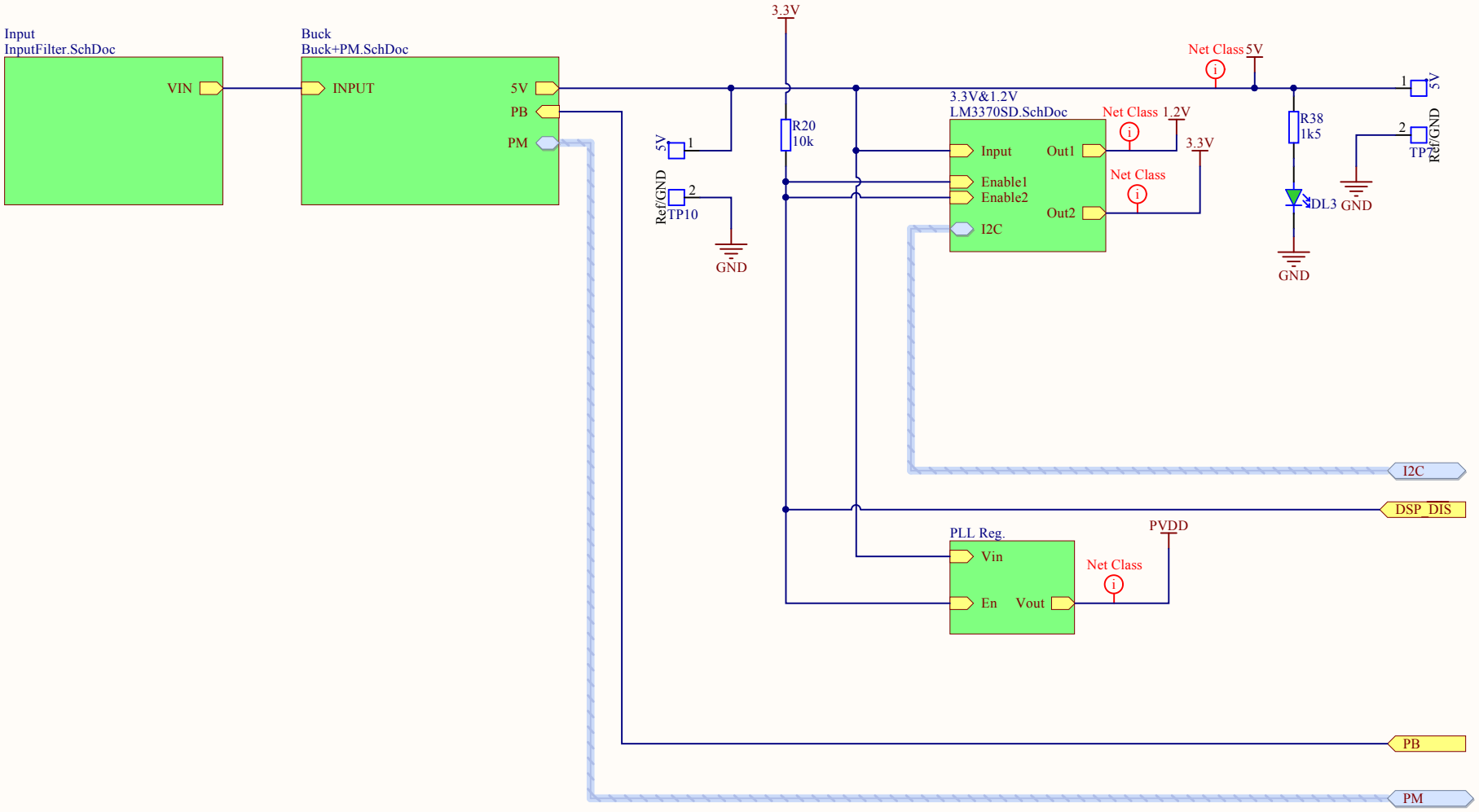


Title	***SBOOT EEPROM***		*FreeDSP* * * * *
Size: A4	Number:11	Revision:*	
Date: 28.2.2016	Time: 14:18:16	Sheet6 of 14	
File: C:\Projekty_PiDSPBig\PCB\EEPROM.SchDoc			

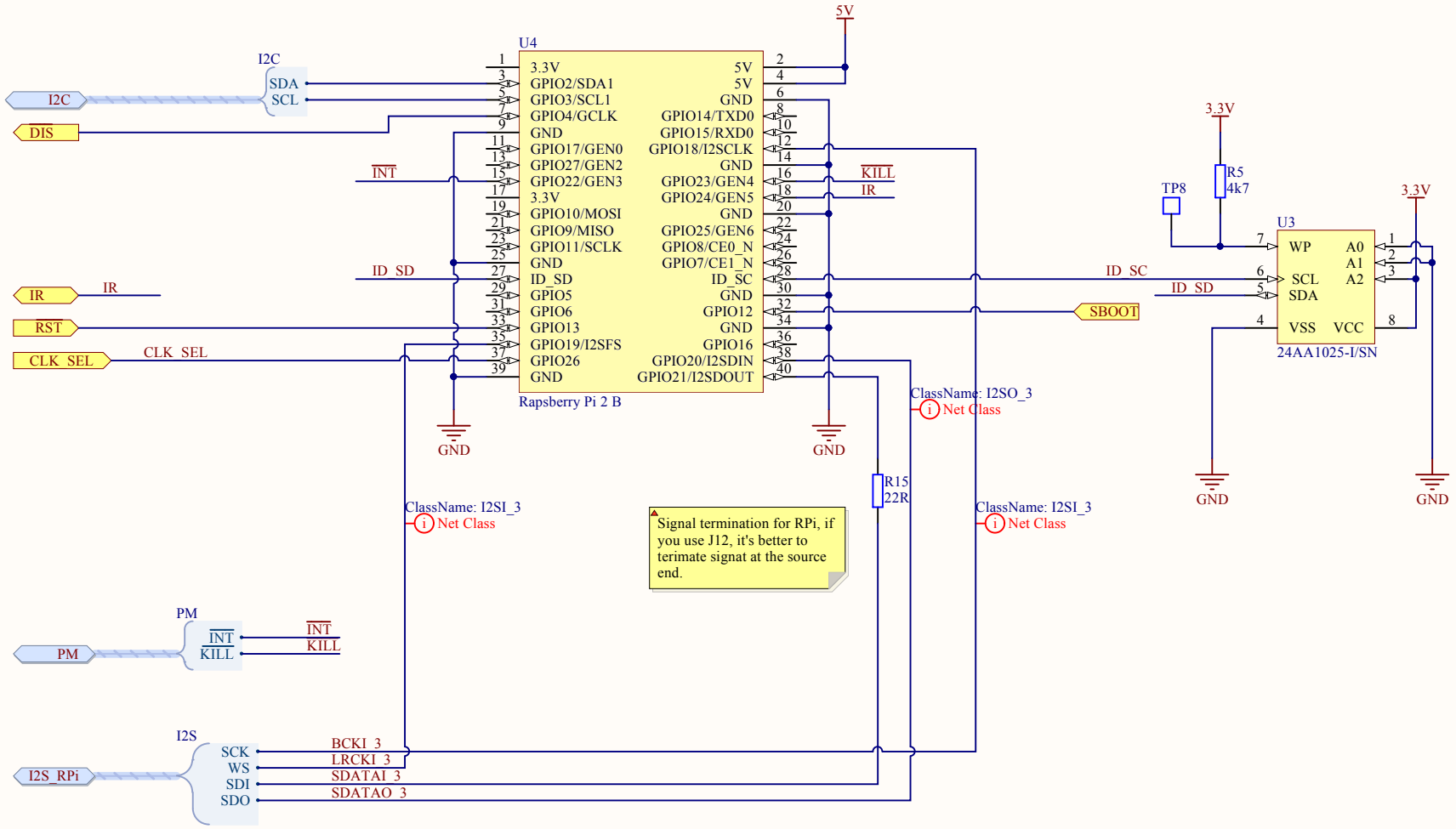
Altium



Title	*I/O Connectors*	FreeDSP
Size: A4	Number:12	Revision:*
Date: 28.2.2016	Time: 14:18:16	Sheet7 of 14
File: C:\Projekty\ PiDSPBig\PCB\IOConn.SchDoc		

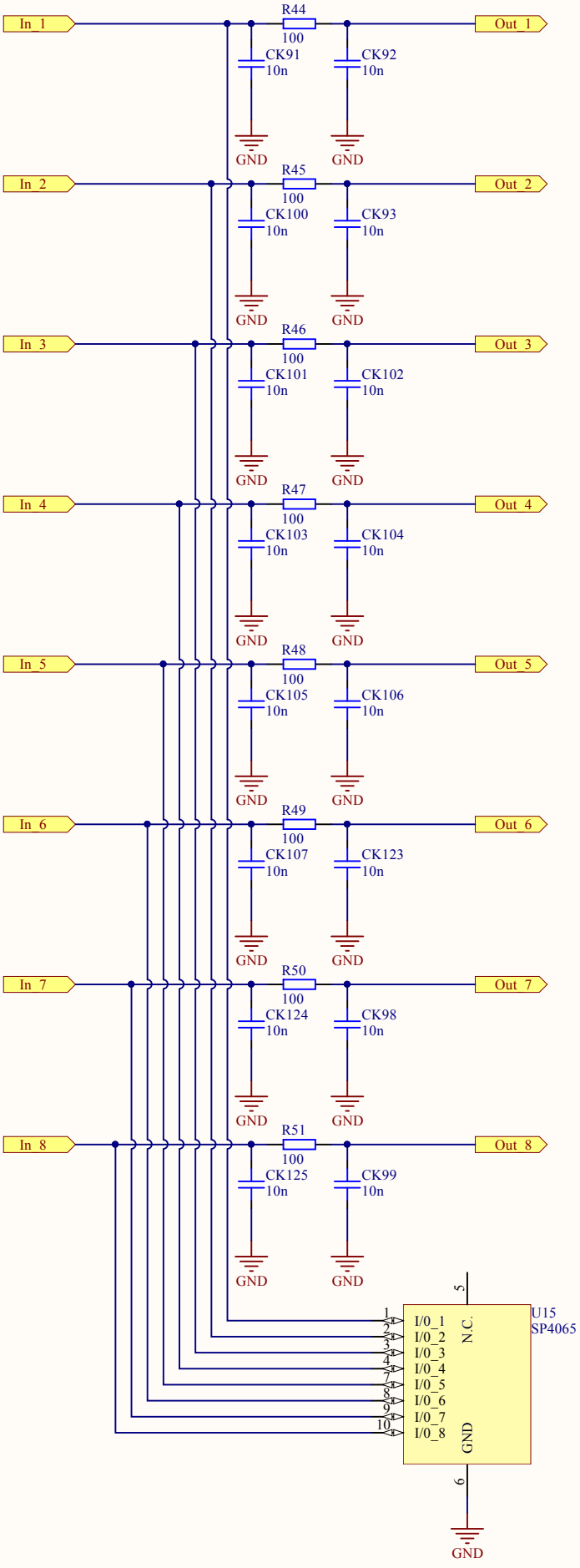


Title	***Power Supply***		*FreeDSP*
Size: A4	Number:1	Revision:*	
Date: 28.2.2016	Time: 14:18:16	Sheet8 of 14	
File: C:\Projekty\ PiDSPBig\PCB\PowerSupply.SchDoc			

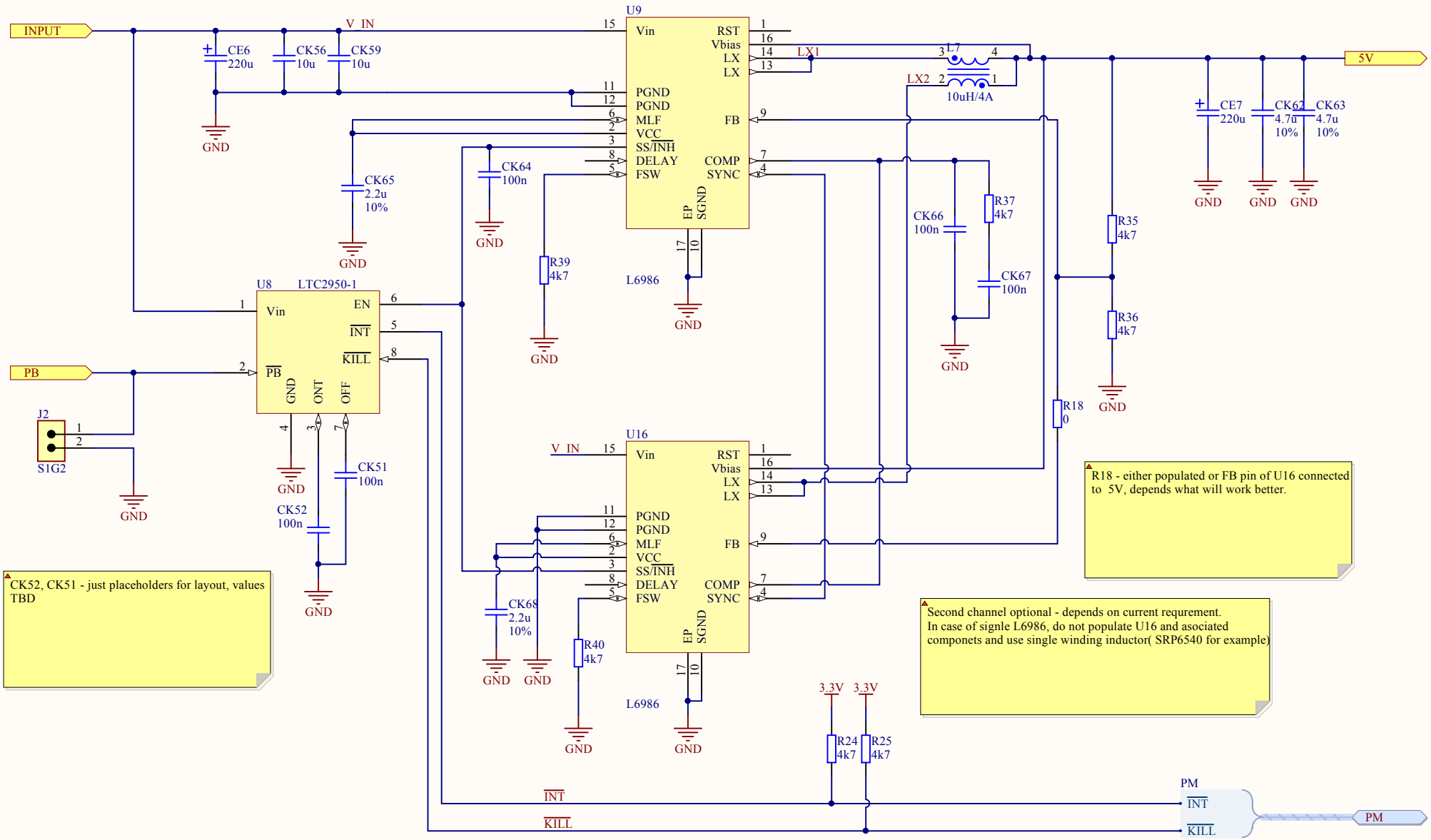


Title **RPi**			*FreeDSP*
Size: A4	Number:4	Revision:*	
Date: 28.2.2016	Time: 14:18:16	Sheet9 of 14	
File: C:\Projekty\ PiDSPBig\PCB\RPi&MCU.SchDoc			

Altium

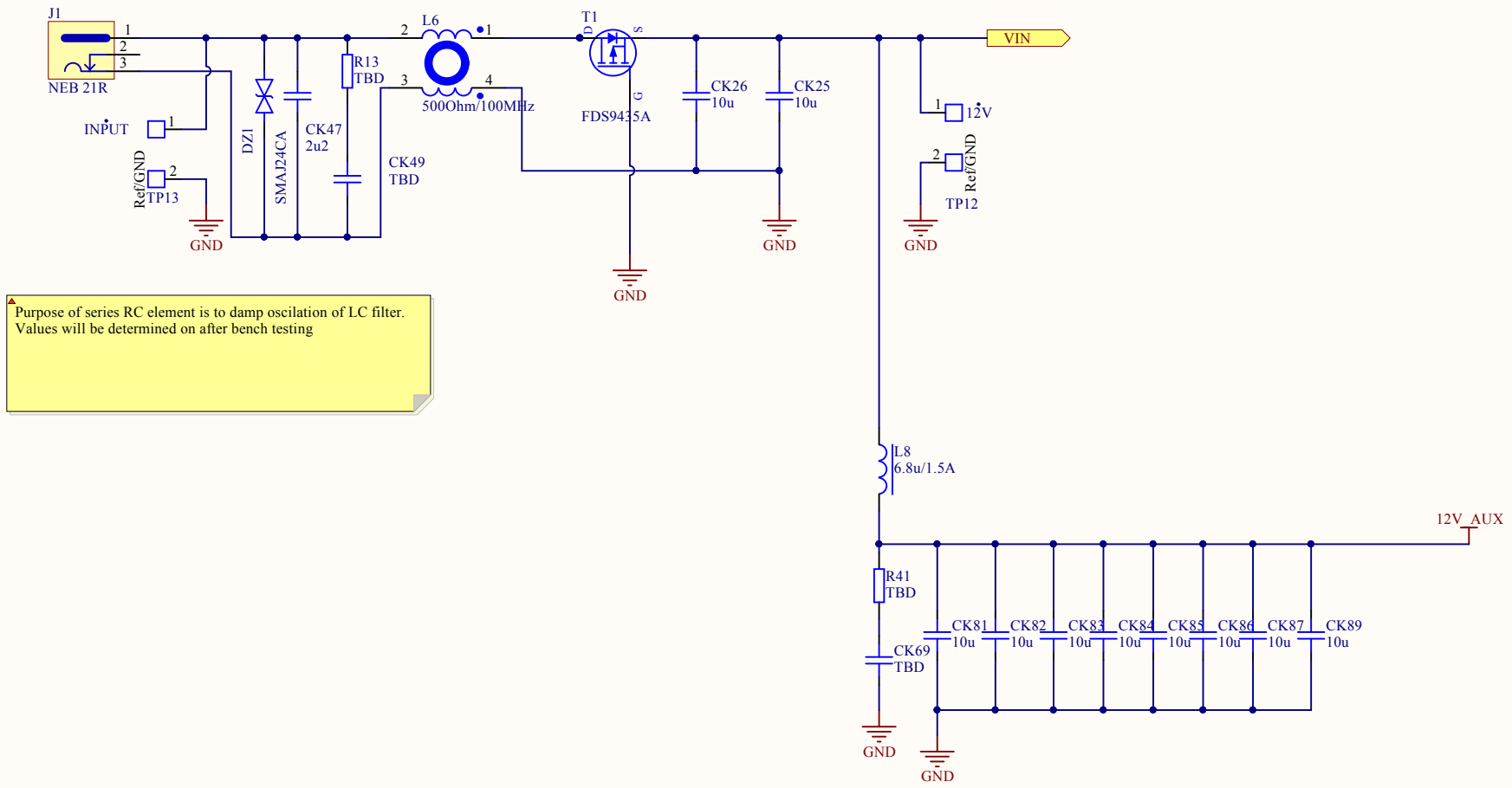


Title	ESD Protection		FreeDSP * * * *
Size: A4	Number:3	Revision:*	
Date: 28.2.2016	Time: 14:18:16	Sheet10 of 14	
File: C:\Projekty\ PiDSPBig\PCB\ESD.SchDoc			

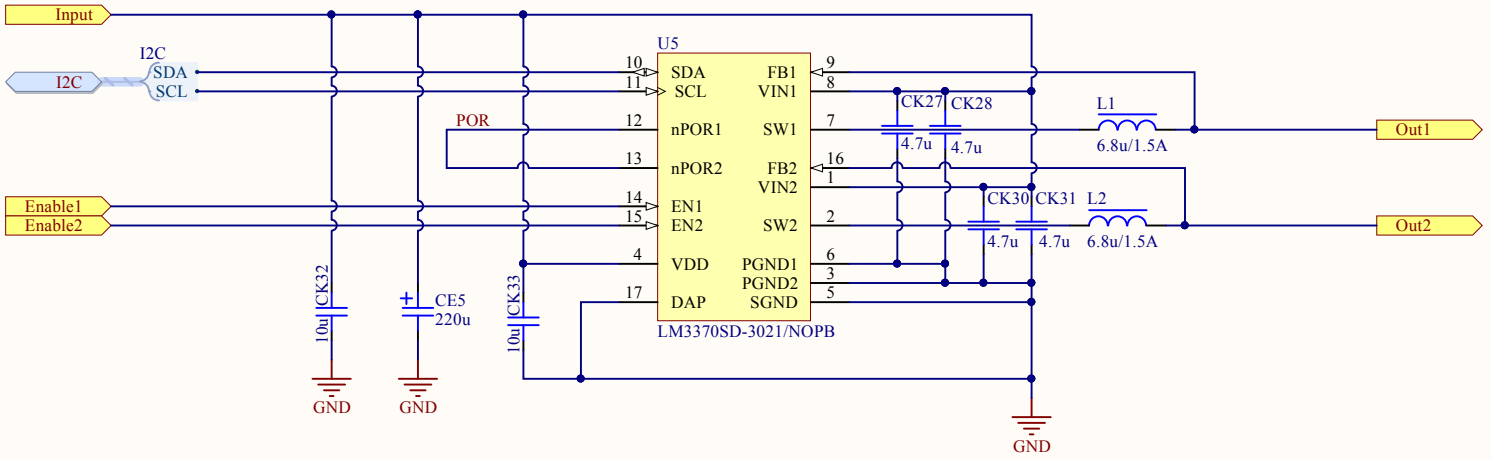


Title	*5V Regulator*	FreeDSP
Size: A4	Number:14	Revision:*
Date: 28.2.2016	Time: 14:18:16	Sheet11 of 14
File: C:\Projekty\ PiDSPBig\PCB\Buck+PM.SchDoc		

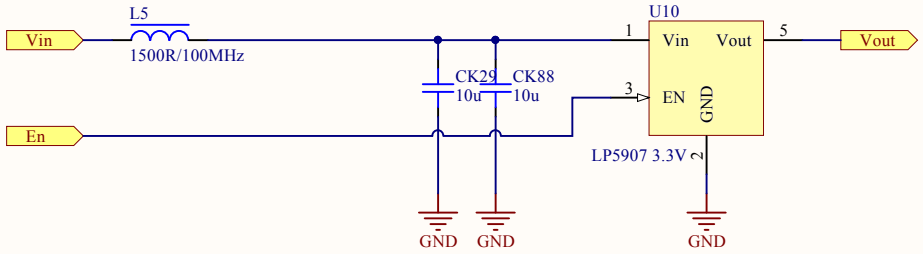
Altium



Title	***Input Filter***		*FreeDSP*
Size: A4	Number:13	Revision:*	
Date: 28.2.2016	Time: 14:18:16	Sheet12 of 14	
File: C:\Projekty\ PiDSPBig\PCB\InputFilter.SchDoc			



Title	***3.3V + 1.2V Regulator***		*FreeDSP* * * * *
Size: A4	Number:9	Revision:*	
Date: 28.2.2016	Time: 14:18:16	Sheet13 of 14	
File: C:\Projekty\ PiDSPBig\PCB\LM3370SD.SchDoc			



Title	***PVDD Regulator***		*FreeDSP* * * * *
Size: A4	Number:10	Revision:*	
Date: 28.2.2016	Time: 14:18:16	Sheet14 of 14	
File: C:\Projekty\ PiDSPBig\PCB\LP5907MFX-3.3NPB.SchDoc			