

F5 Board Cviller layout v3

Input voltage 10VDC / All values are mV

P1 and P2 set to read 0 ohm over R5 and R6

	Board #1	Board #2	R
R1	3	20	4k7
R2	38	120	47k5
R3	40	21	10
R4	40	21	10
R5	1	1	2k2
R6	2	2	2k2
R7	0	0	0.47
R8	0	0	0.47
R9	64	46	100
R10	64	46	100
R11	15	1	100
R12	15	1	100
R13	0	0	100
R14	0	0	100
R15	0	0	1k
R16	88	86	1k
R17	0	0	100
R18	88	86	100
R19	70	160	10k
R20	9900	9670	10k
R21	0	0	2k2
R22	0	0	2k2

Voltage between V(-) and leg of Q1-Q6

J174/J70

Q1	#1	#2	Q2	#1	#2
e	10	9,9	e	10	9,85
g	10	10	g	10	10
d	10	10	d	0	10

ZTX450/550

Q5	#1	#2	Q6	#1	#2
e	10	10	e	0	0
b	10	10	b	0.08	0.08
c	10	10	c	0	0

9240/240

Q3 #1			Q3 #2		
10	10	10	10	9,85	10

Q4 #1			Q4 #2		
0	10	10	0	9,85	0

Viewed with pins up