

The easiest way is to do the calculating is using the "plate current vs plate voltage"- and the "plate current vs grid voltage"-characteristics and solve things graphically. First you start what supply voltage you will use, lets say $E_{bb}=300V$ and the plate working voltage $V_p=200V$. Then you decide, while examining the "plate current vs "grid voltage", what grid bias voltage is favourable having a linear as possible part of the characteristic: $V_g=-1.5V$. Now it is possible to draw both the Plate-load-line and the Cathode-bias-resistor line.

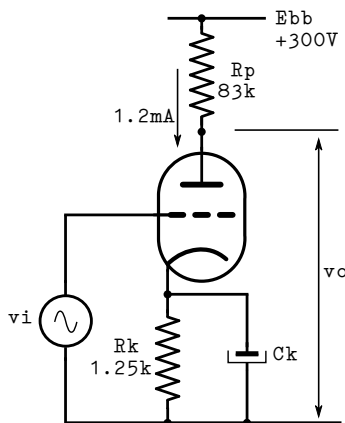
And then it is possible to calculate both the R_k and R_p :

$$R_k = \frac{V_g}{I_p} = \frac{1.5V}{1.2mA} = 1.25k$$

$$R_p = \frac{\Delta V_p}{\Delta I_p} = \frac{100V}{1.2mA} = 83k$$

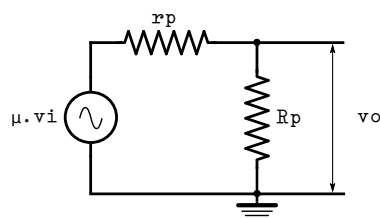
Now we can draw the circuit diagram to use this for the calculation of the small signal voltage gain:

R_k is decoupled with a sufficiently large capacitor.



Drawing the ac-equivalent circuit diagram for the plate-loop network of the triode (we use the quick calculation method described in this thread:

<https://www.diyaudio.com/forums/tubes-valves/38278-line-tube-learning-newbies-24.html> post number #240)



Where r_p is the internal plate resistance of the triode and R_p is the load resistance used in the application.

$$v_o = \frac{R_p}{r_p + R_p} \cdot \mu \cdot v_i$$

$$\frac{v_o}{v_i} = \frac{R_p}{r_p + R_p} \cdot \mu$$

With $R_p=83k$ and using the 12AX7 data sheet $r_p=62.5k$ and $\mu=100$, it is now easy to calculate the small signal voltage gain of this triode gain stage.

$$\frac{v_o}{v_i} = \frac{83}{62.5 + 83} \cdot 100 = 57$$

