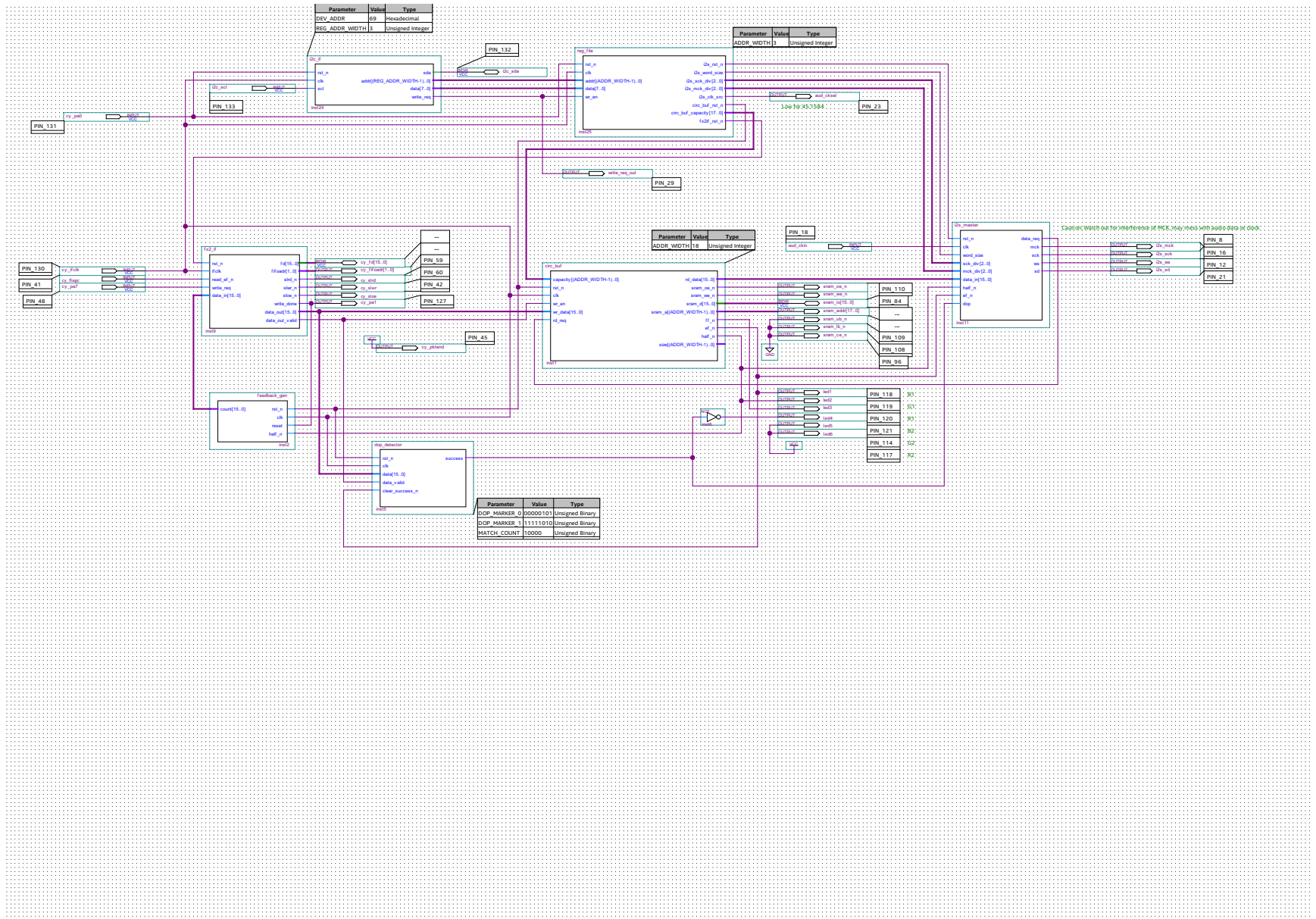
Parameter Value Type
DEV_ADDR 69 Hexadecimal
REG_ADDR_WIDTH 3 Unsigned Integer
i2c_if
PIN_132
i2c_sda
i2c_scl
PIN_133
PIN_131
cy_pa0
reg_file
Parameter Value Type
ADDR_WIDTH 3 Unsigned Integer
aud_clkout
low for 45.1584
PIN_23
write_req_out
PIN_29
Parameter Value Type
ADDR_WIDTH 18 Unsigned Integer
PIN_18
aud_clkin
i2s_master
Caution: Watch out for interference of MCK (big noise) with audio data or clock
PIN_8
PIN_16
PIN_12
PIN_21
i2s_mck
i2s_sck
i2s_ws
i2s_sd
fx2_if
PIN_130
PIN_41
PIN_48
cy_ifclk
cy_flagc
cy_pa7
PIN_59
PIN_60
PIN_42
PIN_127
circ_buf
PIN_110
PIN_84
PIN_109
PIN_108
PIN_96
sram_oe_n
sram_we_n
sram_a[15..0]
sram_addr[17..0]
sram_ub_n
sram_lb_n
sram_ce_n
cy_pklsend
PIN_45
feedback_gen
dop_detector
success
Parameter Value Type
DOP_MARKER_0 00000101 Unsigned Binary
DOP_MARKER_1 11111010 Unsigned Binary
MATCH_COUNT 10000 Unsigned Binary
led1
led2
led3
led4
led5
led6
PIN_118
PIN_119
PIN_120
PIN_121
PIN_114
PIN_117
R1
S1
R1
R2
S2