



## SM5817AP

Pin No.	Symbol	I/O	Description
1,2	XT,XT	I/O	Reference clock input/output terminals. fref=384fs
3	/CKSL	I	Reference clock frequency selection input.384fs at high level.
4	CKO	O	Clock output terminal (Buffer output signal of input XT).
5	LCRI	I	Synchronizing clock input terminal for fs.
6	DIN	I	Serial data input terminal.
7	BCKI	I	Bit clock input terminal.
8	Vss GND		Connect to ground.
9	/OMOD1	I	Stereo output mode at high level.
10	DG	O	Deglitching signal output (8 fs rates)
11	DOL	O	Serial data output terminal for left channel (16 bits).
12	DOR	O	Serial data output terminal for right channel (16 bits).
13	/OMOD2	I	Mode selection input for output signal of terminals DOL and DOR.16 bit DAC connection mode at high level.
14	WDCO	O	Word clock output terminal.
15	BCKO	O	Bit clock output terminal.(192 fs rates)
16	VDD		+5V power supply terminal.

NOTE:fs=44.1kHz