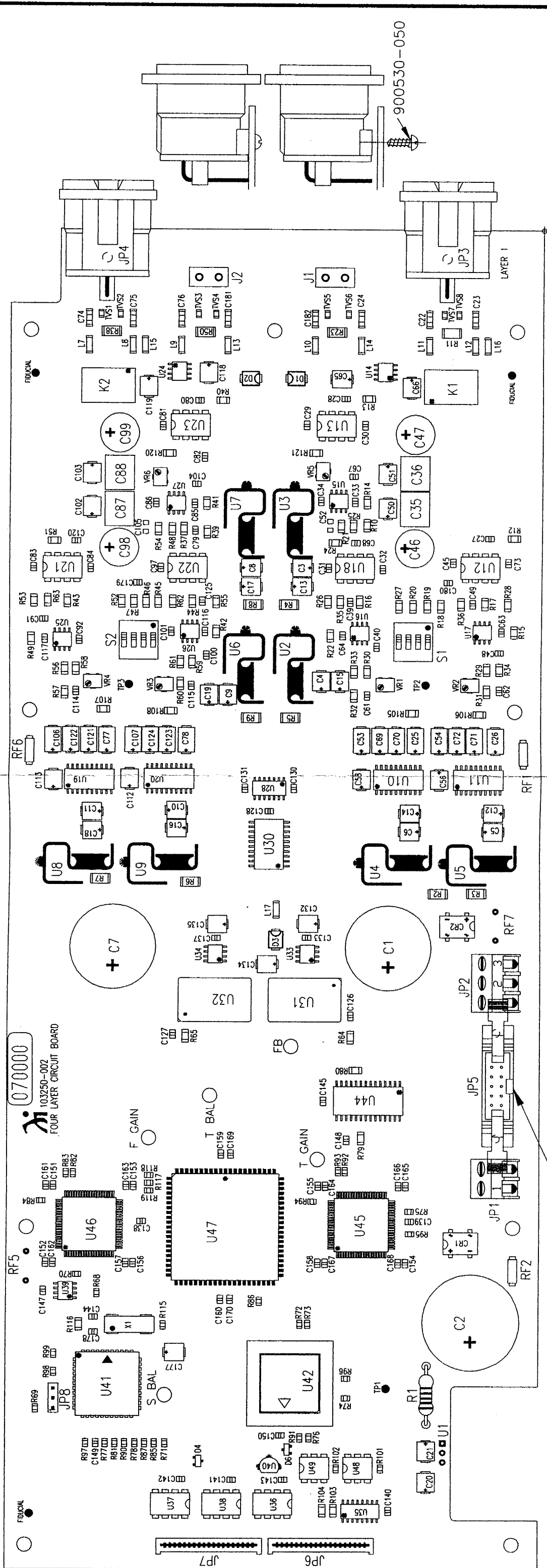


REVISION HISTORY TABLE

REV	DATE:	ECO#	CHANGES:
A	01-25-96	NONE	OFFICIAL RELEASE
B	02-09-96	ECO033	CHANGES TO SMT ASSEMBLY 113321-101
C	02-20-96	ECO038	REMOVE RF2.5,&7 (200026-001)
D	03-01-96	ECO042	ADD RF2 (DIPCAP 200026-001)



NOTES FOR ASSEMBLY OF 113321-001:

- 1) MASK C1, C2, C7, J1, J2, RF5, RF7 AND U1
- 2) CLOSE JAWS OF JP1(700028-001)& JP2(700033-001) PRIOR TO SOLDERING.
- 3) HAND SOLDER AND HAND WASH C1, C2, AND C7.
- 4) JP3 & JP4 MUST BE ALIGNED STRAIGHT, & SEATED FLAT TO BOARD.
- 5) ADD 1 SPACER UNDER EACH 3 PIN REGULATOR WITH NUBS DOWN.
CLIP STANDUP HEAT SINK (600081-002) TO REGULATORS AND
SOLDER TO BOARD AS ILLUSTRATED IN DRAWING.
- 6) J1, J2, & U1, ARE NOT INSTALLED AT THIS ASSEMBLY LEVEL.
- 7) SECURE JP3 AND JP4 TO BOARD WITH 900530-050 AS SHOWN.
- 8) DO NOT INSTALL U12,13,18,21,22 OR U23 IF U50,51,52,53,54,&55
ARE ALREADY INSTALLED ON BOARD.
- 9) DO NOT MIX COLORS, LOTS, OR MANUFACTURES COMPONENTS OF
THE SAME WADIA PART NUMBER ON ANY BOARDS.

Diagram of the rear view of the board. Components labeled include 600081-002, U2, 3, 4, 5, U6, 7, 8, 9, 900072-001, and BOARD.

Wadia Digital Corp.

624 TROY STREET RIVER FALLS, WISCONSIN 54022
PHONE: (715) 426-5900 FAX: (715) 426-5665

TITLE: W16 CDP/ANALOG BOARD ASSEMBLY

DRAWN BY: MAG	TOLERANCE: 0.025"	BOM REV: D	
DATE: 01-10-96	DRAWING#: 113321-D.DWG	SCALE: FIT	REV
CHKD:	PART NUMBER:	SIZE: B	D
113321-001		SHEET 1 of 1	
MARCH 01, 1996			