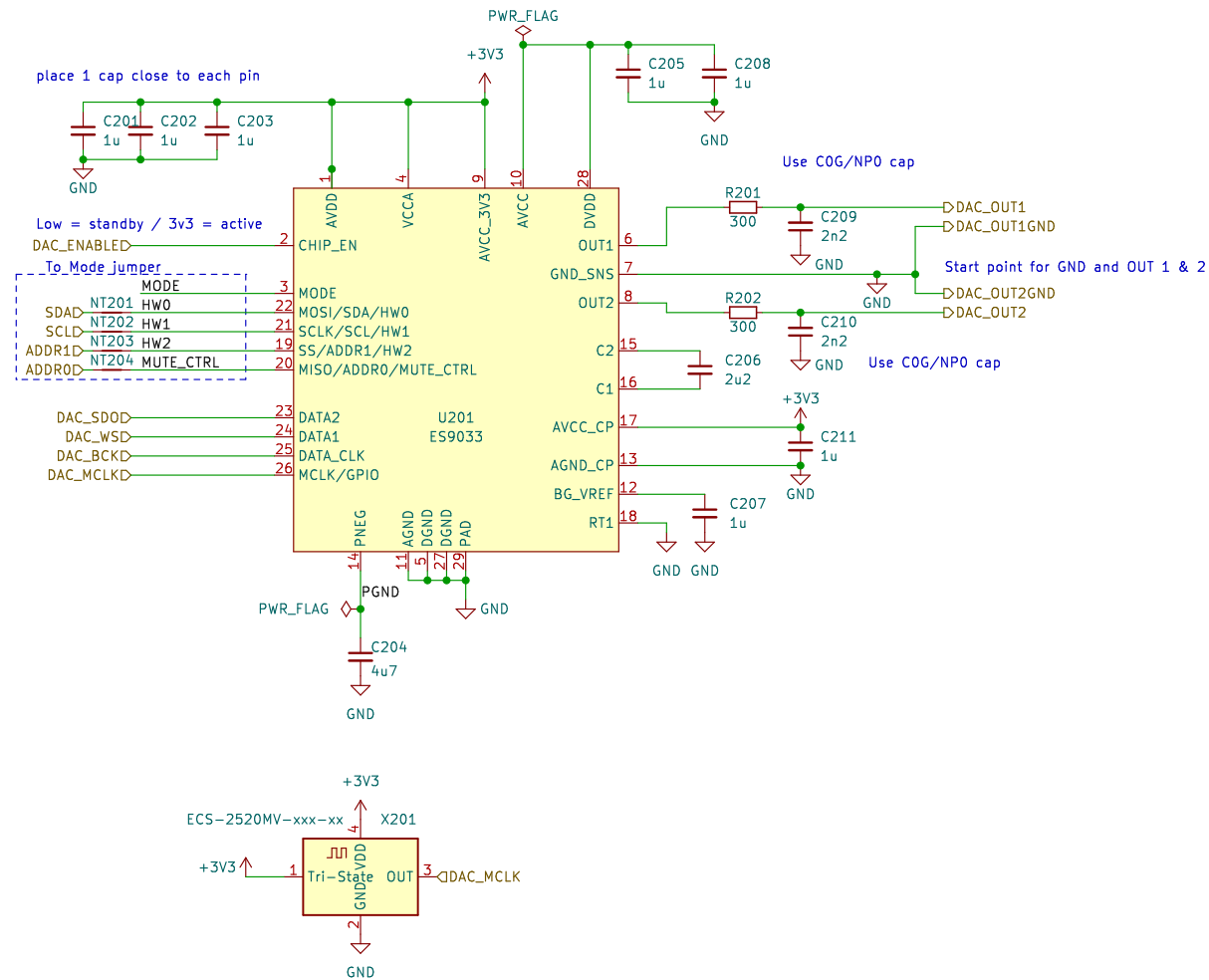


+3.3V PWR_FLAG



Mode and settings

See datasheet page 11 for mode settings:
Each pin has 4 states.
GND/ AVDD/ GND with 47kR/ AVDD with 47kR

