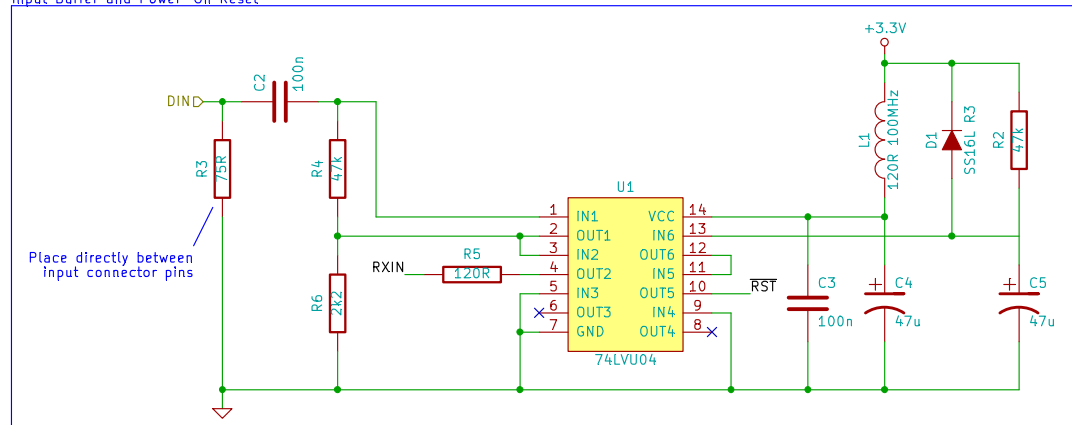
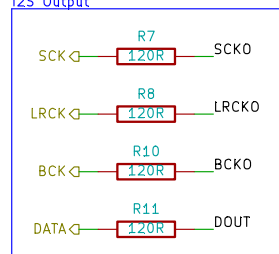




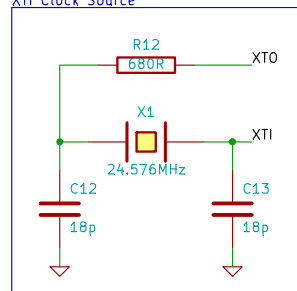
Input Buffer and Power-On Reset



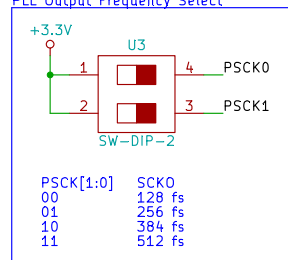
I2S Output



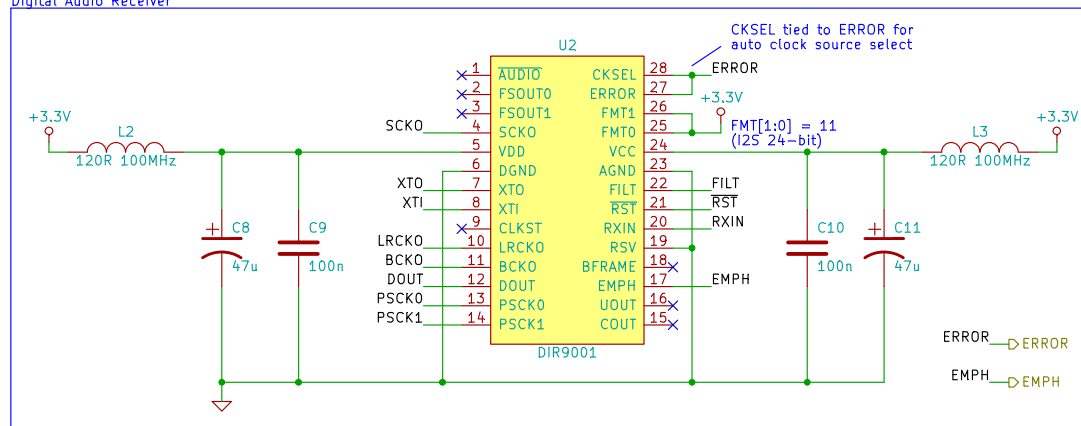
XTI Clock Source



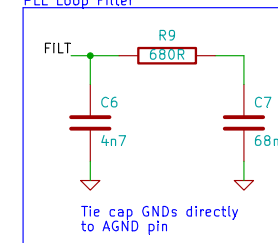
PLL Output Frequency Select



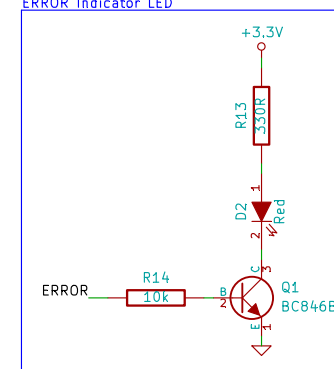
Digital Audio Receiver



PLL Loop Filter



ERROR Indicator LED



Drawn by:

**PS KEMP**

File:

Sheet: /Receiver/

**Title: Receiver**

Size: A4

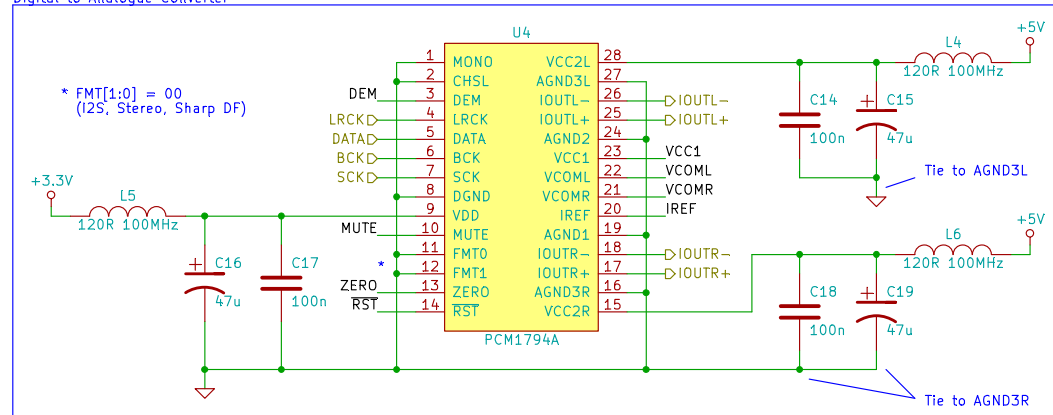
Date: 1 aug 2014

**Rev: Draft 1**

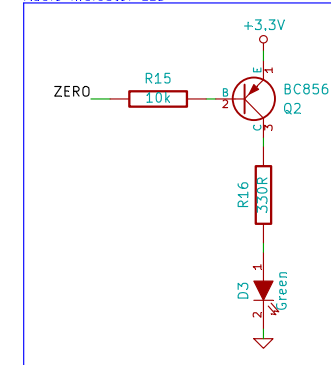
KiCad E.D.A. eeschema (2013-03-04 BZR 3984)-stable

Id: 2/5

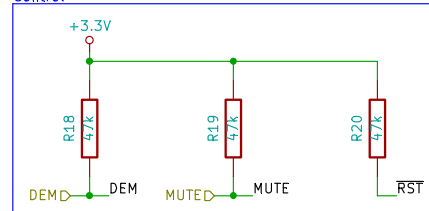
# Digital to Analogue Converter



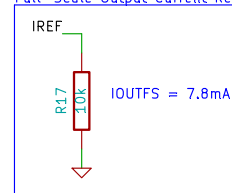
## Audio Indicator LED



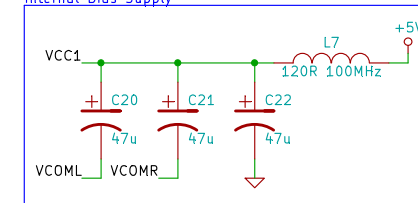
## Control



## Full-Scale Output Current Ref

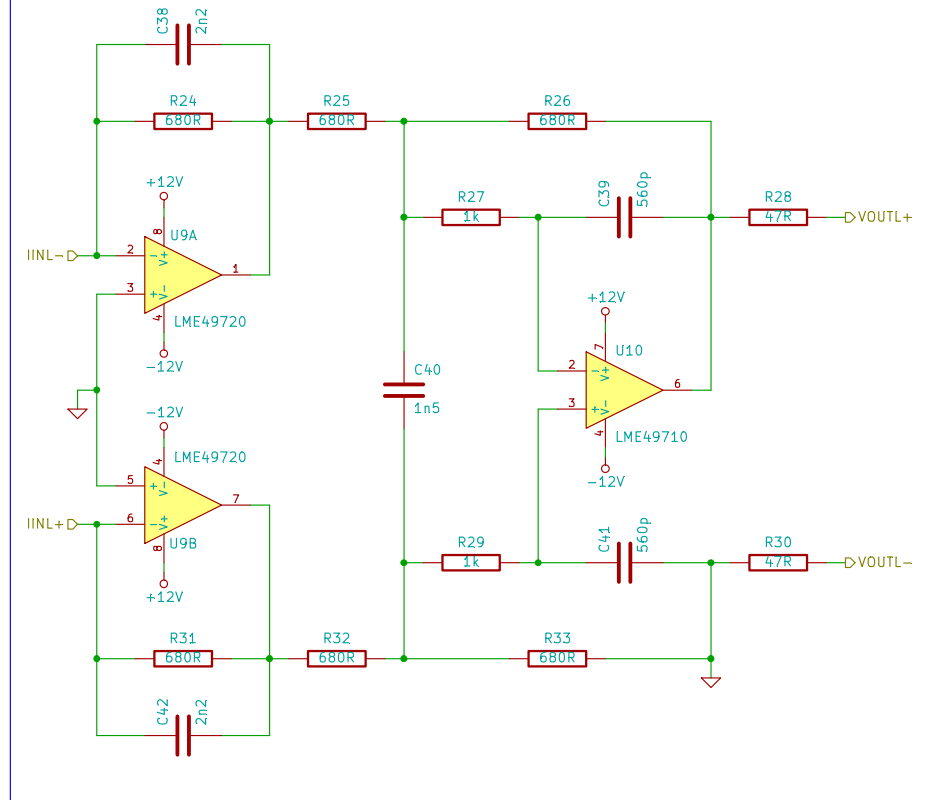


## Internal Bias Supply

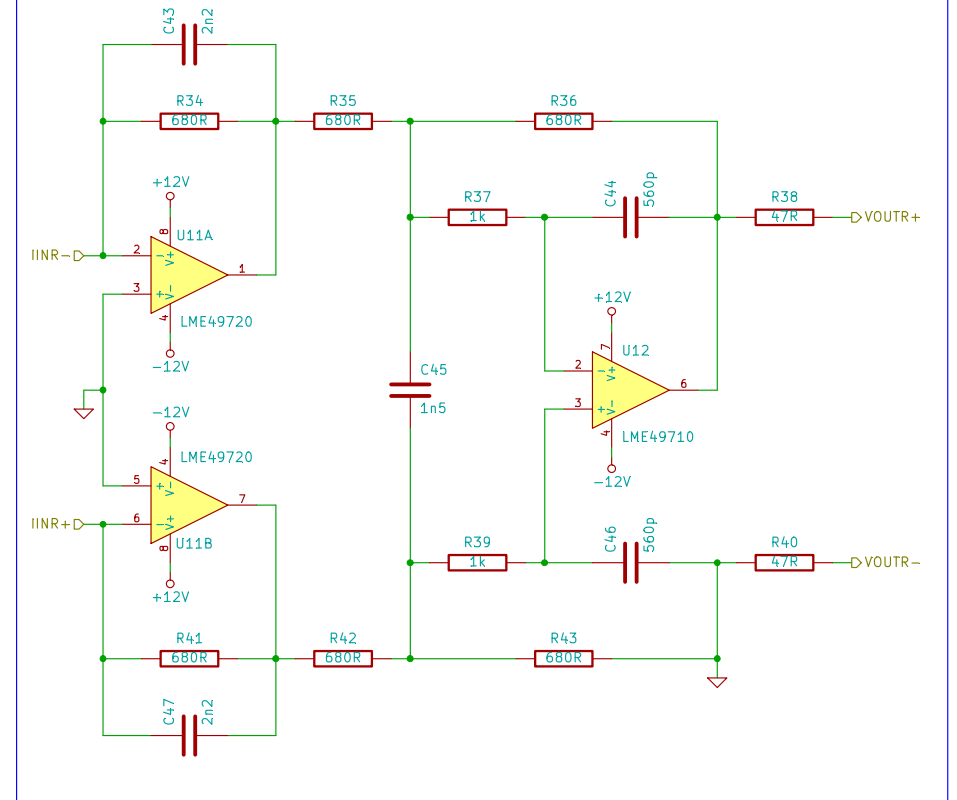


Drawn by: <b>PS KEMP</b>		
File:		
Sheet: /DAC/		
Title: DAC		
Size: A4	Date: 1 aug 2014	Rev: Draft 1
KiCad E.D.A. eschema (2013-03-04 BZR 3984)-stable		Id: 3/5

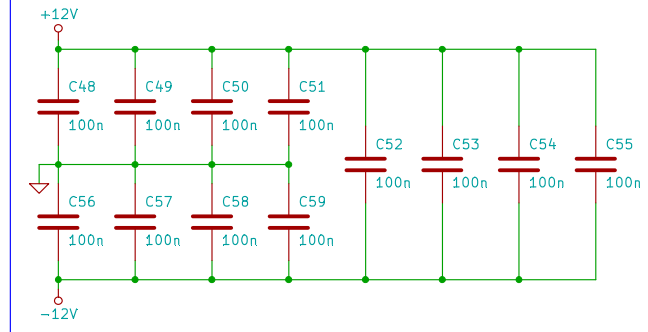
Left Channel I/V Conversion and LPF



Right Channel I/V Conversion and LPF



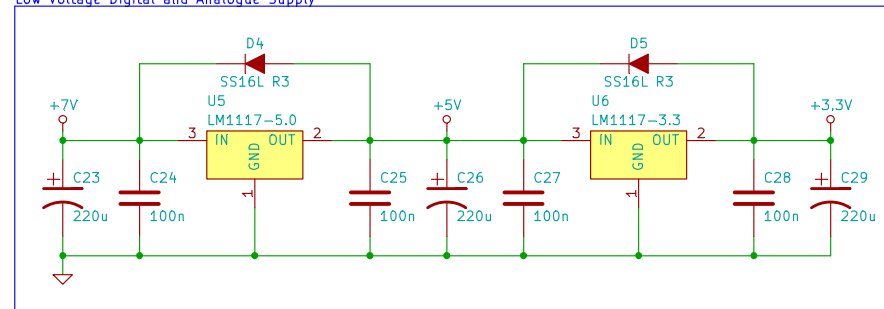
Supply Decoupling



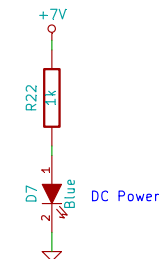
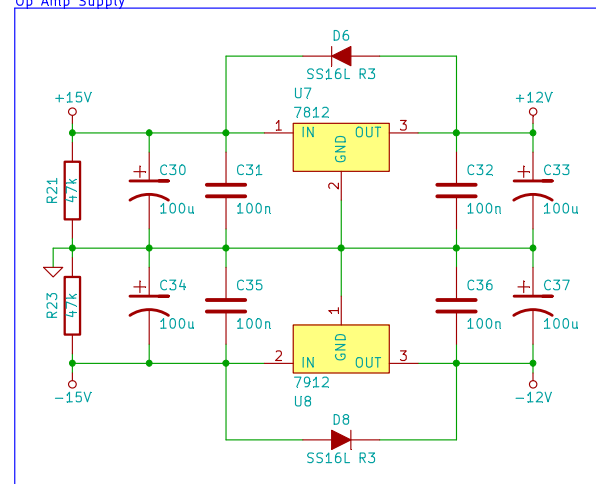
Output Filter:  
150kHz, 2nd Order Butterworth  
3.75 Vrms FS Output

Drawn by: <b>PS KEMP</b>		
File:		
Sheet: /IV_Filter/		
Title: I/V and Output Filter		
Size: A4	Date: 1 aug 2014	Rev: Draft 1
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# Low Voltage Digital and Analogue Supply



# Op Amp Supply



Drawn by: <b>PS KEMP</b>		
File: Sheet: /Power/		
<b>Title: Power</b>		
Size: A4	Date: 1 aug 2014	Rev: Draft 1
KiCad E.D.A. eeschema (2013-03-04 BZR 3984)-stable		Id: 5/5