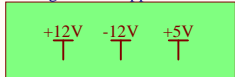


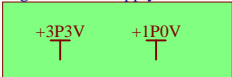
Power Supply

Sheet12
Analog Power Supplies.SchDoc



PSD

Sheet13
Digital Power Supply.SchDoc



PSD
FPD



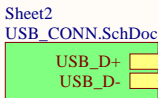
FID1
FIDUCIAL



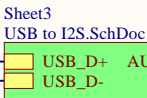
FID2
FIDUCIAL



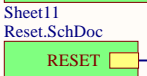
FID3
FIDUCIAL



FPD PSD

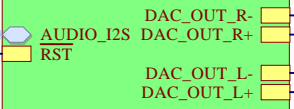


FPD PSD



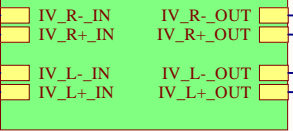
FPD
PSD

Sheet4
DAC.SchDoc



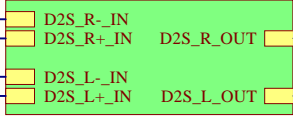
FPD PSD

Sheet5
IV STAGE.SchDoc



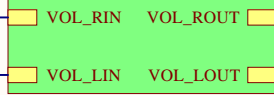
PSD FPD

Sheet6
Differential to Single Ended.SchDoc



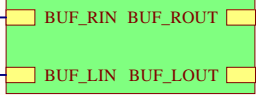
PSD FPD

Sheet7
VolumeCTRL.SchDoc



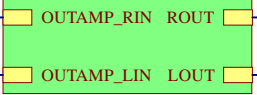
FPD PSD

Sheet8
Buffer.SchDoc



FPD PSD

Sheet9
Output Amp.SchDoc



PSD FPD

Sheet10
Output_Con.SchDoc



FPD PSD

Title **USB Headphone DAC/AMP**

Size: A

Number:00002

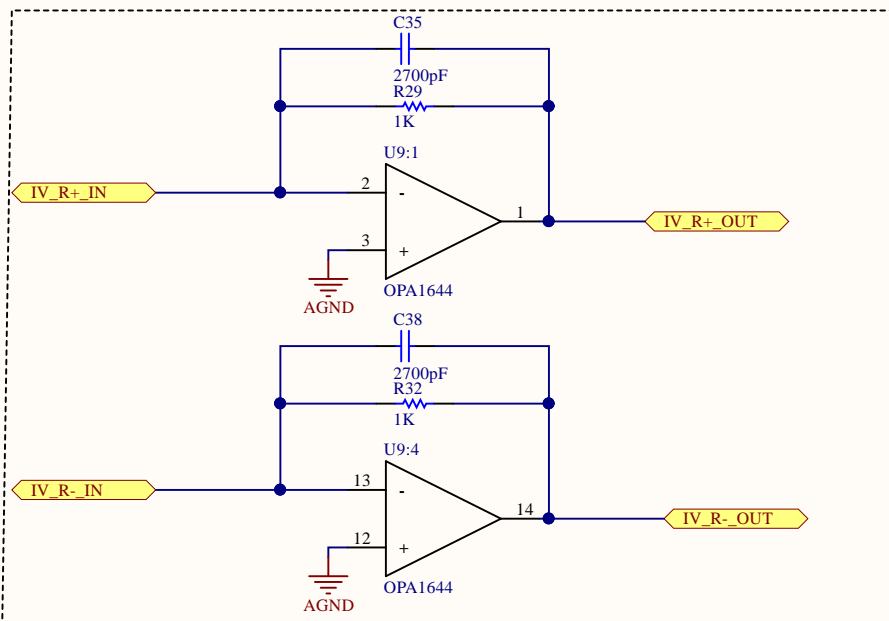
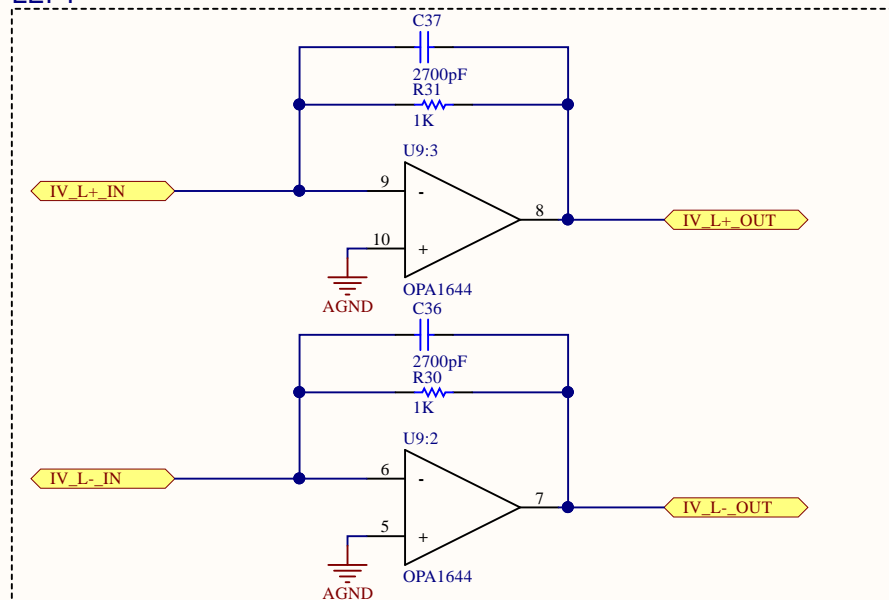
Revision3

Date: 8/21/2017

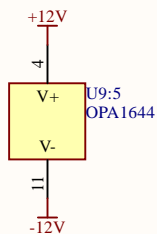
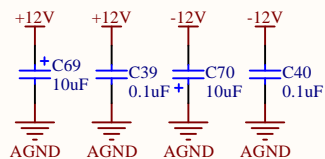
Time: 8:19:09 PM Sheet 1 of 13

File: H:\DAC_AMP\DAC_AMP\TopAssy.SchDoc

LEFT



RIGHT

Title **IV Stage**

Size: A

Number:00002

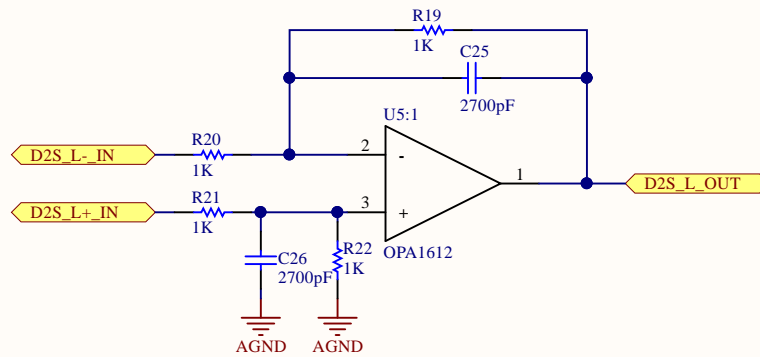
Revision3

Date: 8/21/2017

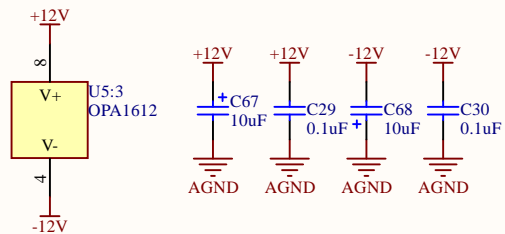
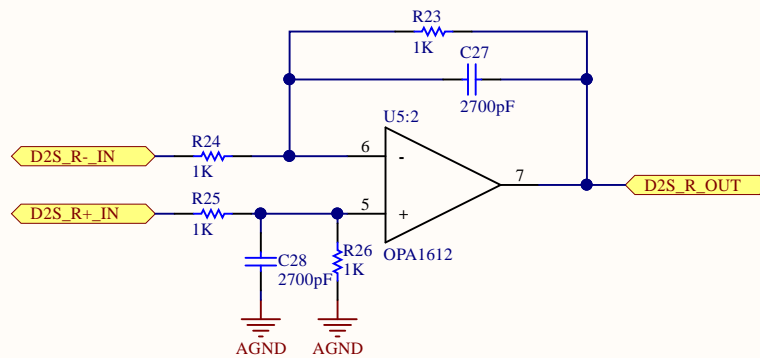
Time: 8:19:09 PM Sheet5 of 13

File: H:\DAC_AMP\DAC_AMP\IV STAGE.SchDoc

LEFT



RIGHT



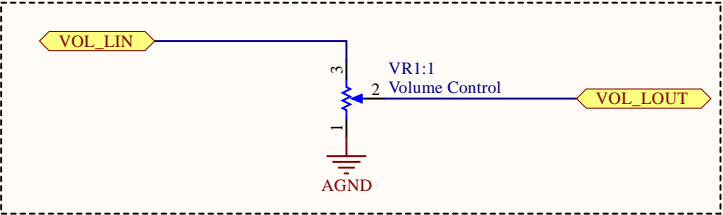
Title ***Differential to Single Ended***

Size: **A** Number: **00002** Revision: **3**

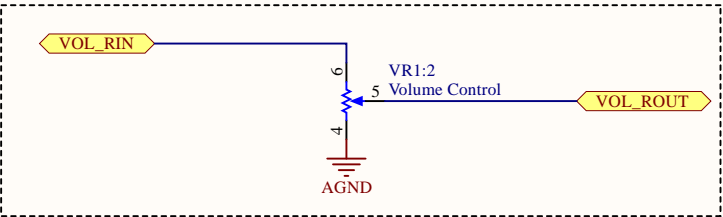
Date: **8/21/2017** Time: **8:19:09 PM** Sheet **6** of **13**

File: **H:\DAC_AMP\DAC_AMP\Diffrential to Single Ended.Sch** Doc

LEFT

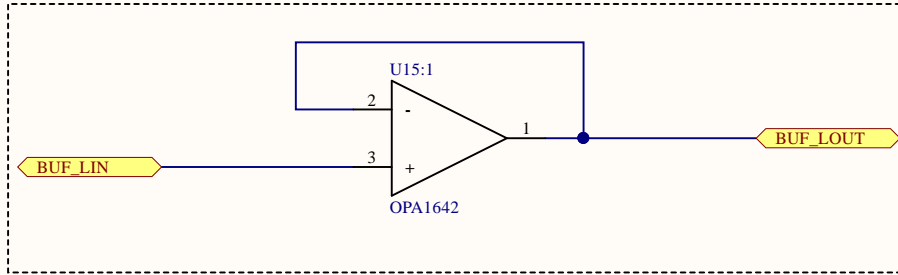


RIGHT

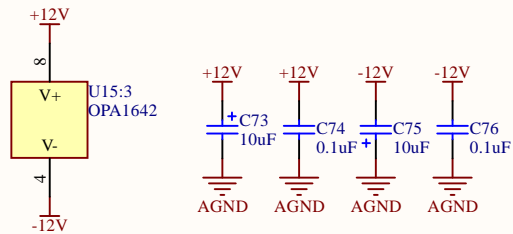
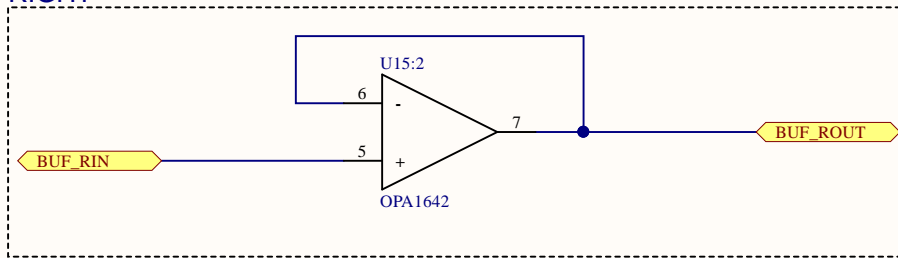


Title <i>Volume Control</i>		
Size: A	Number:00002	Revision3
Date: 8/21/2017	Time: 8:19:09 PM	Sheet7 of 13
File: H:\DAC AMP\DAC AMP\VolumeCTRL.SchDoc		

LEFT

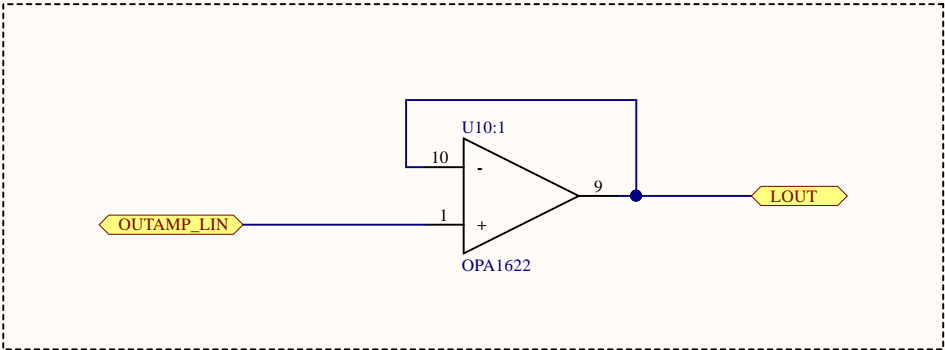


RIGHT

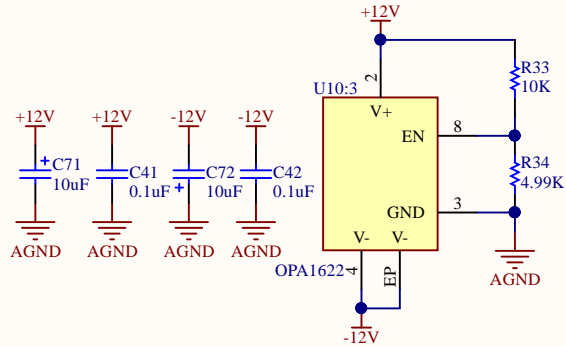
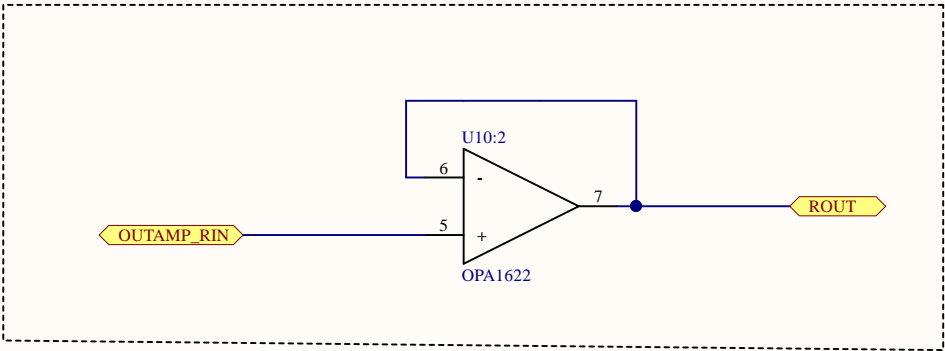


Title Buffer Ouput Driver		
Size: A	Number:00002	Revision3
Date: 8/21/2017	Time: 8:19:09 PM	Sheet8 of 13
File: H:\DAC_AMP\DAC_AMP\Buffer.SchDoc		

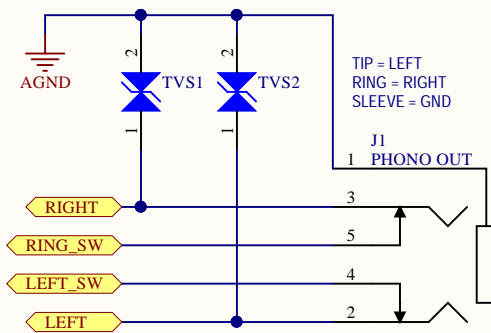
LEFT



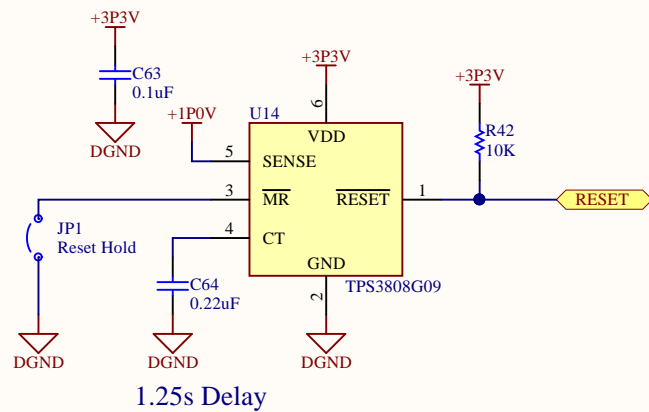
RIGHT



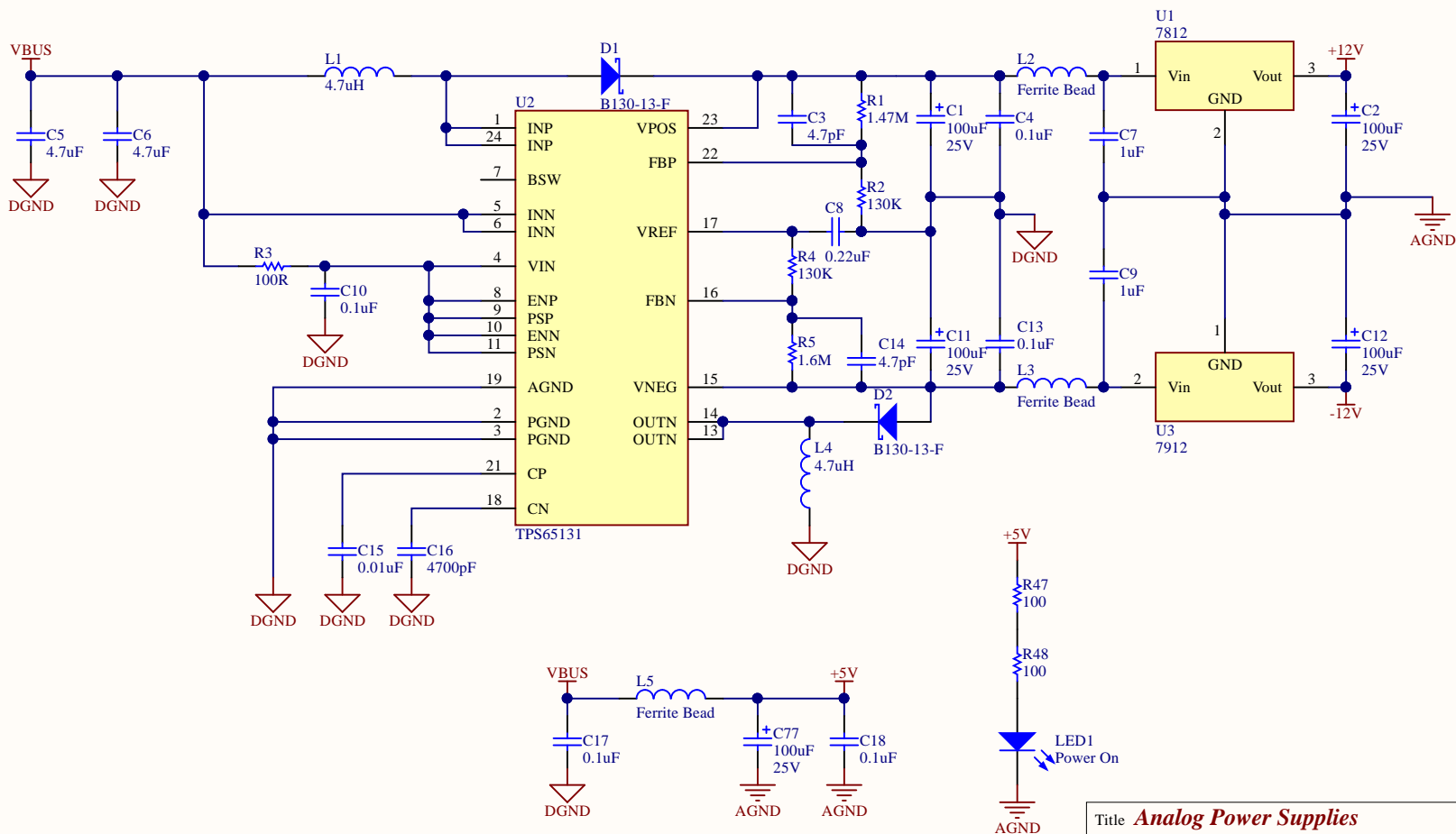
Title OPA1622 OUTPUT AMP		
Size: A	Number: 00002	Revision: 3
Date: 8/21/2017	Time: 8:19:10 PM	Sheet 9 of 13
File: H:\DAC_AMP\DAC_AMP\Output Amp.SchDoc		



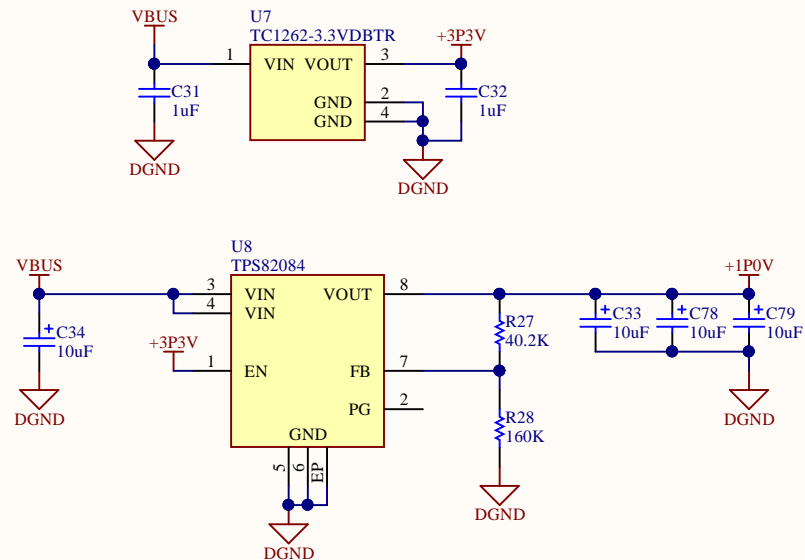
Title <i>Output Connector</i>		
Size: A	Number: 00002	Revision: 3
Date: 8/21/2017	Time: 8:19:10 PM	Sheet 10 of 13
File: H:\DAC_AMP\DAC_AMP\Output_Con.SchDoc		



Title Reset Circuit		
Size: A	Number: 00002	Revision: 3
Date: 8/21/2017	Time: 8:19:10 PM	Sheet 11 of 13
File: H:\DAC_AMP\DAC_AMP\Reset.SchDoc		



Title Analog Power Supplies		
Size: A	Number:00002	Revision:3
Date: 8/21/2017	Time: 8:19:10 PM	Sheet 12 of 13
File: H:\DAC AMP\DAC AMP\Analog Power Supplies.SchDoc		

Title **Digital Power Supply**

Size: A

Number:00002

Revision:3

Date: 8/21/2017

Time: 8:19:10 PM

Sheet 13 of 13

File: H:\DAC_AMP\DAC_AMP\Digital Power Supply.SchDoc