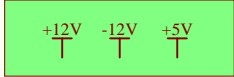
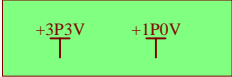


Power Supply

Sheet12
Analog Power Supplies.SchDoc



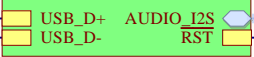
Sheet13
Digital Power Supply.SchDoc



Sheet2
USB_CONN.SchDoc



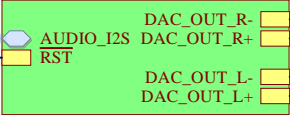
Sheet3
USB to I2S.SchDoc



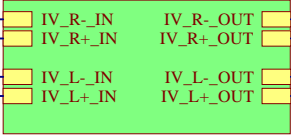
Sheet11
Reset.SchDoc



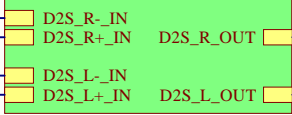
Sheet4
DAC.SchDoc



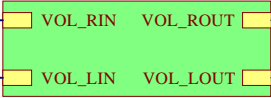
Sheet5
IV STAGE.SchDoc



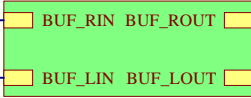
Sheet6
Differential to Single Ended.SchDoc



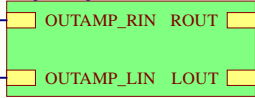
Sheet7
VolumeCTRL.SchDoc



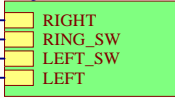
Sheet8
Buffer.SchDoc



Sheet9
Output Amp.SchDoc



Sheet10
Output_Con.SchDoc



Title **USB Headphone DAC/AMP**

Size: A

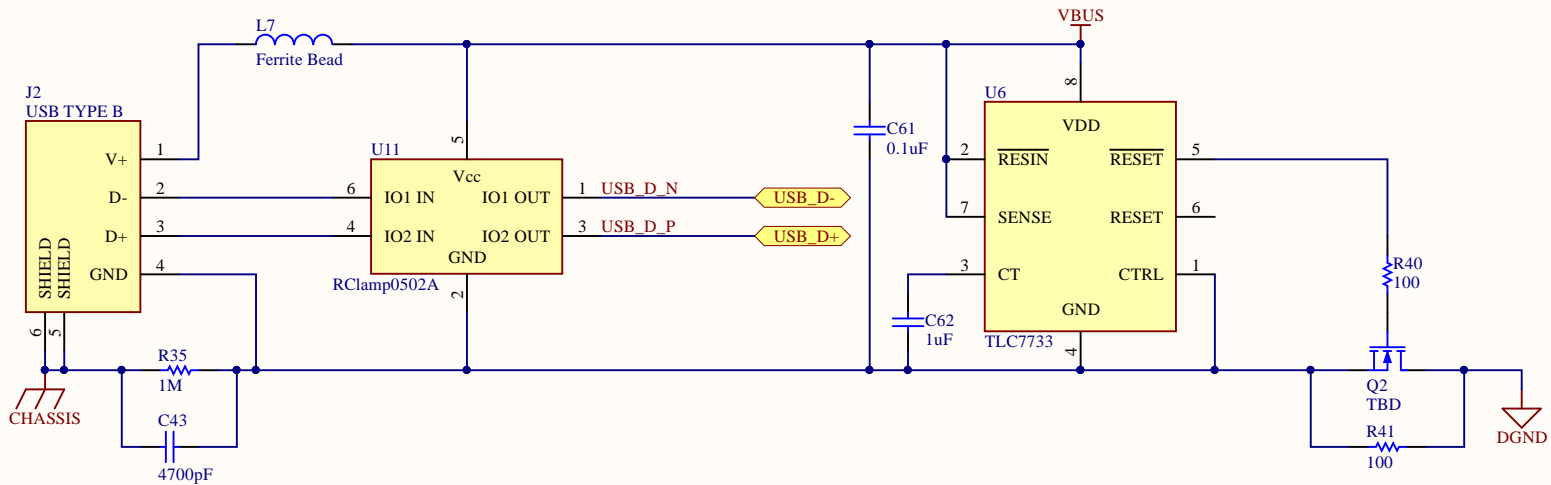
Number:00002

Revision:1

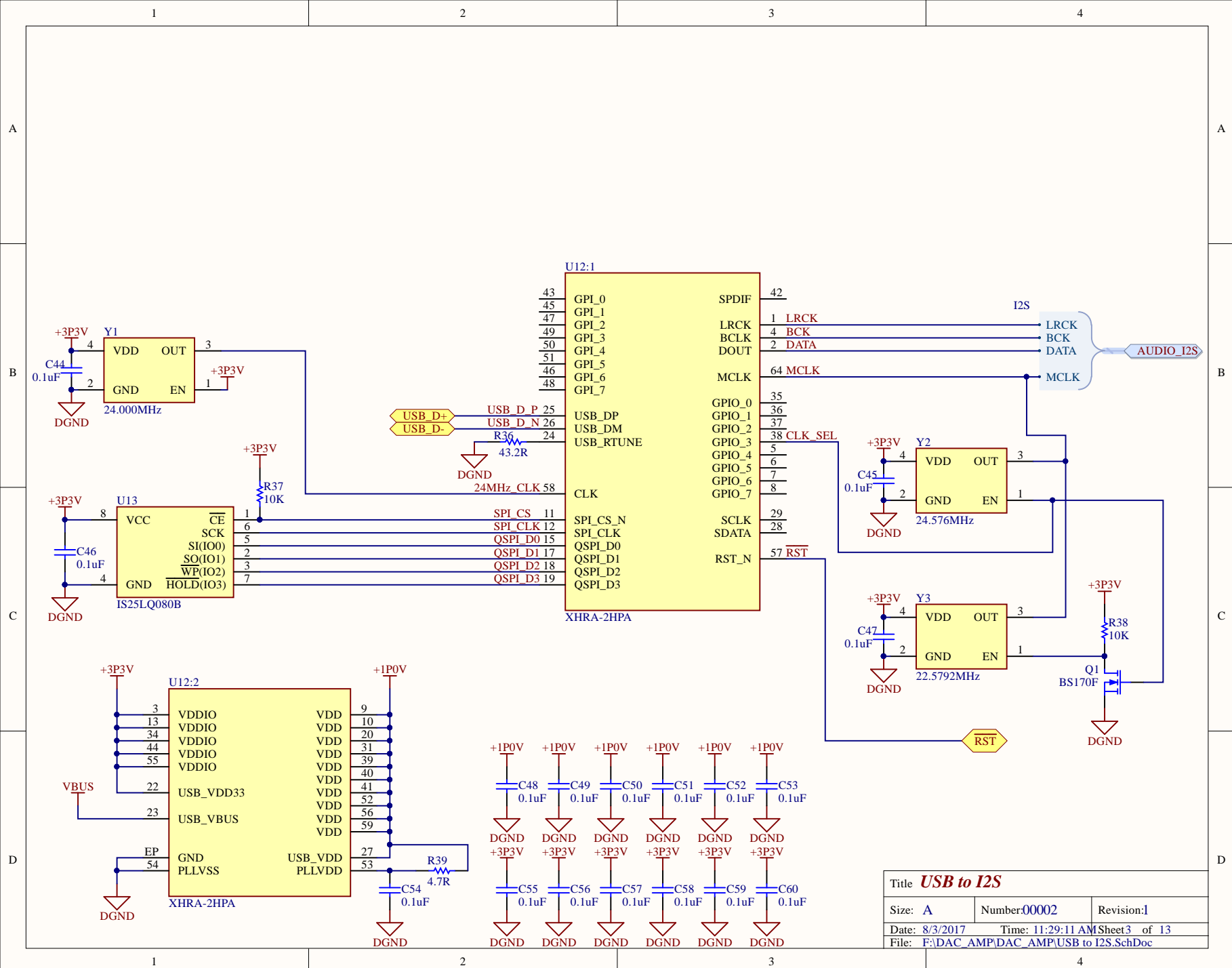
Date: 8/3/2017

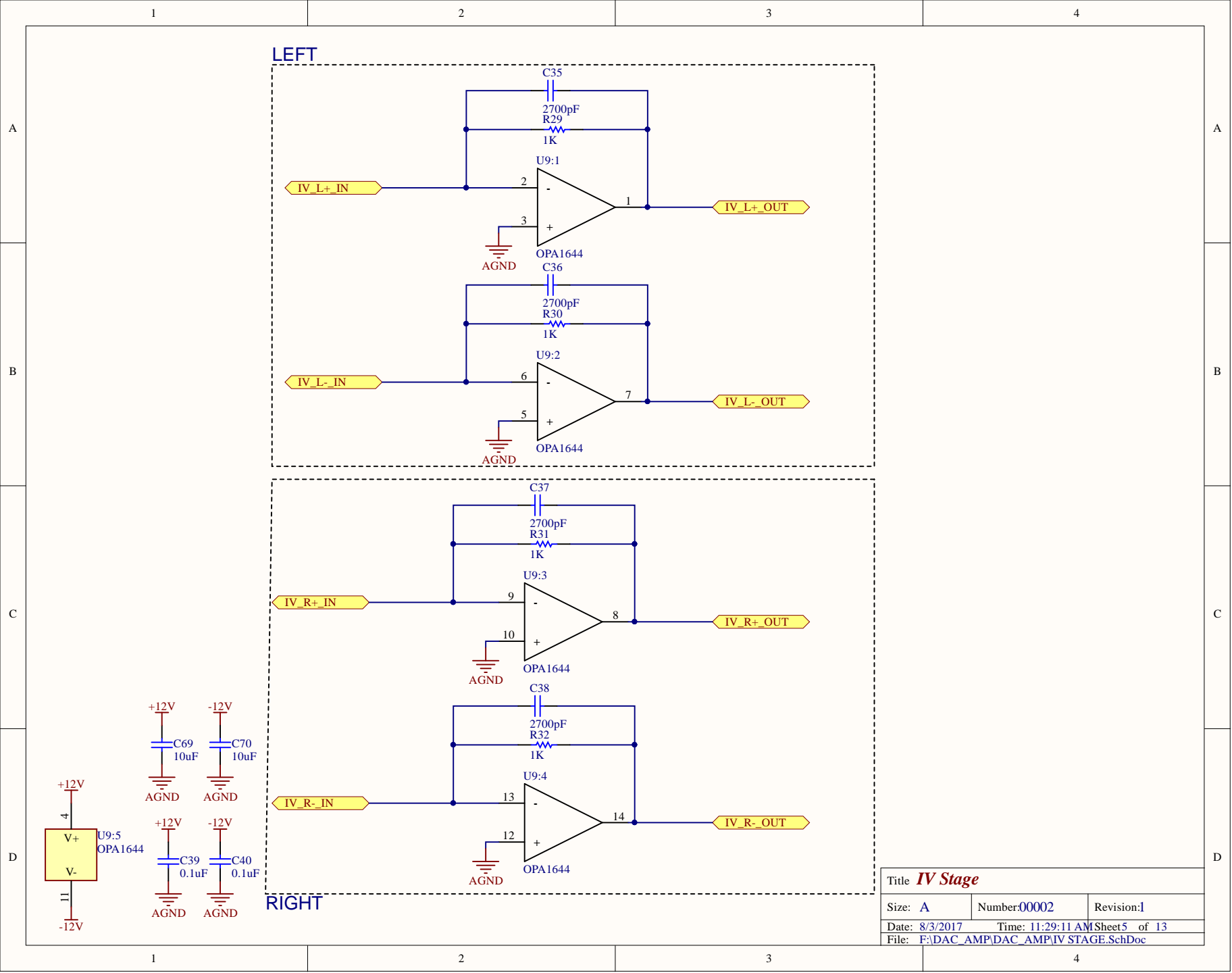
Time: 11:29:10 AM Sheet 1 of 13

File: F:\DAC AMP\DAC AMP\TopAssy.SchDoc

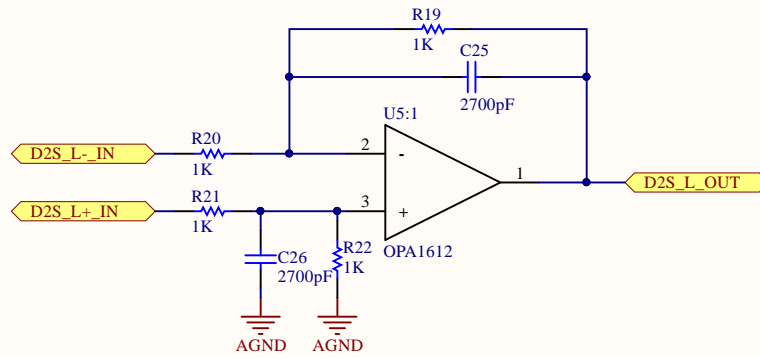


Title <i>USB Conenctor</i>		
Size: A	Number:00002	Revision:1
Date: 8/3/2017	Time: 11:29:10 AMSheet2 of 13	
File: F:\DAC AMP\DAC AMP\USB_CONN.SchDoc		

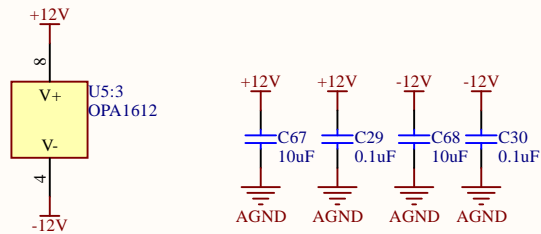
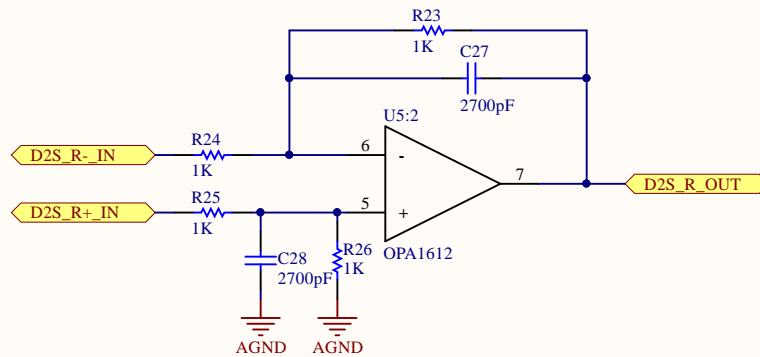




LEFT



RIGHT



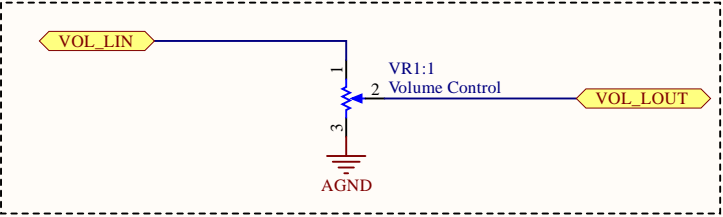
Title ***Differential to Single Ended***

Size: **A** Number: **00002** Revision: **1**

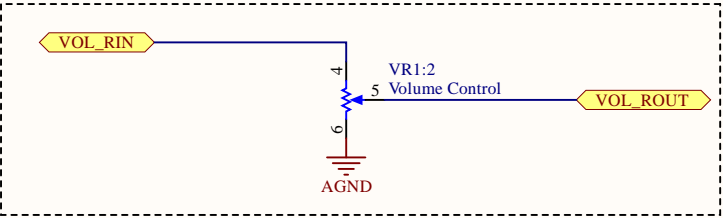
Date: **8/3/2017** Time: **11:29:11 AM** Sheet **6** of **13**

File: **F:\DAC AMP\DAC AMP\Diffrential to Single Ended.SchDoc**

LEFT

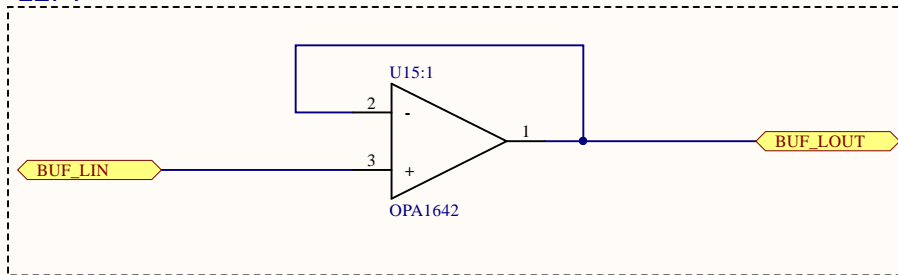


RIGHT

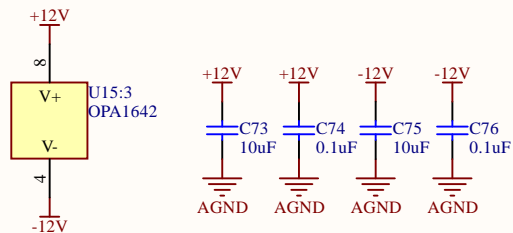
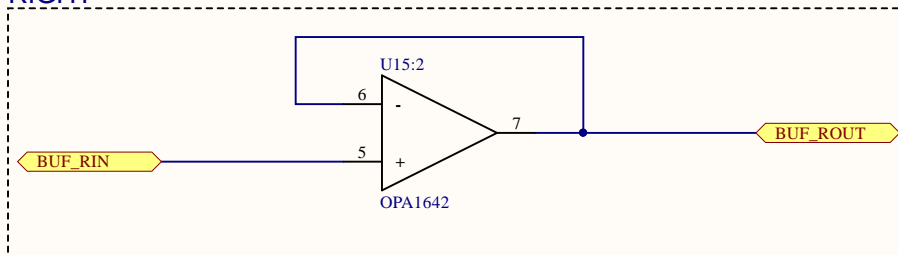


Title <i>Volume Control</i>		
Size: A	Number:00002	Revision:1
Date: 8/3/2017	Time: 11:29:11 AM Sheet 7 of 13	
File: F:\DAC_AMP\DAC_AMP\VolumeCTRL.SchDoc		

LEFT



RIGHT



Title Buffer Ouput Driver		
Size: A	Number:00002	Revision:1
Date: 8/3/2017	Time: 11:29:11 AM	Sheet8 of 13
File: F:\DAC_AMP\DAC_AMP\Buffer.SchDoc		

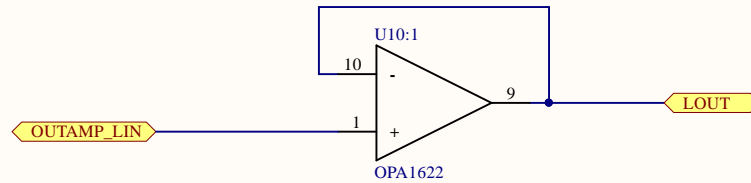
1

2

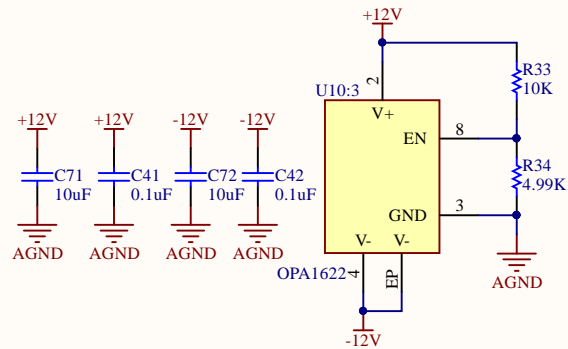
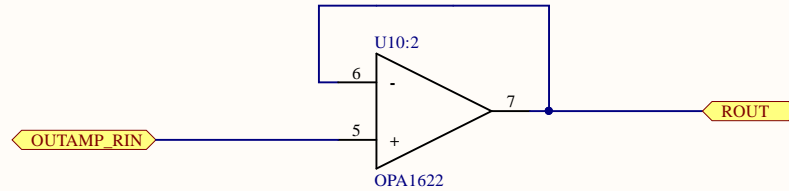
3

4

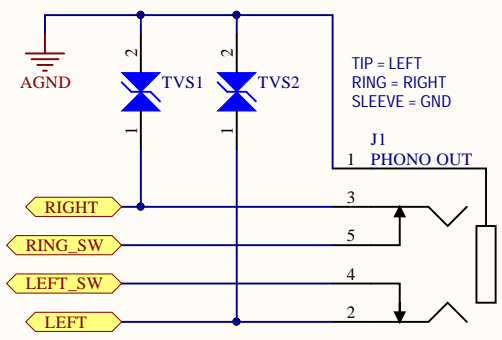
LEFT



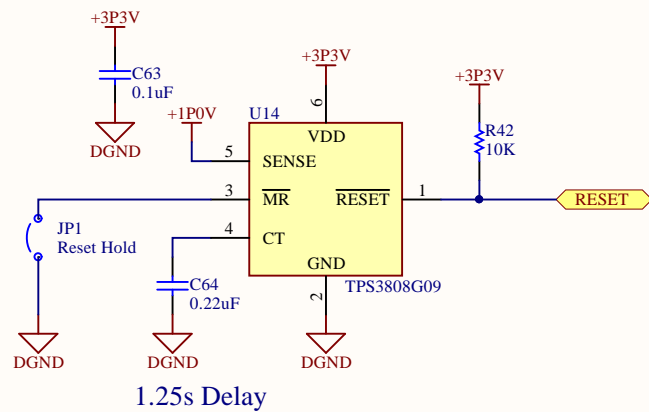
RIGHT



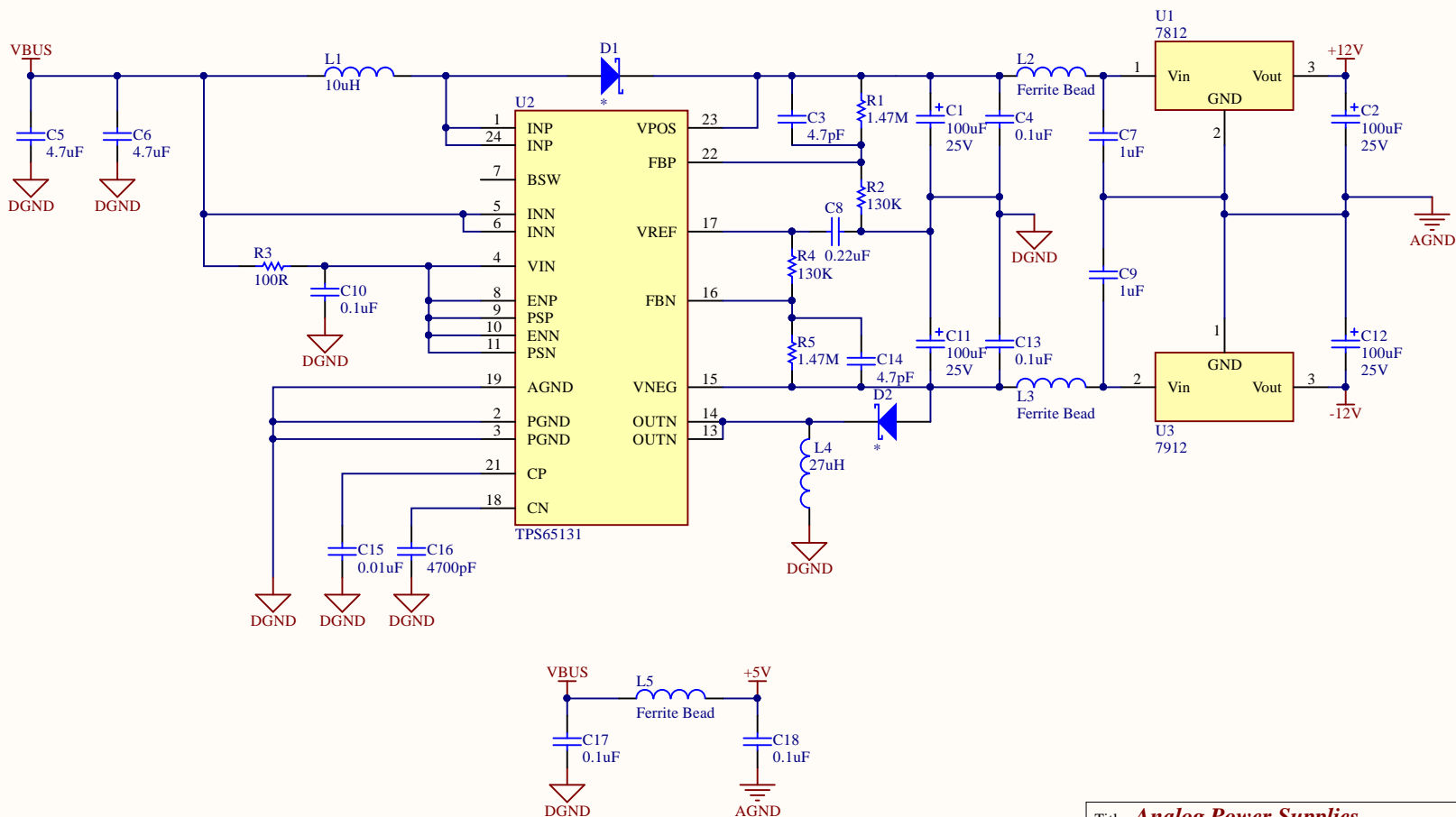
Title OPA1622 OUTPUT AMP		
Size: A	Number: 00002	Revision: 1
Date: 8/3/2017	Time: 11:29:11 AM	Sheet 9 of 13
File: F:\DAC AMP\DAC AMP\Output AMP.SchDoc		



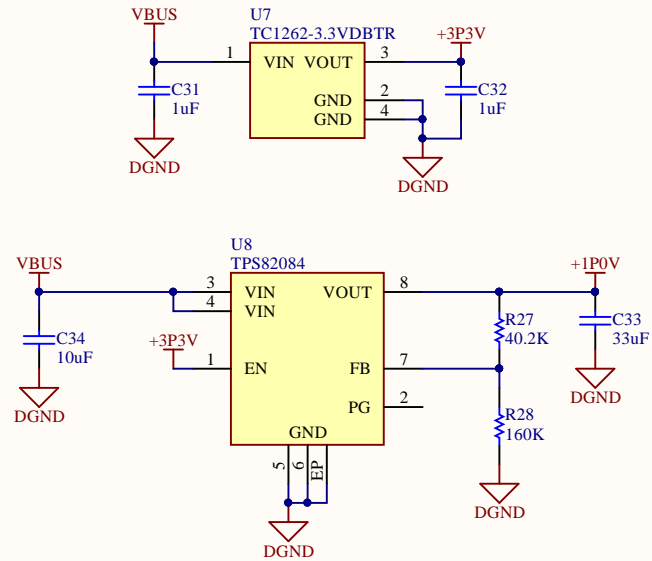
Title <i>Output Connector</i>		
Size: A	Number: 00002	Revision: 1
Date: 8/3/2017	Time: 11:29:11 AM Sheet 10 of 13	
File: F:\DAC_AMP\DAC_AMP\Output_Con.SchDoc		



Title <i>Reset Circuit</i>		
Size: A	Number:00002	Revision:1
Date: 8/3/2017	Time: 11:29:11 AM Sheet 11 of 13	
File: F:\DAC AMP\DAC AMP\Reset.SchDoc		



Title <i>Analog Power Supplies</i>		
Size: A	Number:00002	Revision:1
Date: 8/3/2017	Time: 11:29:11 AM Sheet 12 of 13	
File: F:\DAC AMP\DAC AMP\Analog Power Supplies.SchDoc		

Title ***Digital Power Supply***Size: **A**

Number:00002

Revision:1

Date: 8/3/2017

Time: 11:29:11 AM Sheet 13 of 13

File: F:\DAC AMP\DAC AMP\Digital Power Supply.SchDoc