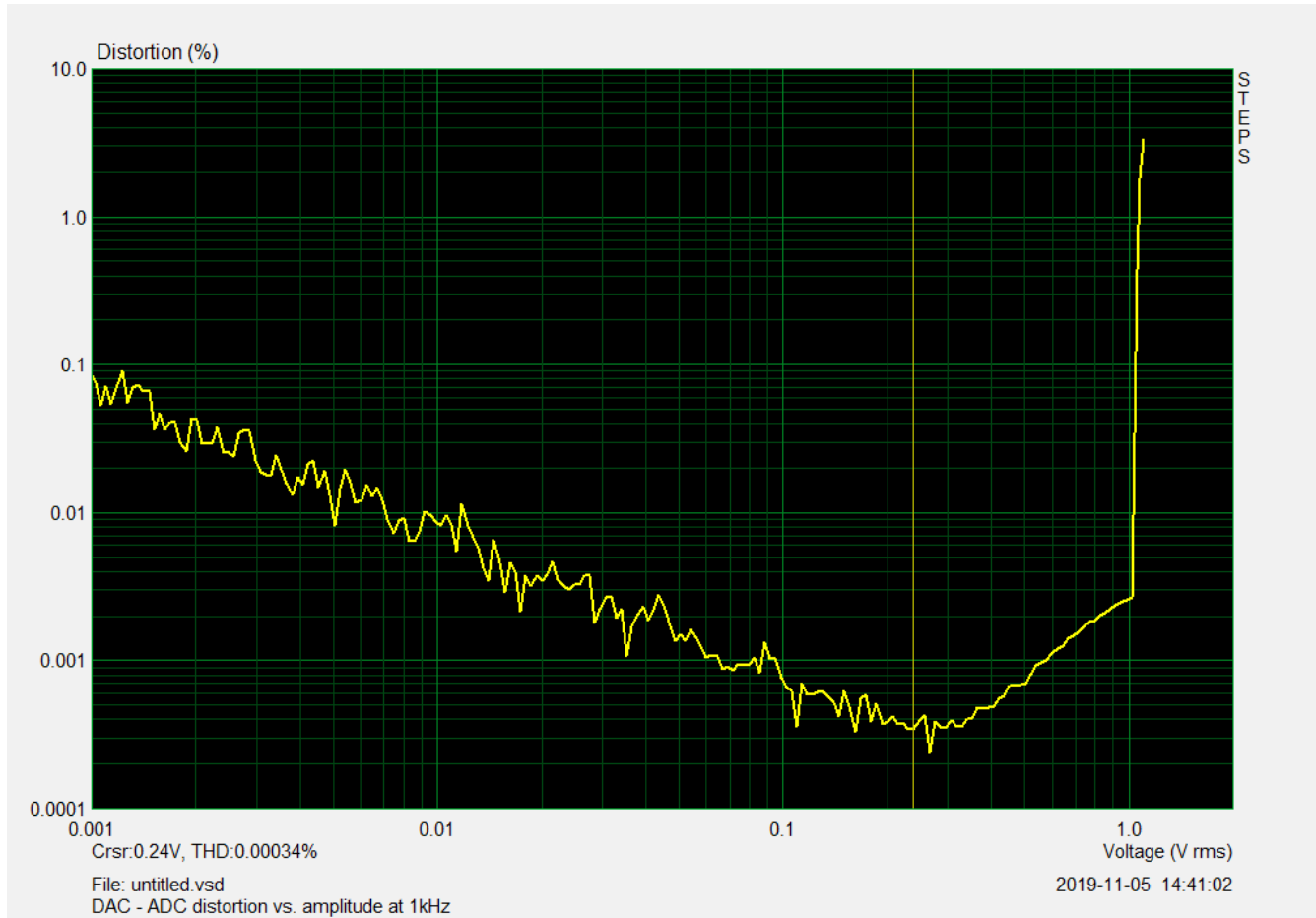
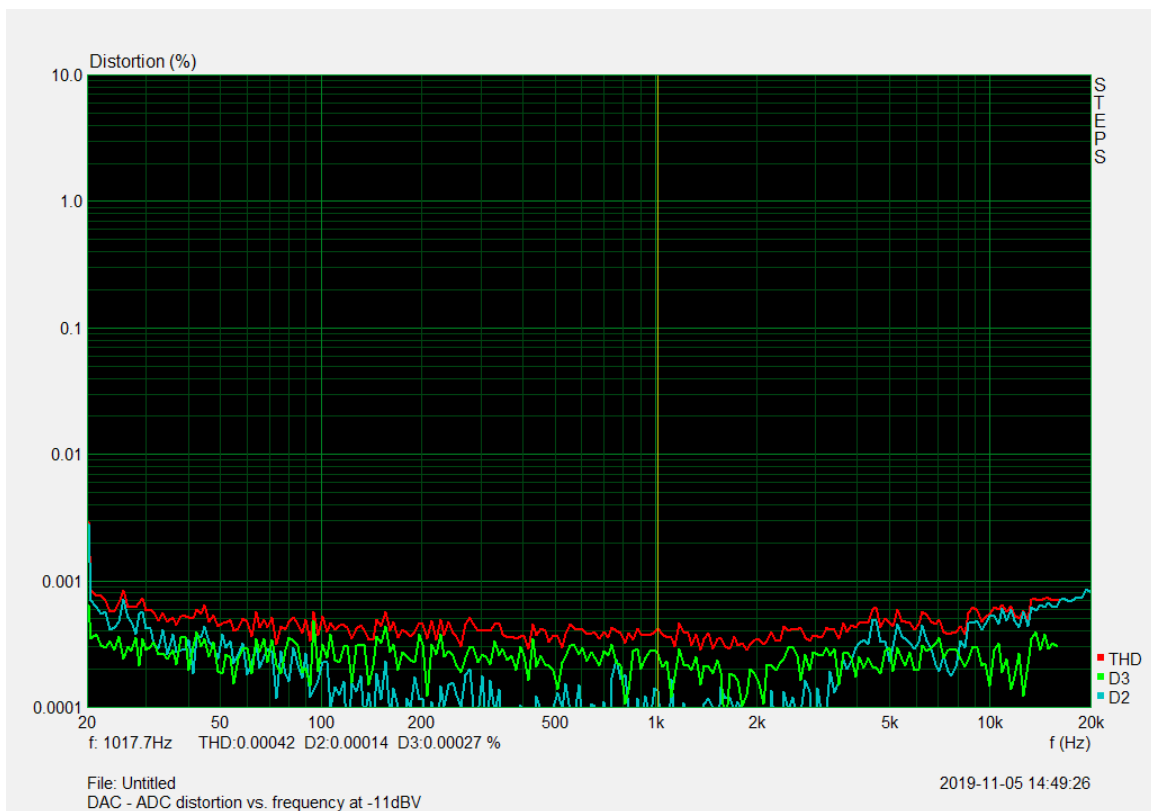


## DAC – ADC path (loop)

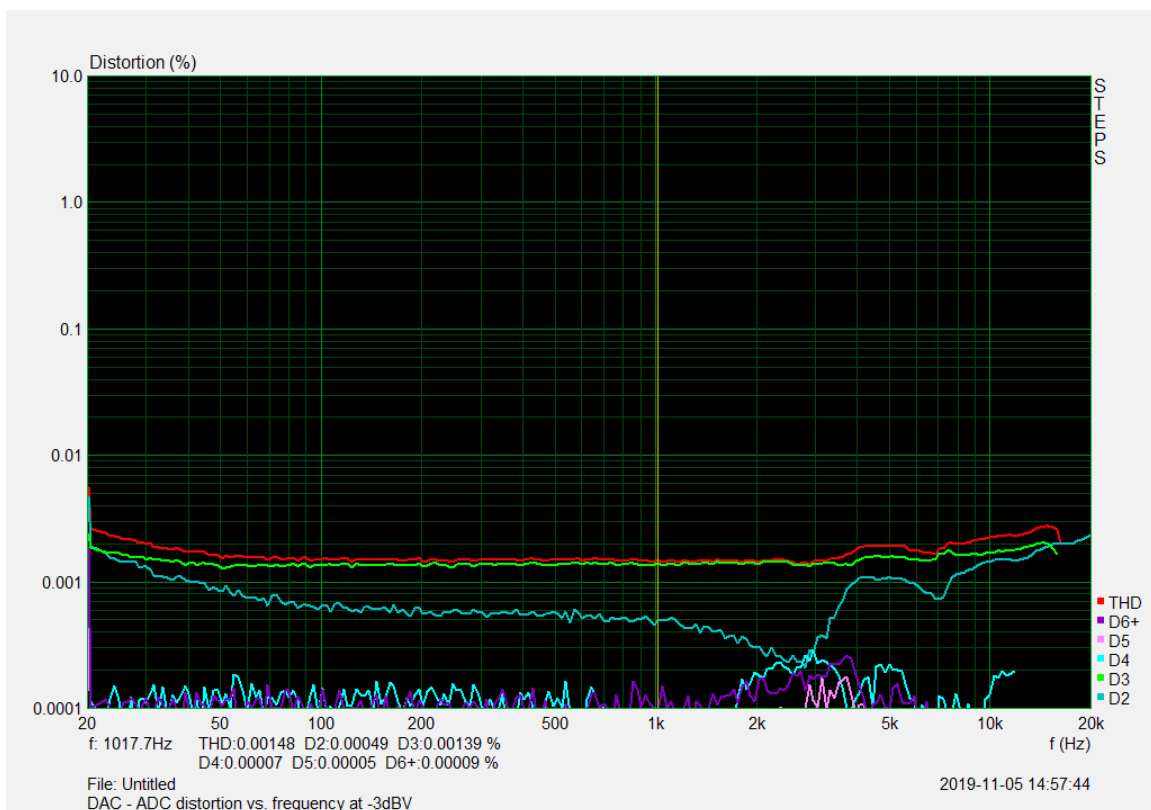
The samples were recorded at 96kHz/24bit format. The DAC-ADC loop has maximum ADC input level of 1Vrms (1dBV). Minimum of THD(1kHz) is 0.00034% at 0.24V – below this point distortion is buried in noise. At 1V, THD(1kHz) is 0.0026%. THD is quite flat with frequency, as will be seen from the posted plots. Both input and output are only single-ended, provided with RCA connectors.



*DAC – ADC loop THD vs. amplitude at 1kHz*



*DAC – ADC THD vs. frequency at 0.282V*



*DAC – ADC THD vs. frequency at 0.708V*