



Circuits

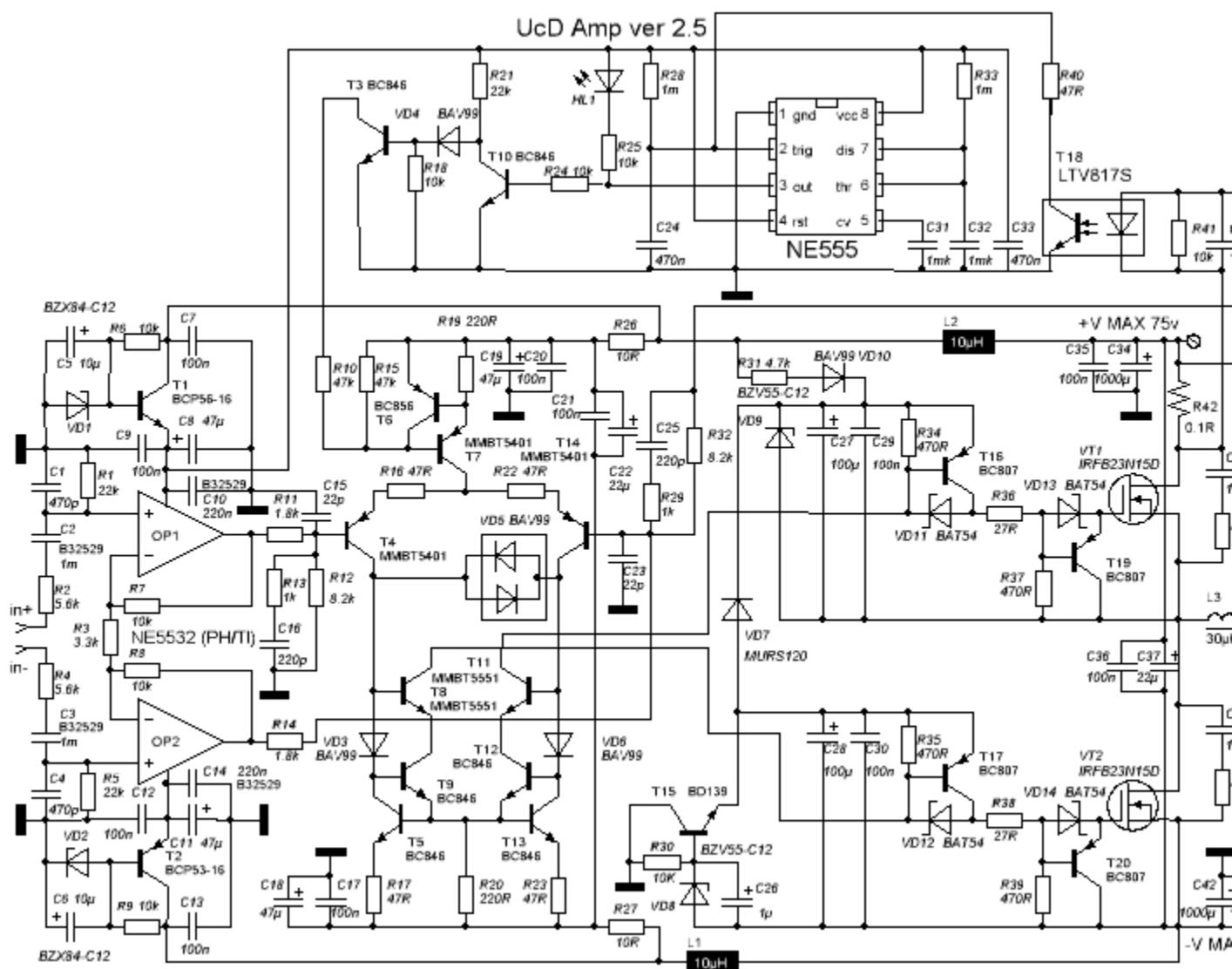
Near-final (verification) version 2.5 UcD amplifier



Completely as possible similar to [version 2.2](#). Just for safety, before being given charge of production, decided to collect vsetaki her house to make sure that no where did not allow mistakes in layout optimization. Optimization consists in the fact that I have increased the Heat Sink polygons operatsionnika stabilizers, as well as in the old version, they warmed themselves. In addition, it is possible to solder the radiator of copper wire bent zigzag, for the next contact,

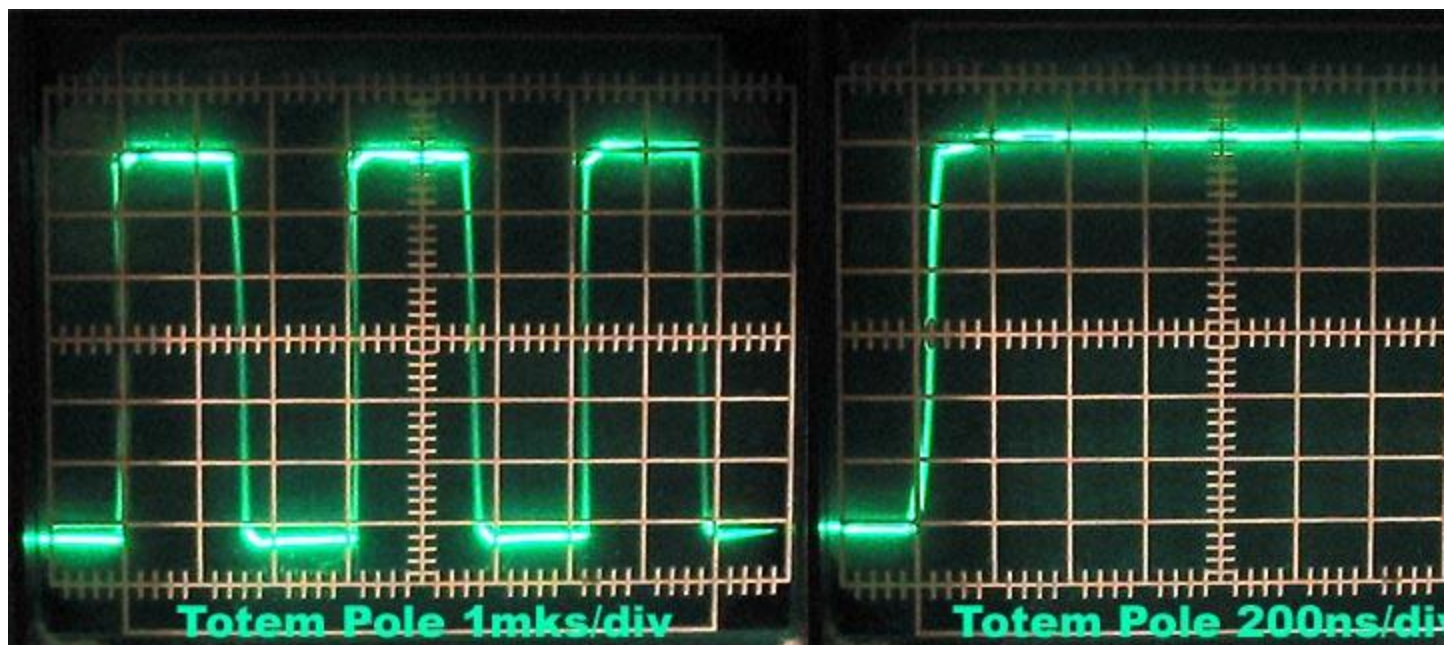
the collector is 2 Piglet. Also a little apart, electrolytes, so that when they inserted the installation is free.

Scheme with no significant changes looks like this:

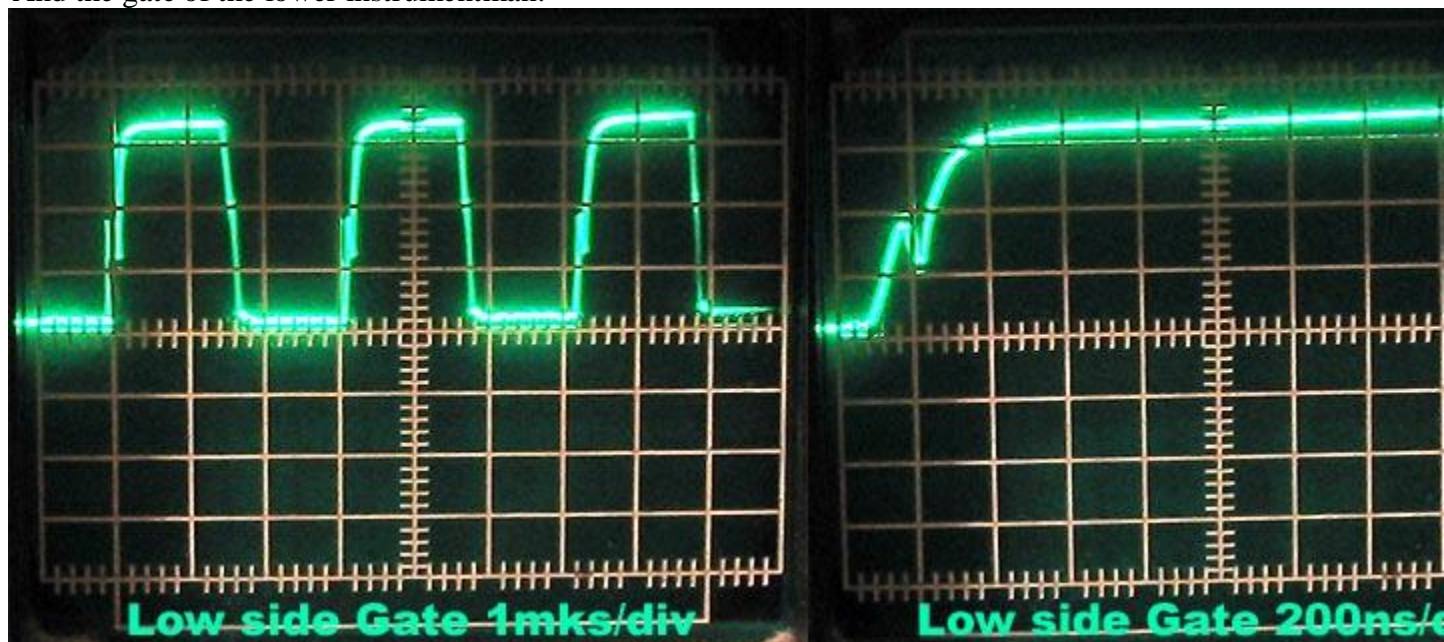


[Zoom](#)

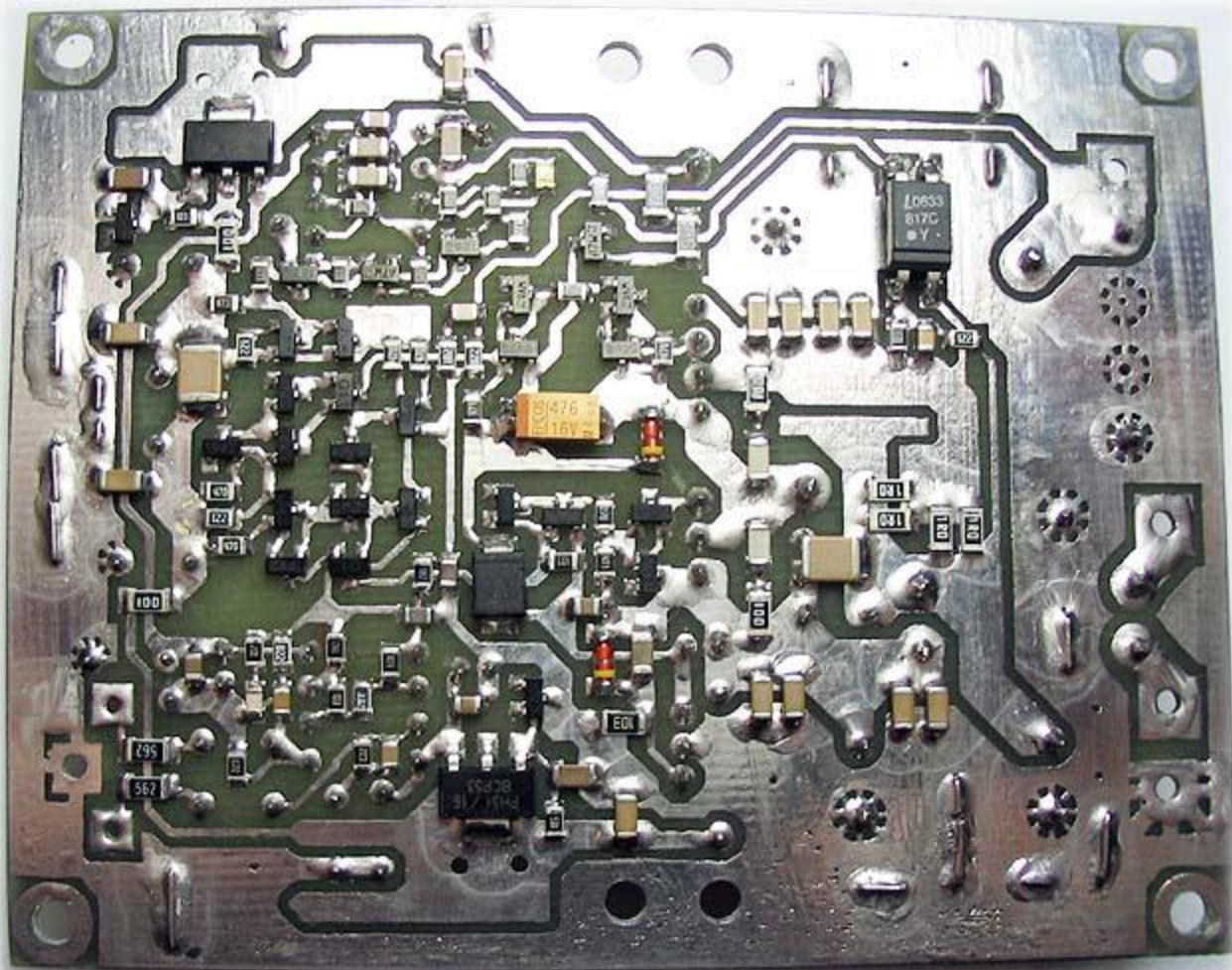
Some bypass capacitor which provides a place on the board, not shown. I also added capacitors to the input of the comparator on the ground, which if desired can be set. Below waveforms at a common point Polevikov.



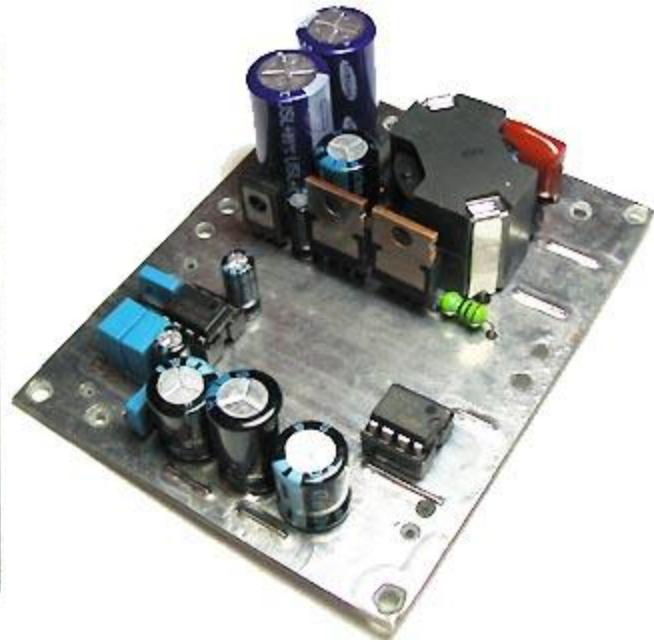
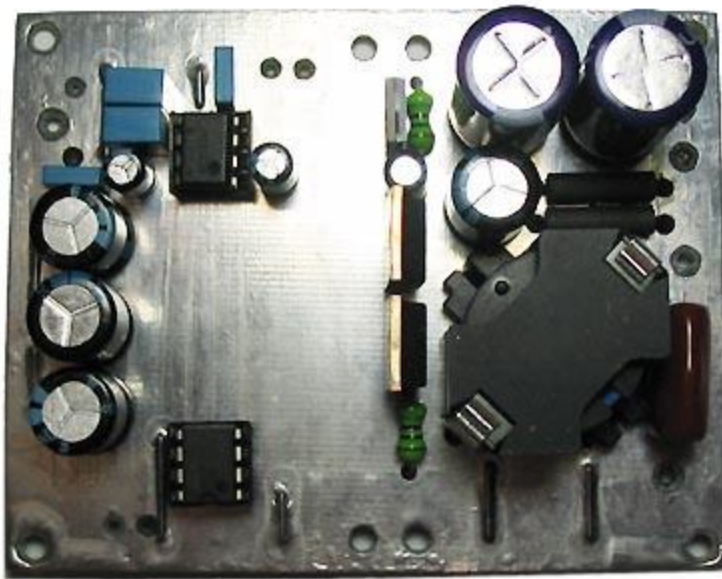
And the gate of the lower instrumentman.



PCB version 2.4 format LAY in [the archive](#). It is more convenient to make at home than 2.5, which I podredaktirovat specifically for production at the plant. The following picture card from mounting.







/ Sergey Kuznetsov (TDA) / 21.12.2006

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