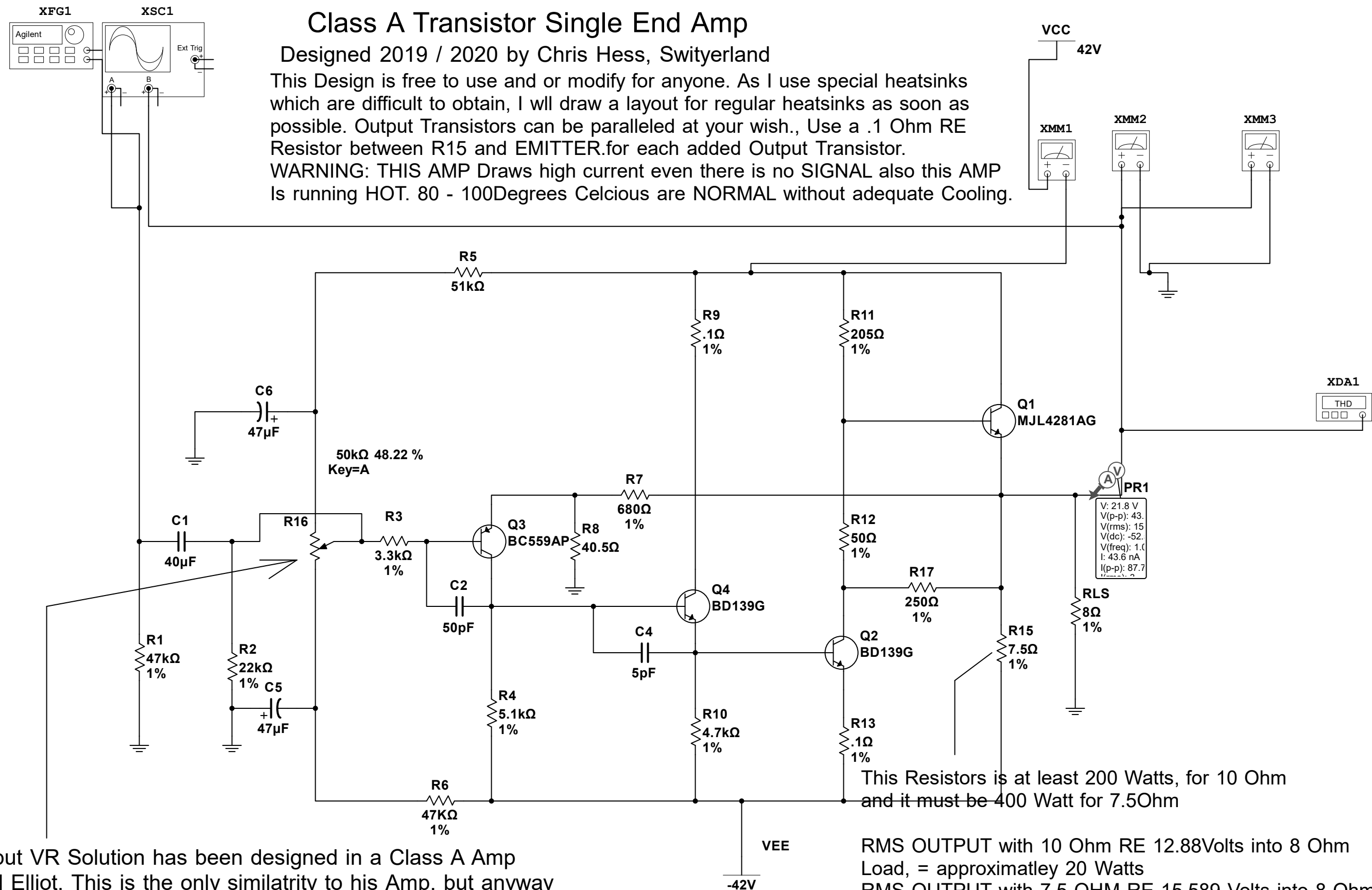


# Class A Transistor Single End Amp

Designed 2019 / 2020 by Chris Hess, Switzerland

This Design is free to use and or modify for anyone. As I use special heatsinks which are difficult to obtain, I will draw a layout for regular heatsinks as soon as possible. Output Transistors can be paralleled at your wish., Use a .1 Ohm RE Resistor between R15 and EMITTER.for each added Output Transistor.  
 WARNING: THIS AMP Draws high current even there is no SIGNAL also this AMP Is running HOT. 80 - 100Degrees Celcius are NORMAL without adequate Cooling.



The input VR Solution has been designed in a Class A Amp By Rod Elliot. This is the only similatrity to his Amp, but anyway I haven't seen this way of adjusting Output DC so Credit goes to ROD ELLIOT, Australia. The Rest of this Amp is a follow up of Testing and designing for a long time using a lot of time and funds as well.

RMS OUTPUT with 10 Ohm RE 12.88Volts into 8 Ohm Load, = approximatley 20 Watts  
 RMS OUTPUT with 7.5 OHM RE 15.589 Volts into 8 Ohm Load, = approximately 30 Watt. Distortion less than 0.2%

Frequency Response 10-100 Khz +- 0.2 db @1 Watt 0.024% Distortion  
 10-100khz +- 1db @ full load before Clipping 0.9% Distortion