

	TDA7292	TDA7293	TDA7294	TDA7295	TDA7296	TDA7265	TDA7297	TDA7377	TDA7375V	TDA8947	LM1875
Technology Power FET		DMOS	DMOS	DMOS	DMOS			1998	2013		
Channels	2	2	2	2	2	2	2	4SE/2 BTL	4SE/2 BTL	4SE/2 BTL	mono
Voltage	35V +/-	50V +/-	40V +/-	22V +/-	35V +/-	25V +/-	18V +/-	18V +/-	18V +/-	26V +/-	
Current (peak)	3.5 (5) A	6,5 (10) A	6,5 (10) A	4,24 (6) A	3.5 (5) A	3.18 (4,5) A	1,41 (2) A	2,47 (3,5) A	2,47 (3,5) A	2,82 (4) A	2,82 (4) A
THD, 1kHz, 4R - %	x	x	0,01 @5W	0,01 @5W	0,01 @5W	0,7 @8R -15W	x	0,03 @4W	0,03 @4W	x	0,022@20W
THD, 1kHz, 5W - %	to 15khz 0,02 @1W	0,005	0,005	0,005	0,005	0,02 @1W	to 15khz 0,3 @ 3 W	x	x	0,05 @1W	x
SVR-Supply Voltage Rejection (dB)	75	75	75	75	75	60	56	50	50	65	52
Bridged Application	No	Yes	Yes	Yes	Yes	Yes	No	Yes	Yes	Yes	x
master /slave	No	YES	No	No	No	No	No	No	No	No	x

TDA7293

Figure 14. Distortion vs output power

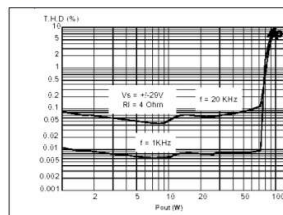
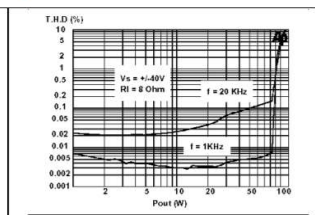
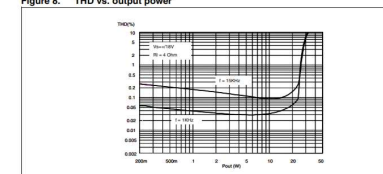


Figure 15. Distortion vs output power



TDA7292

Figure 8. THD vs. output power



TDA7265

Figure 5: Output Power vs. Supply Voltage

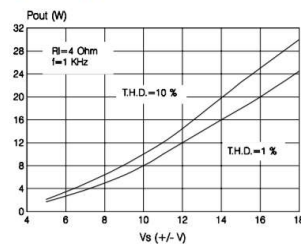
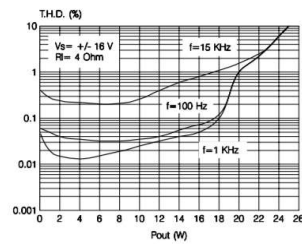


Figure 6: T.H.D. vs. Output Power



TDA7294

Figure 7: Distortion vs. Frequency

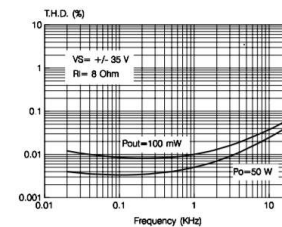


Figure 8: Distortion vs. Frequency

