

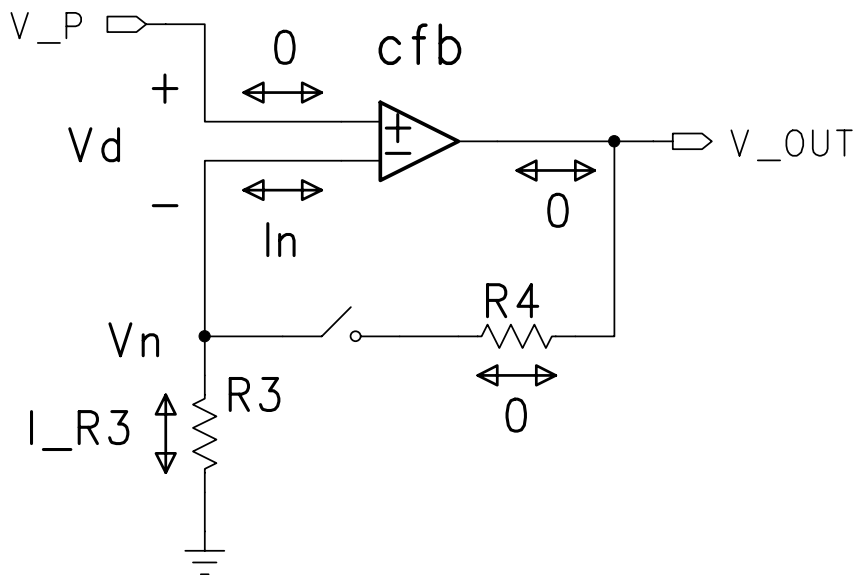
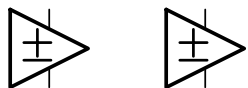
$V_P = \text{input}, V_n = 0, \rightarrow V_d = V_P$

$V_{OUT} = \text{positive rail}$

both resistors carry zero current

both op amp inputs source/sink

zero current



$V_P = \text{input}, V_n = V_P, \rightarrow V_d = 0$

$V_{OUT} = \text{positive rail}$

resistor R3 carries current  $I_{R3}$  where

$I_{R3} = V_P / R3$  which equals  $I_n$ , the

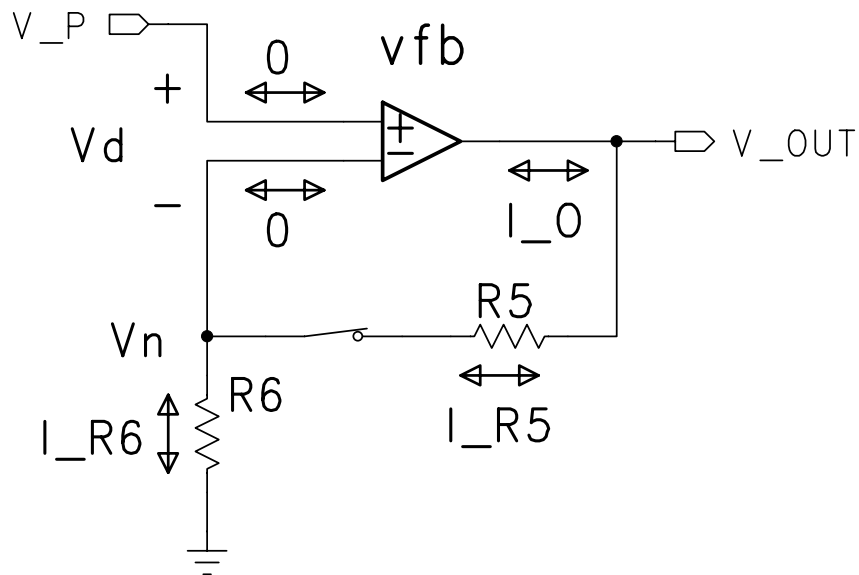
source/sink current provided by the

inverting input terminal which is an

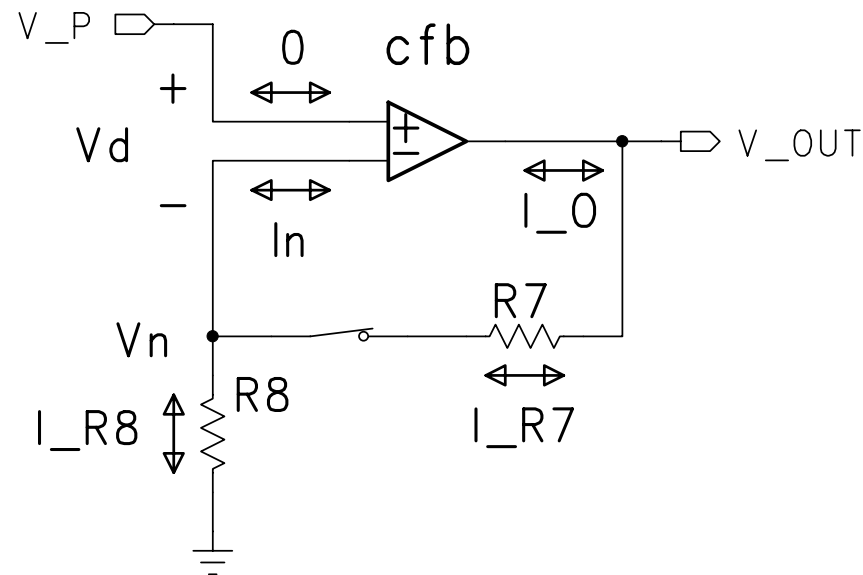
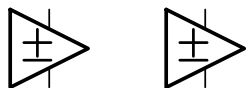
emitter follower output

DRAWN: C Abraham	DATED: 27Feb06	COMPANY: Claude Abraham	
CHECKED:	DATED:	TITLE: VFB/CFB Illustration	
QUALITY CONTROL:	DATED:	SIZE A	DWG NO:
RELEASED:	DATED:	SCALE: N/A SHEET: 1 OF 2	

REVISION RECORD					
DESCRIPTION	REV	EO NO:	APPROVED:	DATE:	



$V_P = \text{input}, V_n = V_P, \rightarrow V_d = 0$   
 $V_{OUT} = V_P * (1 + R5/R6)$   
 $I_0 = I_{R5} = I_{R6}$  (no ext. load)  
 both op amp inputs source/sink  
 zero current



$V_P = \text{input}, V_n = V_P, \rightarrow V_d = 0$   
 $V_{OUT} = V_P * (1 + R7/R8)$   
 $I_0 = I_{R7} = I_{R8}$  (no ext. load) &  
 $I_n = 0$ , since  $I_{R8} = I_{R7}$

DRAWN: C Abraham	DATED: 27Feb06	COMPANY: Claude Abraham
CHECKED:	DATED:	TITLE: VFB/CFB Illustration
QUALITY CONTROL:	DATED:	SIZE A
RELEASED:	DATED:	DWG NO:
		SCALE: N/A
		SHEET: 2 OF 2

REVISION RECORD					
DESCRIPTION	REV	EO NO:	APPROVED:	DATE:	