

FIGURE 1: Unbalanced and asymmetrical differential input stage creates distortion due to common- to differential-mode conversion.

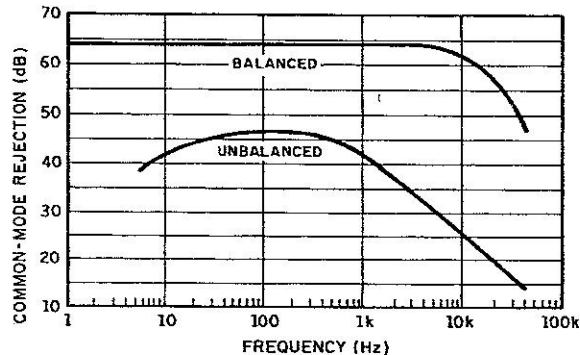


FIGURE 2: CMR improvements of a circuit topology.