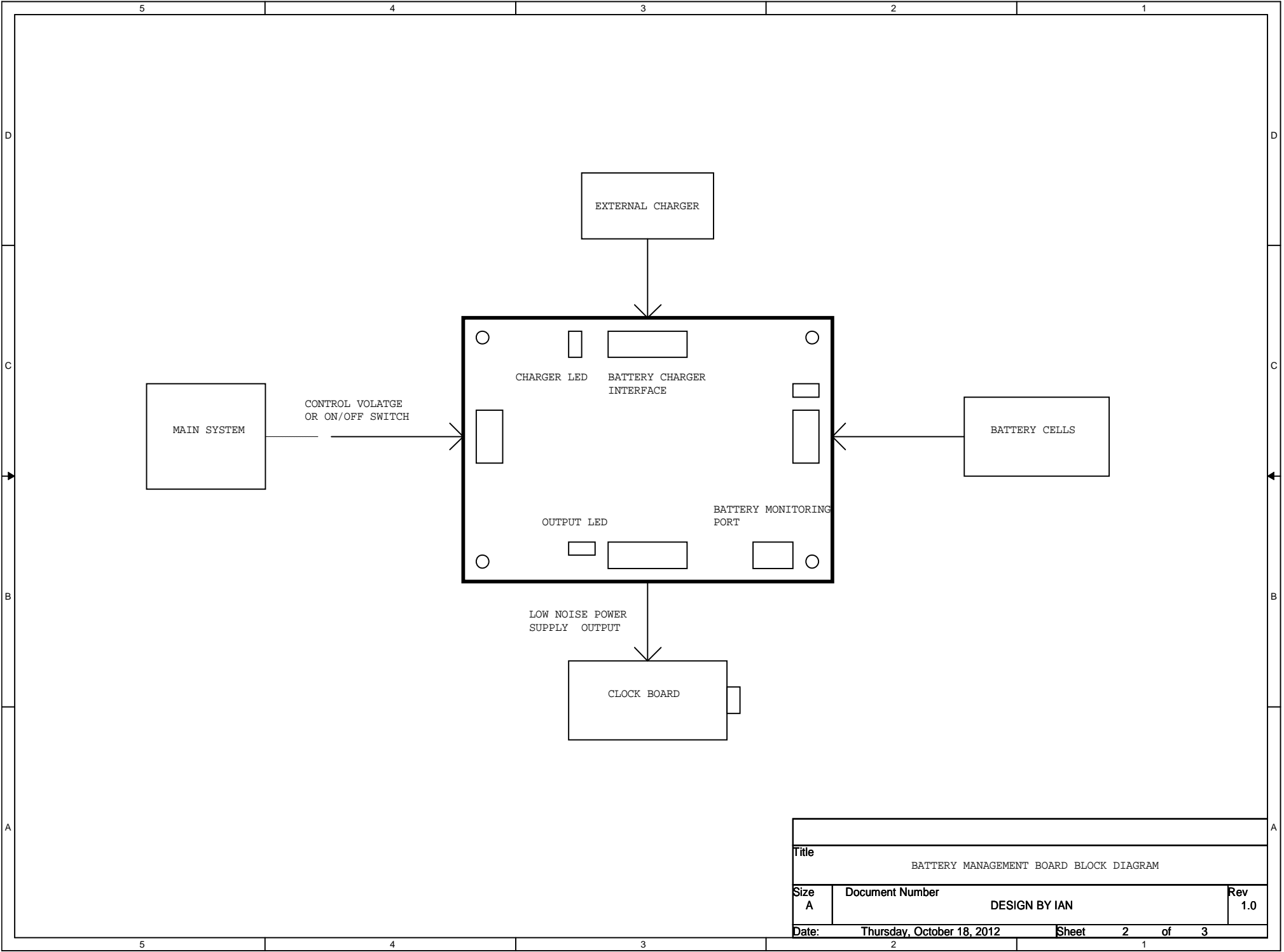


iancanada.mail@gmail.com			
Title BATTERY MANAGEMENT BOARD			
Size A	Document Number	Design by: Ian	Rev 1.0
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Title			
BATTERY MANAGEMENT BOARD BLOCK DIAGRAM			
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Standard configuration

J8: Control voltage input
J1 or J2: Charger input
J11 or J12: Battery input
J3 and J5 or J7: Output
K1 or K2 or K3: Relay
D4: IN4148
F1: MICROSMD110F-2 or just short
R1, R8 and R10: 0 ohm 0805 or just short
R7: 1K 0603
D3: LED 0603

Possible Part Numbers

Relays:

G2RL-2 DC5, Z3087-ND
TX2-5V, 255-1031-5-ND
G2R-2-DC5, G2R-2-DC5-ND
8-1393792-5, PB383-ND
Or function and pin to pin compatible

Connectors:

PH2.0mm 2P DIP, 455-1704-ND
1935161 TERM BLOCK PCB 2POS 5.0MM, 277-1667-ND
BERGSTIK II .100" SR STRAIGHT , 609-3500-ND

Other components:

FUSE: MICROSMD110F-2
RES 0.0 OHM 1/8W 0805 SMD, 541-0.0ACT-ND
LED CHIPLED 587NM YLW 0603 SMD, 475-2558-1-ND
RES 1.0K OHM 1/10W 5% 0603 SMD, 311-1.0KGRCT-ND

Optional functions:

Power saving: R8, R11, C1
Charging current limitation: F1
Reverse current protection: place diode on R1 or R2
Charger indicator: R3, R4, D2, J4
TVS Protection: D1
Cascade control: J9
Battery monitor: J10

Title			
BATTERY MANAGEMENT BOARD APPLICATION NOTE			
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