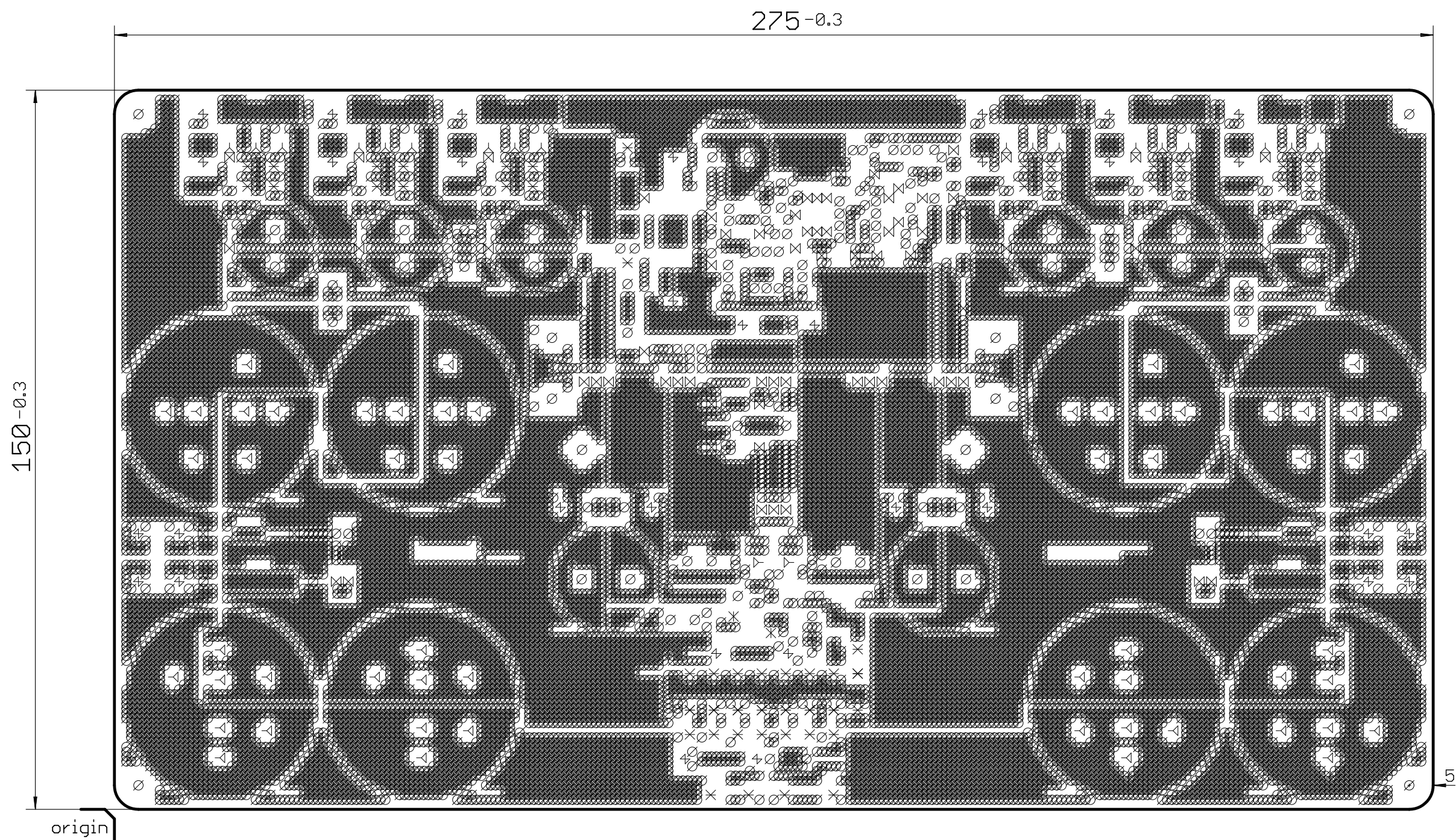


View on top side



Drill Legend:

Sym	Nº	Mils	MM	Qty	Plated
+	1	12	0.30	25879	YES
×	2	32	0.81	32	YES
▣	3	36	0.91	16	YES
◊	4	39	1.00	74	YES
⊠	5	40	1.02	34	YES
⊞	6	44	1.12	6	YES
⦶	7	47	1.20	28	YES
⦿	8	51	1.30	2	YES
⊗	9	55	1.40	6	YES
⊠	10	55	1.40	62	YES
▽	11	63	1.60	18	YES
▲	12	79	2.00	56	YES
◀	13	79	2.01	4	YES
▶	14	87	2.20	2	YES
⊙	15	134	3.40	6	NOT
●	16	165	4.20	4	YES

Layer Stack:

SIG-TOP	70µm Cu
SIG-BOT	core FR4
	70µm Cu

Layer thickness = 2.0 +/-0.2 mm

REV	IND	DATE	CHANGE HISTORY	ISSUE	DIY Community	2017 ©
				DRAWN	TITLEPrinted Circuit Board	REV
				JPS 30/3/20	Keantokens Aurum-X Amplifier Mainboard	001
				CHECKED	DATE	DRG Nº
				DATE	08.04.2020 01:40	
001		08.04.2020	Prototyping		FILE: Aurum-X-MB_VERS7x_001a	PAGE: