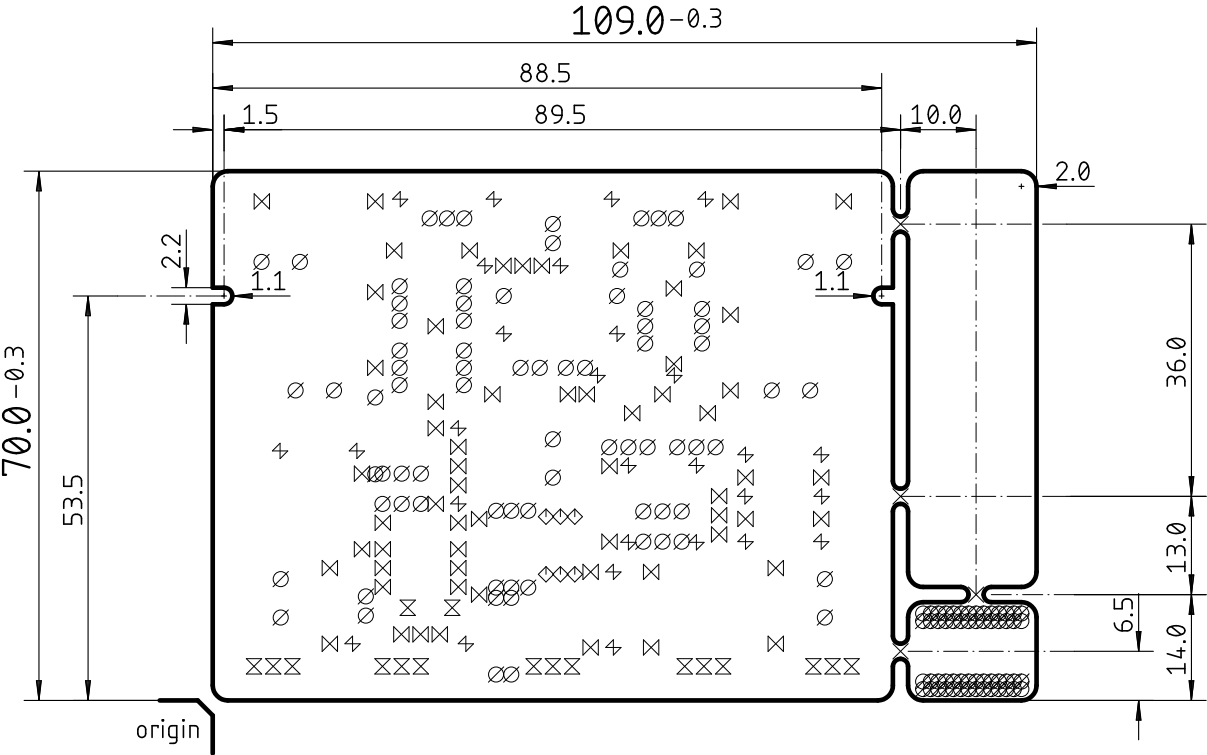


View on top side



Drill Legend:

Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	56	YES
x	2	24	0.60	4	NOT
▣	3	31	0.80	6	YES
◊	4	32	0.81	38	YES
⊗	5	35	0.90	17	YES
⊞	6	39	1.00	62	YES
+	7	40	1.02	40	YES
+	8	44	1.12	2	YES
*	9	47	1.20	28	YES

Layer Stack:

SIG-TOP		70µm Cu
SIG-BOT		70µm Cu

Layer thickness = 1.6 +/-0.2 mm

REV	IND	DATE	CHANGE HISTORY	ISSUE	DIY Community	THIS DRAWING CONTAINS PROPRIETARY INFORMATION AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION	2017 ©	REV
				DRAWN	TITLE Printed Circuit Board			001
				JPS 31/3/20	Aurum-X Front-End			
				CHECKED	DATE 11.06.2020 10:57			DRG N°
002		11.06.2020	Second prototype	DATE	FILE: Aurum-X_THT_FE_003			PAGE:
001		08.04.2020	Prototyping					