

NOTE: UNLESS OTHERWISE INDICATED, ALL DIODES ARE 1N4148, ALL ICS ARE 2043, ALL RESISTORS ARE 5%, ALL CAPACITOR VALUES IN MICROFARADS.

[illegible][illegible]

The schematic shows the power supply circuit starting from an AC cord (120/240 VAC, 50-60Hz) connected to a transformer (L1). The secondary has taps for 120V (R107), 240V (R108), and 44VAC/CT. A switch (SW8) selects between BL (line) and GR (ground) connections. The primary is connected to R36 (2K) and R37 (BL). The secondary winding is connected to J21-1, J21-2, and J21-3. The center tap (J21-4) is grounded through D22 and D18. The bridge rectifier consists of diodes D19, D17, D16, and D20. Filter capacitors C15 (.1), C16 (1000 35V), C17 (1 50V), C18 (1 50V), and C19 (1000 35V) are used. The output is regulated by a 7818 IC (U1) providing +18V (RED) and -18V (-18V) rails, and a 7918 IC (U2) providing -30V. The +18V rail is filtered by C4, C9, C30, and C60. The -18V rail is filtered by C47, C70, and C49. The -30V rail is filtered by C52. The output is connected to J20-5.

PEAK DETECTOR CIRCUIT
FROM PEAK BUSS

C23, C37, C48
C53, C67, C72

R69 2.4K R70 390K
.1u 62K
p71

[illegible]

CHANNEL TWO LOW OUTPUT

TO PEAK BUSS

DCN	DATE	SCH REV	PCB REV	DESCRIPTION
X	10-93	A	10P2411	NEW PCB - ONE CARD FRONT TO BACK. ADDED OUTPUT MUTES, XLR AND 1/4" CONNECTORS, +/- 18V RAILS, RECESSED RANGE SWITCH, ONBOARD PWR SUPPLY 100 PC.
		B	0	
	3-94	A	1	MECH. FIXES, CHANGED MUTE CIRCUIT, BYPASSED POWER SEQUENCER - NETWORK
204	7-95	C	4	PHASE REVERSE ON CH.1 - CHANGED CH.1 OUTPUT LEVEL CIRCUIT - XFMR TO JACK
585	12-01	D	4	ADDED MONO LOW OUT TO XR1001 ONLY - CONNECT WIRE BETWEEN R9 AND R105

ASHLY AUDIO INC.
847 HOLT ROAD
WEBSTER, NY 14580-9103

SCHEMATIC DIAGRAM
XR1001/XR2001 ELECTRONIC CROSSOVER

REV. D

DATE: 12-07-01

DATE :

SHEET 1 OF 1